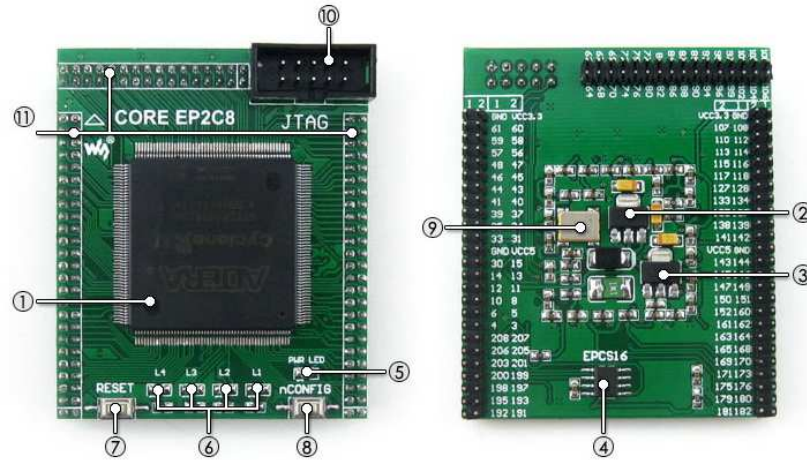


**CoreEP2C8****1. Hardware Description****1.1 What's On Board****1.2 Interfaces****1.3 Hardware Setup****2. Examples****2.1 Overview****2.2 Source Code****3. Schematic****3.1 Schematic Circuit Diagram****3.2 Tcl script****4. Download****4.1 Download to FPGA****4.2 Download to FLASH**

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**1.1 What's on the CoreEP2C8****1. EP2C8Q208C8N: the ALTERA Cyclone II FPGA device which features:**

- **Operating Frequency:** 50MHz
- **Operating Voltage:** 1.15V~3.465V
- **Package:** QFP208
- **I/Os:** 138
- **LEs:** 8K
- **RAM:** 165kb
- **PLLs:** 2
- **Debugging/Programming:** supports JTAG

**2. AMS1117-3.3 (on bottom side), 3.3V voltage regulator****3. AMS1117-1.2 (on bottom side), 1.2V voltage regulator****4. EPCS16 (on bottom side), onboard serial FLASH memory, for storing code****5. Power indicator****6. LEDs****7. Reset button****8. nCONFIG button: for re-configuring the FPGA chip, the equivalent of power resetting****9. 50M active crystal (on bottom side)****10. JTAG interface: for debugging/programming****11. FPGA pins expander, VCC, GND and all the I/O ports are accessible on expansion connectors for further expansion**

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