



# 福昕PDF编辑器

• 永久 • 轻巧 • 自由

升级会员

批量购买



**永久使用**

无限制使用次数



**极速轻巧**

超低资源占用，告别卡顿慢

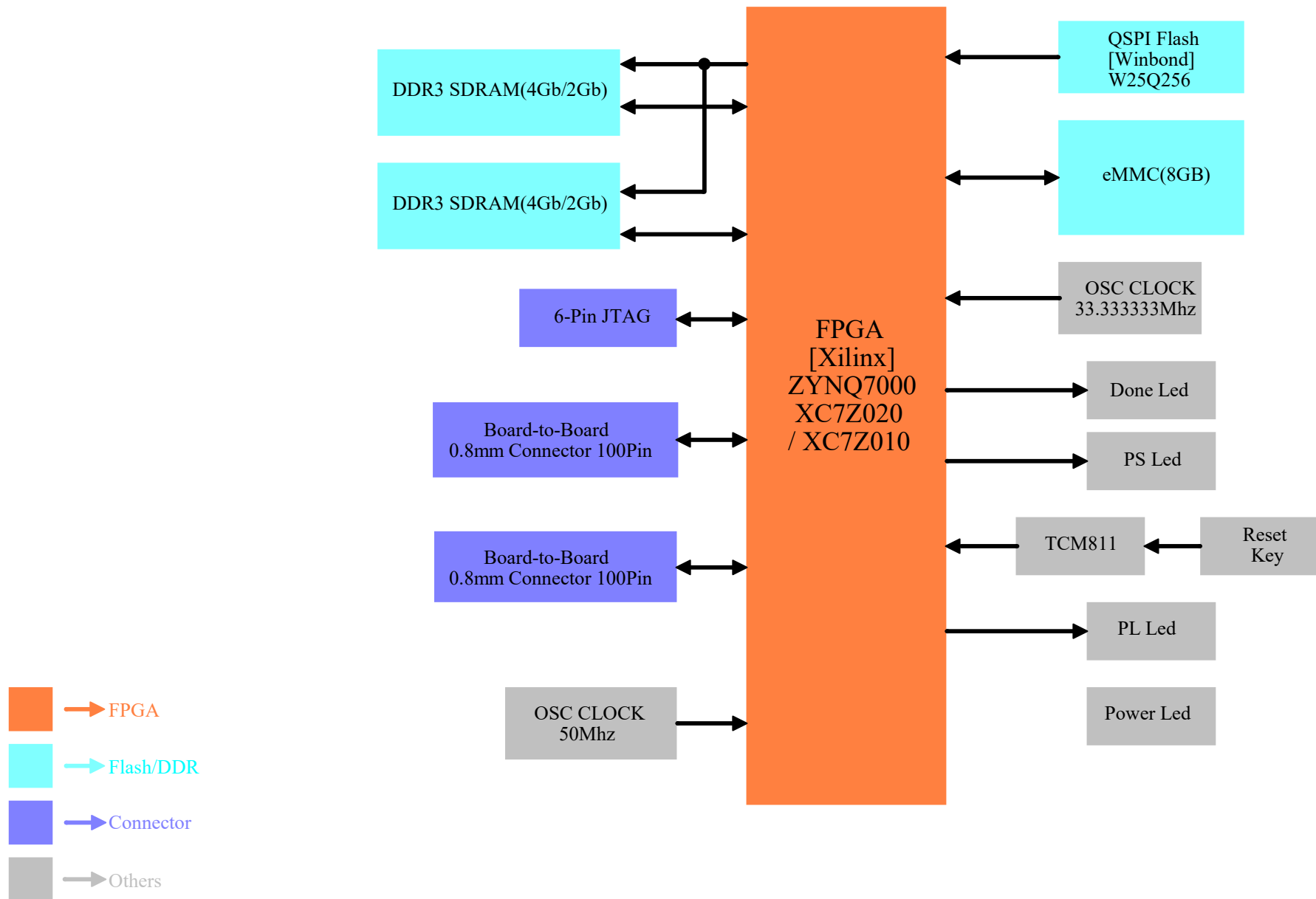


**自由编辑**

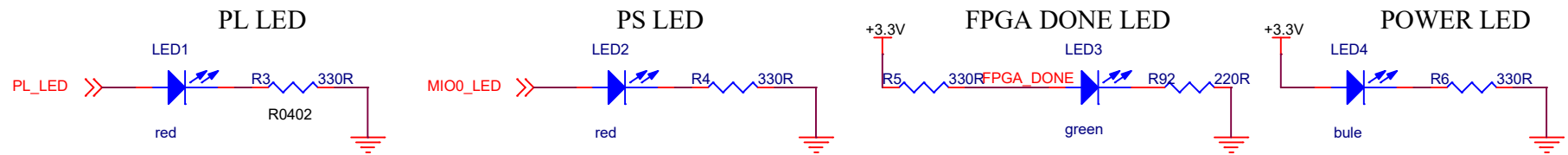
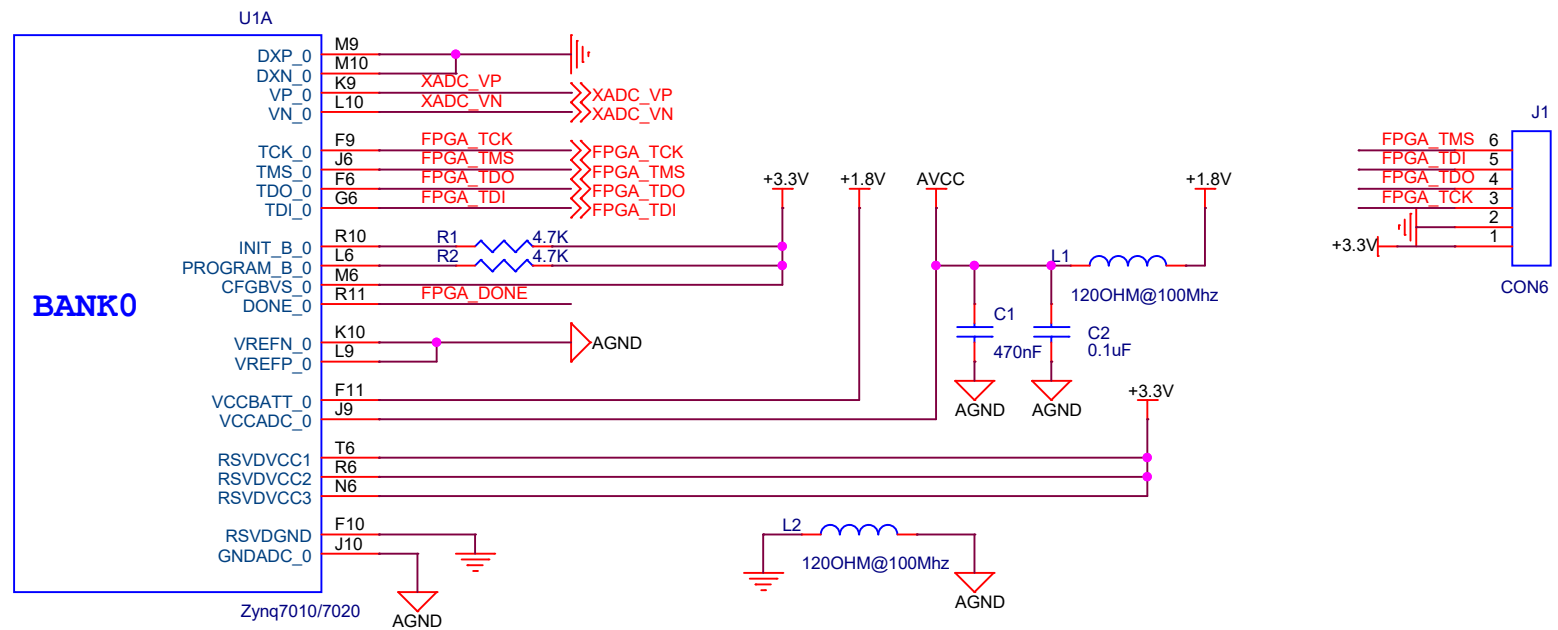
享受Word一样的编辑自由



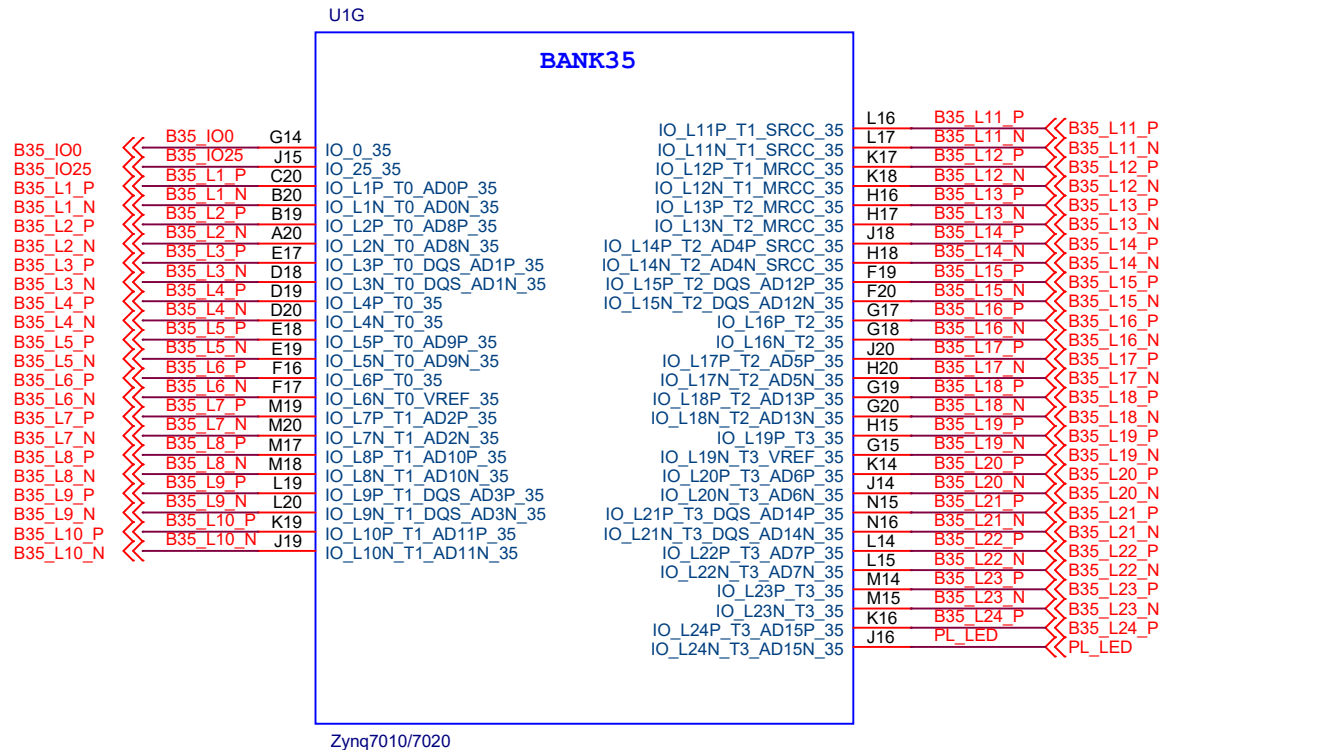
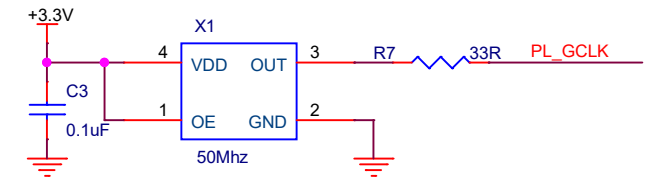
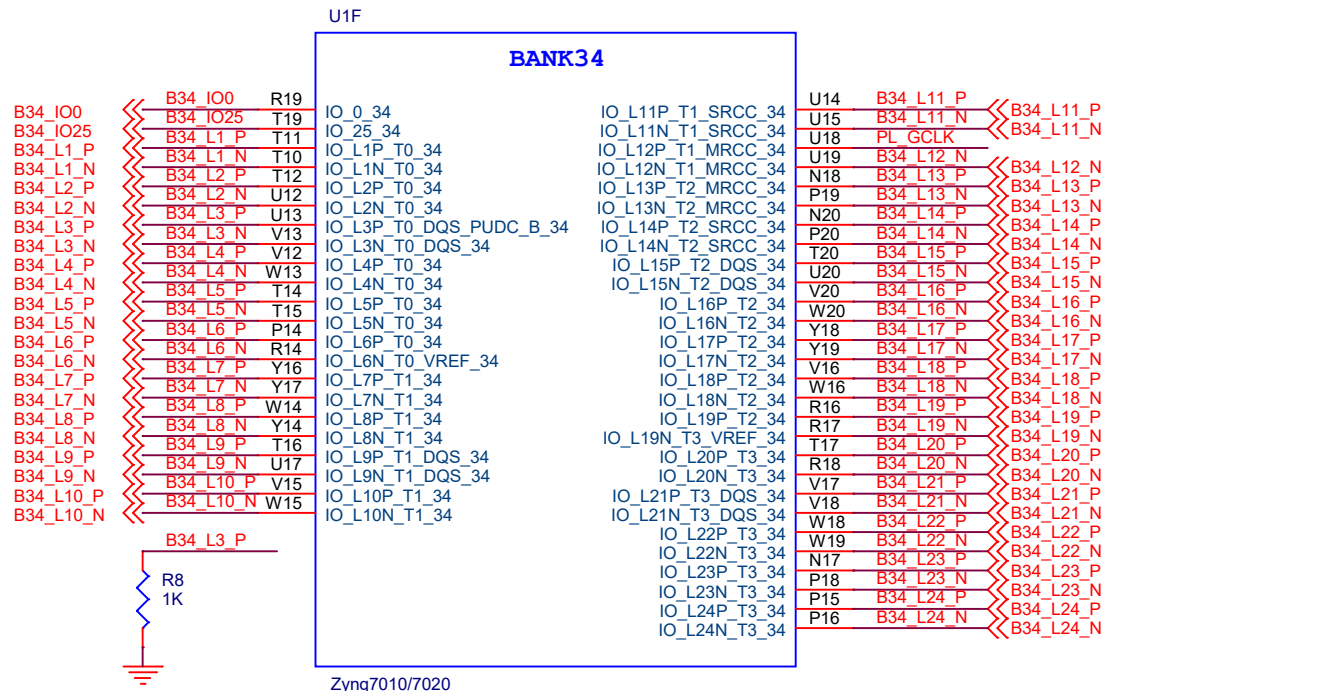
扫一扫，关注公众号



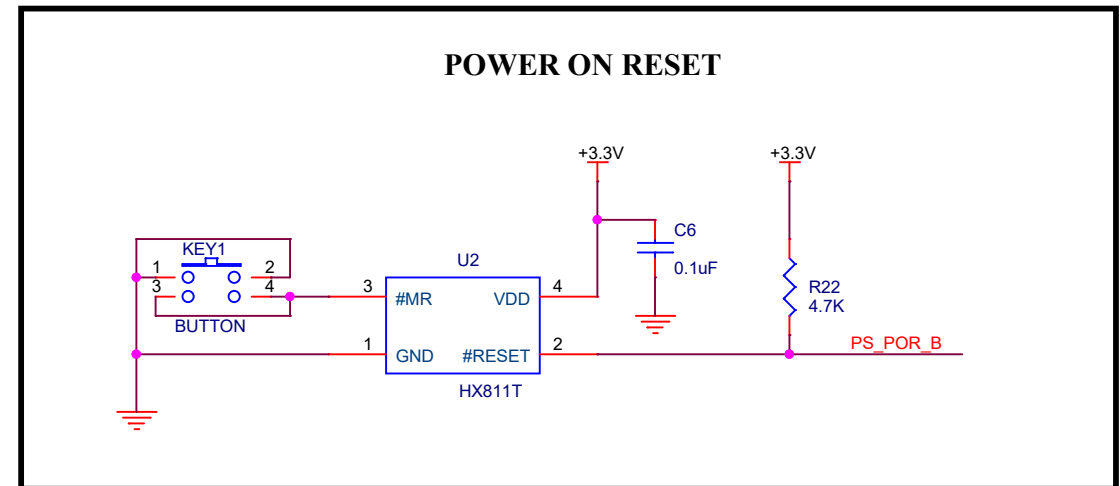
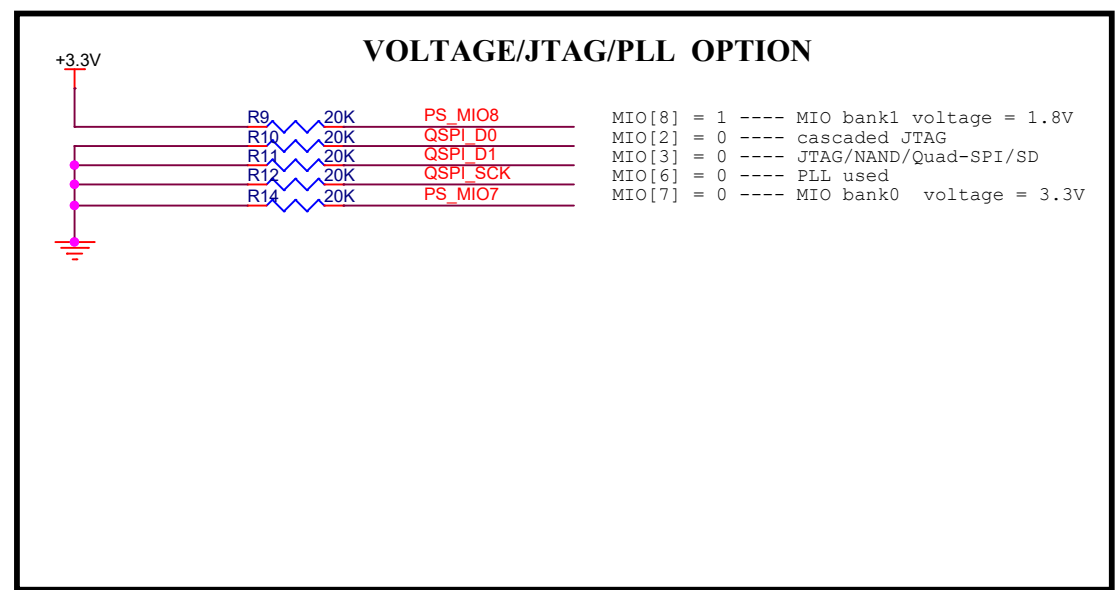
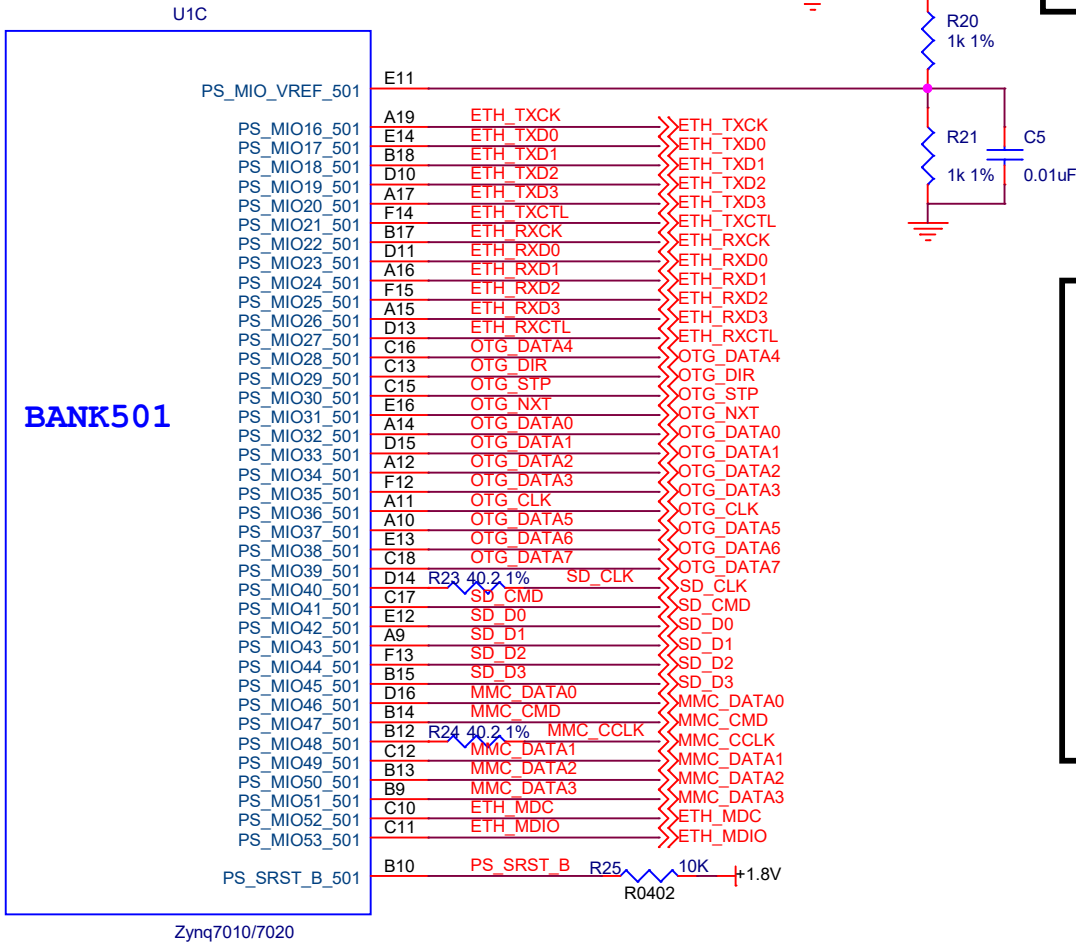
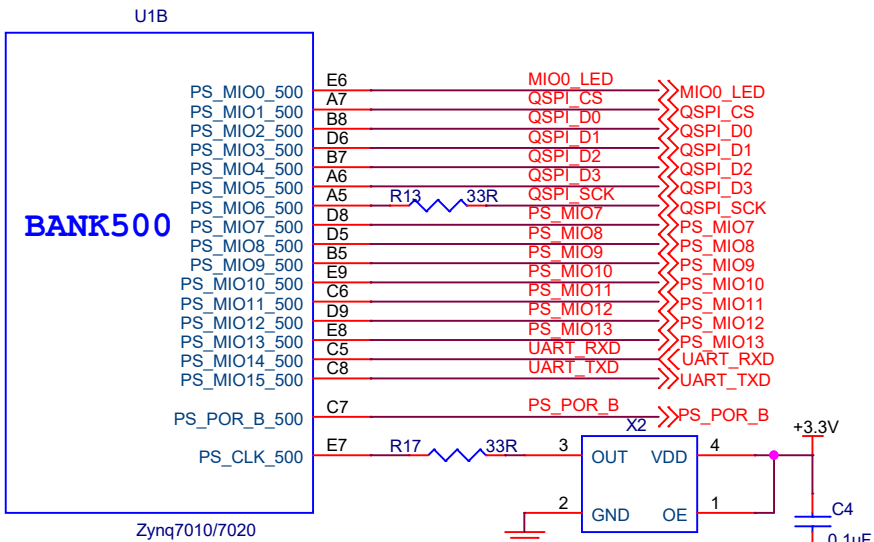
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ZYNQ-CORE		
Size	Page Name	Rev
A4	01_Block Diagram	1.0
Date:	Thursday, November 04, 2021	Sheet 1 of 12



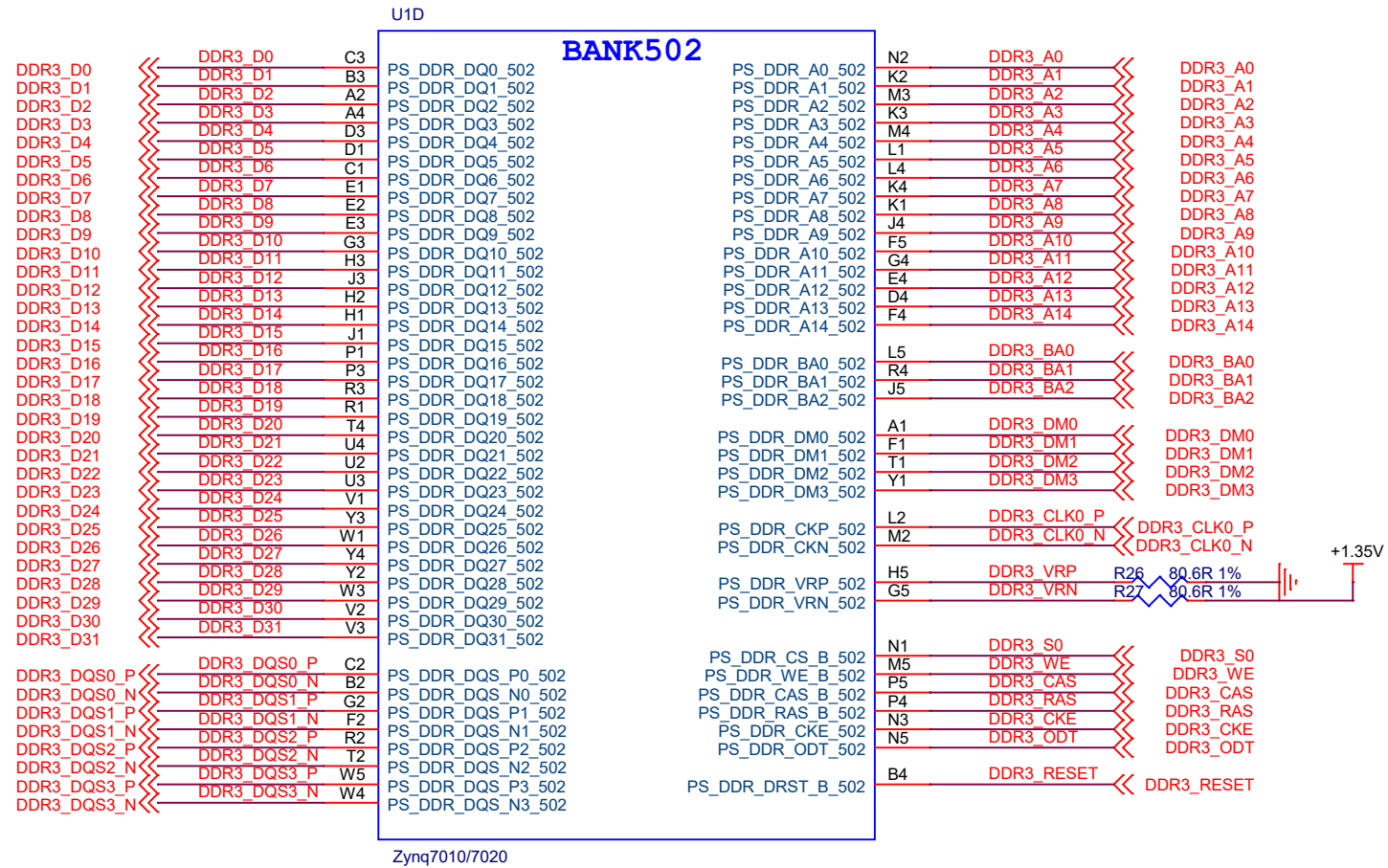
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Size A4	Page Name	02_FPGA Bank0	
Date:	Thursday, November 04, 2021	Sheet	2 of 12
Rev			1.0



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Design Name			
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Size	Page Name	Rev	
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Date:	Thursday, November 04, 2021	Sheet	4 of 12

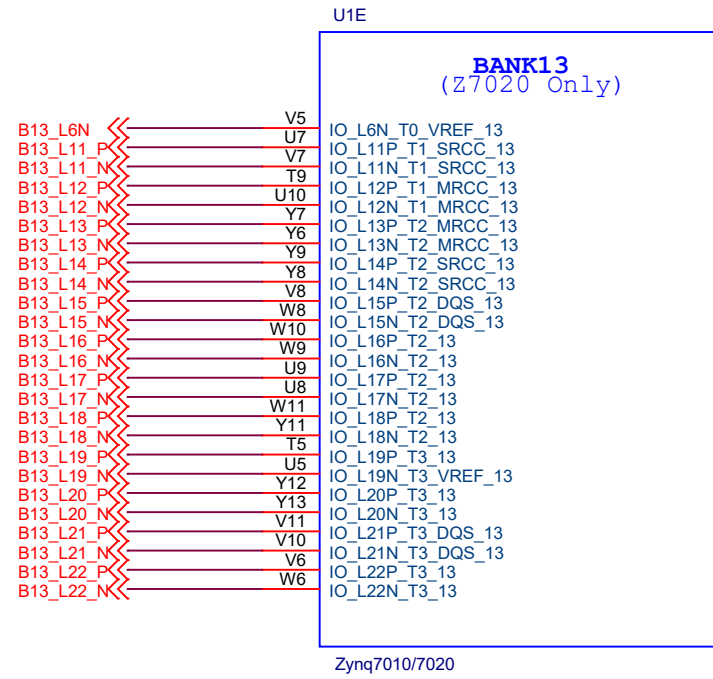


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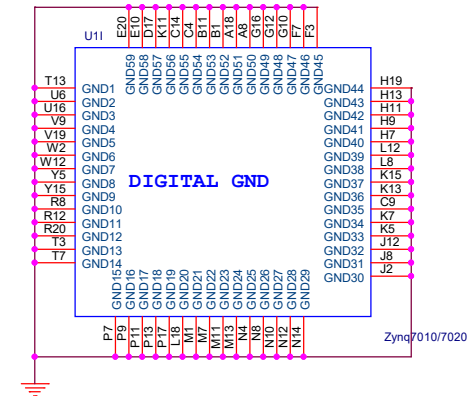
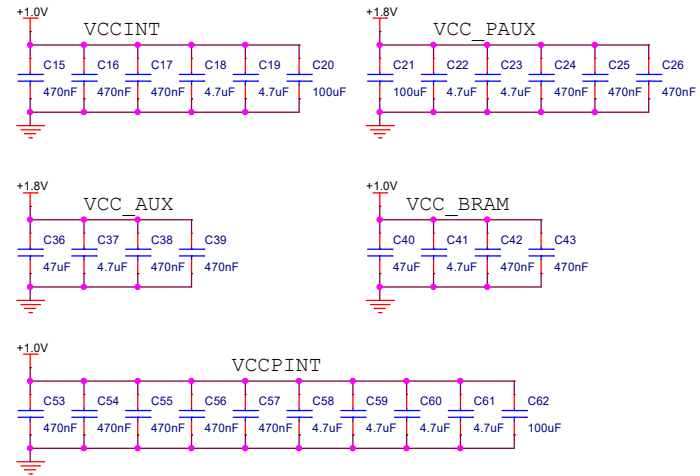
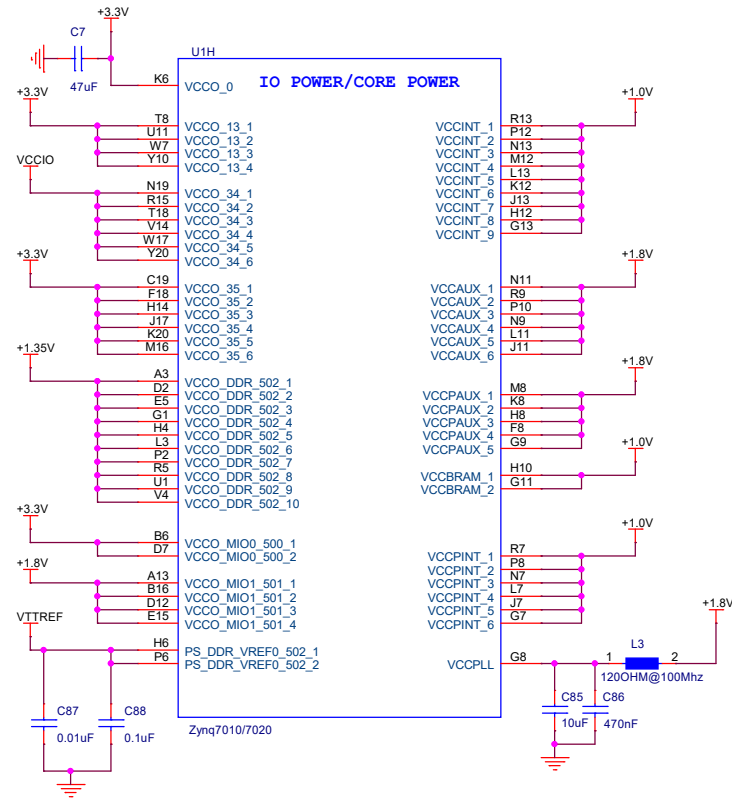
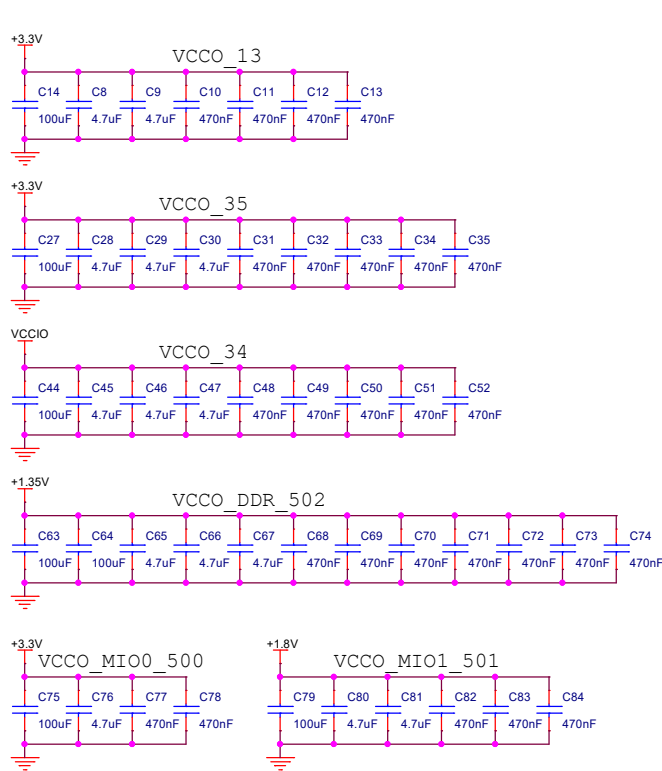
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B

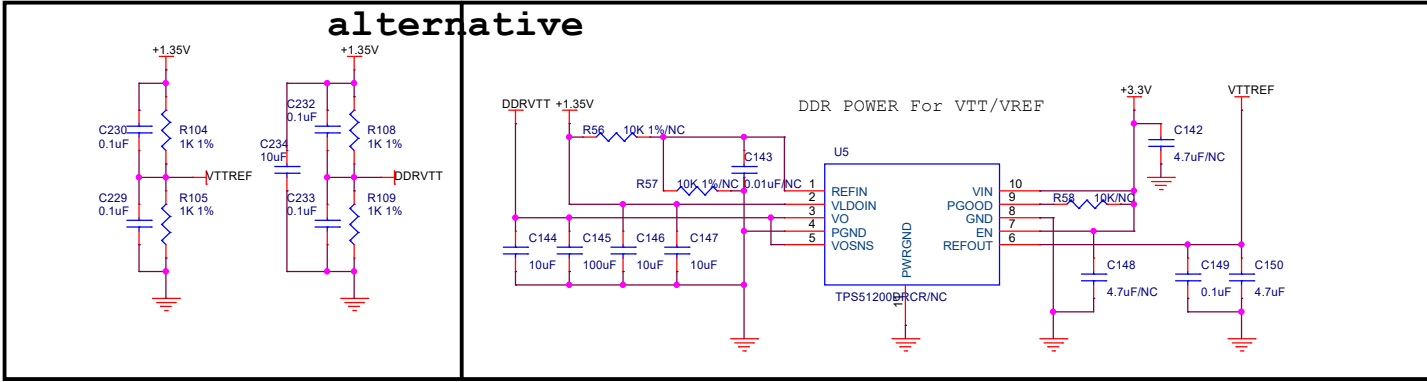
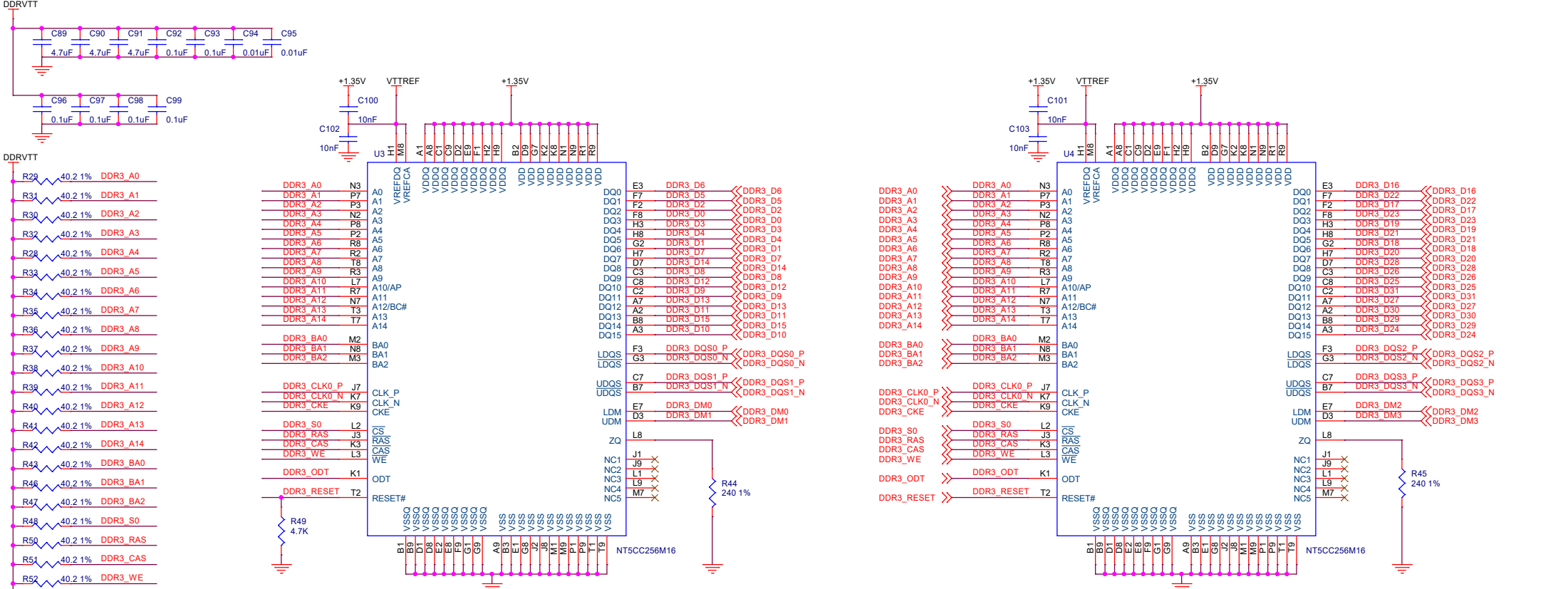
A



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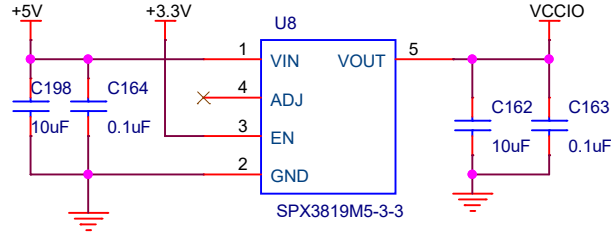
$$V_{out}=0.6 \cdot R1/R2+0.6$$

### 1.0V POWER@2A

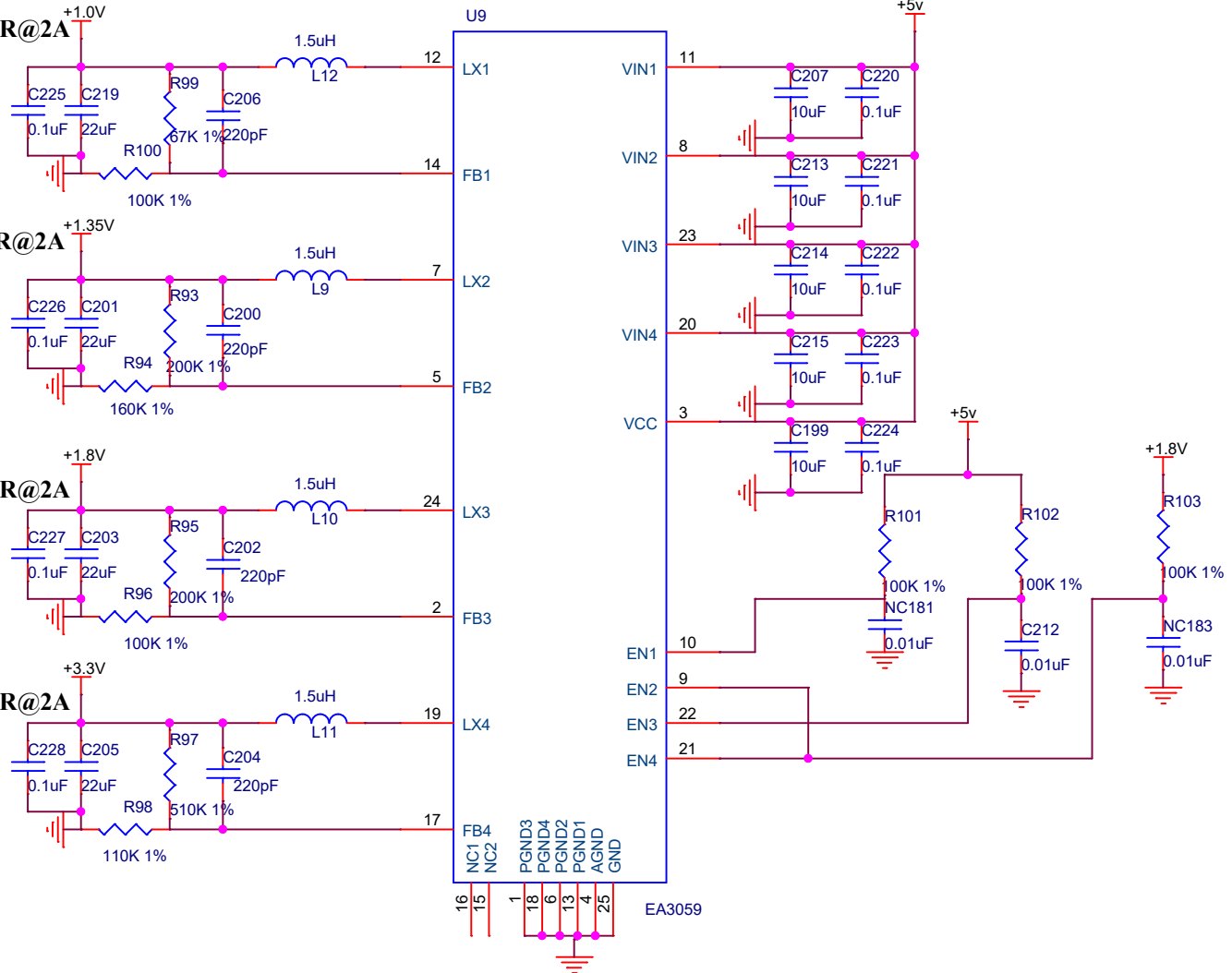
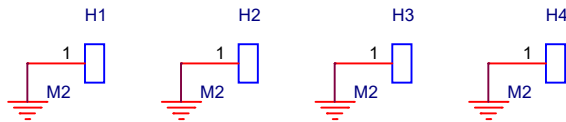
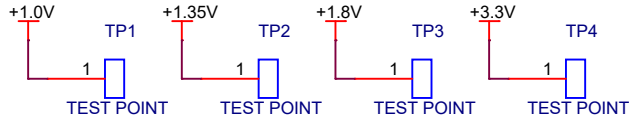
### 1.35V POWER@2A

### 1.8V POWER@2A

### 3.3V POWER@2A



**Power On Sequence:**  
1.0V -> 1.8V -> 1.35V -> 3.3V -> VCCIO



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