



North South University
Department of Electrical & Computer Engineering

LAB REPORT

Course Code: FEE - 141L

Course Title: Electrical Circuits Lab

Faculty: RQN

Experiment Number: 4

Experiment Name: 1

Delta Wye Conversion

Experiment Date: 26/2/25

Date of Submission: 5/3/25

Section: 19

Group Number: 2

Submitted To: KASHFIA MAHMOOD

| Submitted By: | | | Score |
|---------------|---------------|------------|-------|
| Serial No | Student Name | ID | |
| | Dilshad Karim | 2223014042 | |
| 02 | Nadeem Mahmud | 1921877642 | |
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NORTH SOUTH UNIVERSITY

DEPARTMENT OF ELECTRICAL & COMPUTER ENGINEERING



EEE41L/ETE141L

Instructor's Signature _____

Table 1:

| Theoretical R | Measured R | % Error |
|---------------|------------|---------|
| 15k | 14.89 K | 0.733 % |
| 5k | 5.12 K | 2.4% |

Table 2:

Rt= 15 K

Rt= 15 K

| Readings | Circuit 1 | | Circuit 2 | | % Error | |
|----------|-----------|---------|-----------|---------|---------|---------|
| V_{AD} | 10 V | 9.97 | 10 V | 9.97 | 0.3% | 0.3% |
| V_{BD} | 5 V | 4.98 | 5 V | 4.90 | 0.4% | 2% |
| V_{CD} | 5 V | 4.98 | 5 V | 4.89 | 0.4% | 2.2% |
| V_{AB} | 5 V | 4.98 | 5 V | 5.05 | 0.4% | 1% |
| V_{BC} | 0 V | 10.6 mV | 0 V | 12.4 mV | M Error | M Error |
| V_{AC} | 5 V | 4.99 | 5 V | 5.07 | 0.2% | 1.4% |

Report:

1. The resistors in Circuit 1 are in series or in parallel combination?
2. What technique would you use to find the equivalent resistance?
3. Perform Delta-Wye conversion for ΔABC (upper portion) of circuit 1. Show all your steps to find the equivalent resistance R_1 , R_2 , R_3 from R_a , R_b , R_c .
4. Redraw the equivalent the circuit after applying the Delta-Wye conversion for ΔABC . Is it same as circuit 2?
5. Calculate Req.
6. Calculate the voltage of R_1 , R_2 , R_3 .
7. Calculate V_{AB} , V_{BC} , V_{AC} and V_{AD} , V_{BD} , V_{CD} . Do your calculated values match the measured values for circuit 2? Find the % Error.
8. Using Table 2, analyze whether Circuit 2 is equivalent to Circuit 1? Was Delta-Wye conversion successful?

Experiment name:

Delta Wye Conversion

Objectives:

The objective of this experiment is to analyze the resistor network in two different circuit configurations. Then apply the Delta Wye conversion technique and verify whether Circuit 2 is equivalent to Circuit 1.

List of Equipment:

1. Trainer Board
2. DMM
3. 5 x 15 k Ω resistor
4. 3 x 5 k Ω resistor

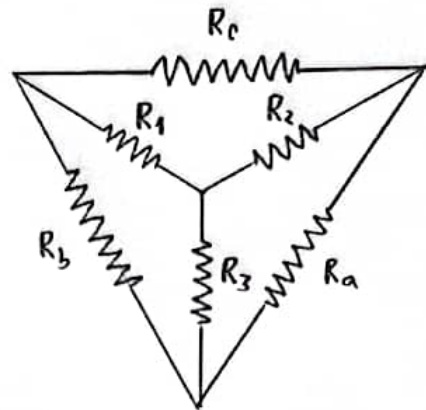
Theory:

Resistor networks can be analyzed using series parallel combinations and transformation techniques such as Delta Wye conversion. That a transformation is used to simplify complex resistor networks that are neither purely series nor parallel. The equivalent resistance of Δ \leftrightarrow transformation is calculated by:

$$R_1 = \frac{R_b R_c}{R_a + R_b + R_c}$$

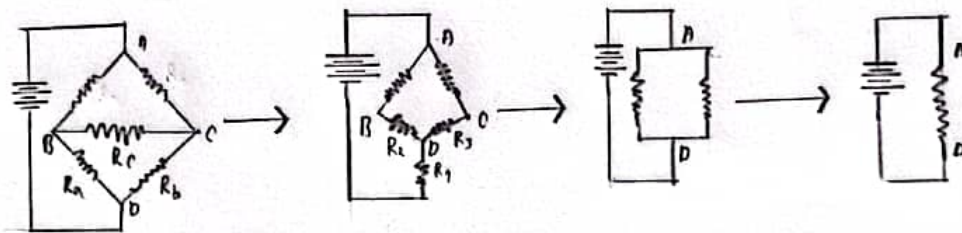
$$R_2 = \frac{R_a R_c}{R_a + R_b + R_c}$$

$$R_3 = \frac{R_a R_b}{R_a + R_b + R_c}$$

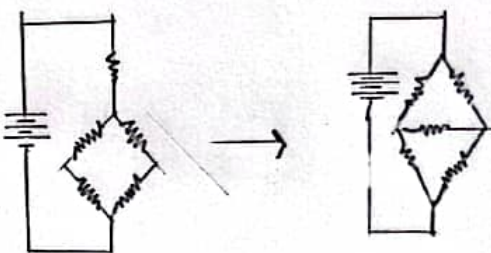


Where R_a, R_b, R_c are the resistances in the Δ configuration and R_1, R_2, R_3 are the transformed Y resistances.

Performing $\Delta - Y$ conversion on circuit 1



Performing $\Delta - Y$ conversion on circuit 2



Calculation:

For circuit 2

$$R_{a1} = 15 \text{ k}\Omega$$

$$R_{b2} = 15 \text{ k}\Omega$$

$$R_{c3} = 15 \text{ k}\Omega$$

$$R_4 = 15 \text{ k}\Omega$$

$$R_5 = 15 \text{ k}\Omega$$

R_2 , R_4 and R_3 , R_5 are in series,

$$R_{s1} = R_2 + R_4$$

$$= 5 + 15$$

$$= 20 \text{ k}\Omega$$

$$R_{s2} = R_3 + R_5$$

$$= 5 + 15$$

$$= 20 \text{ k}\Omega$$

R_{s1} and R_{s2} are in parallel,

$$R_p = \left(\frac{1}{R_{s1}} + \frac{1}{R_{s2}} \right)^{-1}$$

$$= \left(\frac{1}{20} + \frac{1}{20} \right)^{-1}$$

$$= 10 \text{ k}\Omega$$

R_1 and R_p are in series,

$$R_{eq} = R_1 + R_p$$

$$= 5 + 10$$

$$= 15 \text{ k}\Omega$$

Calculating voltage of R_1 , R_2 and R_3

$$\begin{aligned} V_{R_1} &= \frac{E \cdot R_1}{R_{eq}} \\ &= \frac{10 \times 5}{15} \\ &= 3.33 \text{ V} \end{aligned}$$

$$\begin{aligned} V_{R_2} &= V_{AB} - V_{R_1} \\ &= 5 - 3.33 \\ &= 1.67 \text{ V} \end{aligned}$$

$$\begin{aligned} V_{R_3} &= V_{R_2} \\ &= 1.67 \text{ V} \end{aligned}$$

Calculating V_{AB} , V_{BC} , V_{AC} , V_{AD} , V_{BD} and V_{CD}

$$\begin{aligned} V_A &= E \\ &= 10 \text{ V} \end{aligned}$$

$$\begin{aligned} V_B &= E - V_{R_1} - V_{R_2} \\ &= 10 - 3.33 - 1.67 \\ &= 5 \text{ V} \end{aligned}$$

$$V_c = V_B$$

$$= 5V$$

$$V_D = E - I_r \times R_{eq}$$

$$= 10 - \frac{E}{R_{eq}} \times R_{eq}$$

$$= 10 - 10$$

$$= 0$$

$$\therefore V_{AB} = 10 - 5$$

$$= 5V$$

$$\therefore V_{BC} = 5 - 5$$

$$= 0V$$

$$\therefore V_{AC} = 10 - 5$$

$$= 5V$$

$$\therefore V_{AD} = 10 - 0$$

$$= 10V$$

$$\therefore V_{BD} = 5 - 0$$

$$= 5V$$

$$\therefore V_{CD} = 5 - 0$$

$$= 5V$$

$$\% \text{ Error} = \left| \frac{\text{Experimental value} - \text{Theoretical value}}{\text{Theoretical value}} \right| \times 100$$

From table 2

$$V_{AD} = 9.97V$$

$$V_{BD} = 4.90V$$

$$V_{CD} = 4.89V$$

$$V_{AB} = 5.05V$$

$$V_{BC} = 0.0124V$$

$$V_{AC} = 5.07V$$

Now,

$$\% \text{ Error for } V_{AB} = \left| \frac{5.05 - 5}{5} \right| \times 100$$

$$= 1\%$$

$$\% \text{ Error for } V_{BC} = \left| \frac{0 - 0.0124}{0} \right| \times 100$$

$$= \text{Measurement error}$$

$$\% \text{ Error for } V_{Ae} = \left| \frac{5 - 5.07}{5} \right| \times 100$$

$$= 1.4\%$$

$$\% \text{ Error for } V_{Ad} = \left| \frac{10 - 9.97}{10} \right| \times 100$$

$$= 0.3\%$$

$$\% \text{ Error for } V_{Bd} = \left| \frac{5 - 4.9}{5} \right| \times 100$$

$$= 2\%$$

$$\% \text{ Error for } V_{Cd} = \left| \frac{5 - 4.89}{5} \right| \times 100$$

$$= 2.2\%$$

Using Table 2 we can say that circuit 2 is equivalent to circuit 1. We can also see that the % Error of both circuit is almost identical, which indicate that both the circuit has same amount of voltages running through them. So the Delta Wye conversion was successful.

Discussion:

From this experiment we've learned the Delta-Wye conversion and Wye Delta conversion. Using this method we can calculate circuit. For example, in circuit 1 it wasn't clear if R_a , R_b and R_c were in series or parallel but after performing Delta Wye conversion we could easily calculate R_{eq} of the circuit. However this conversion method not only help us for this experiment but also it will help us in future. As a result we got slight error. Otherwise our Delta Wye conversion was successful.