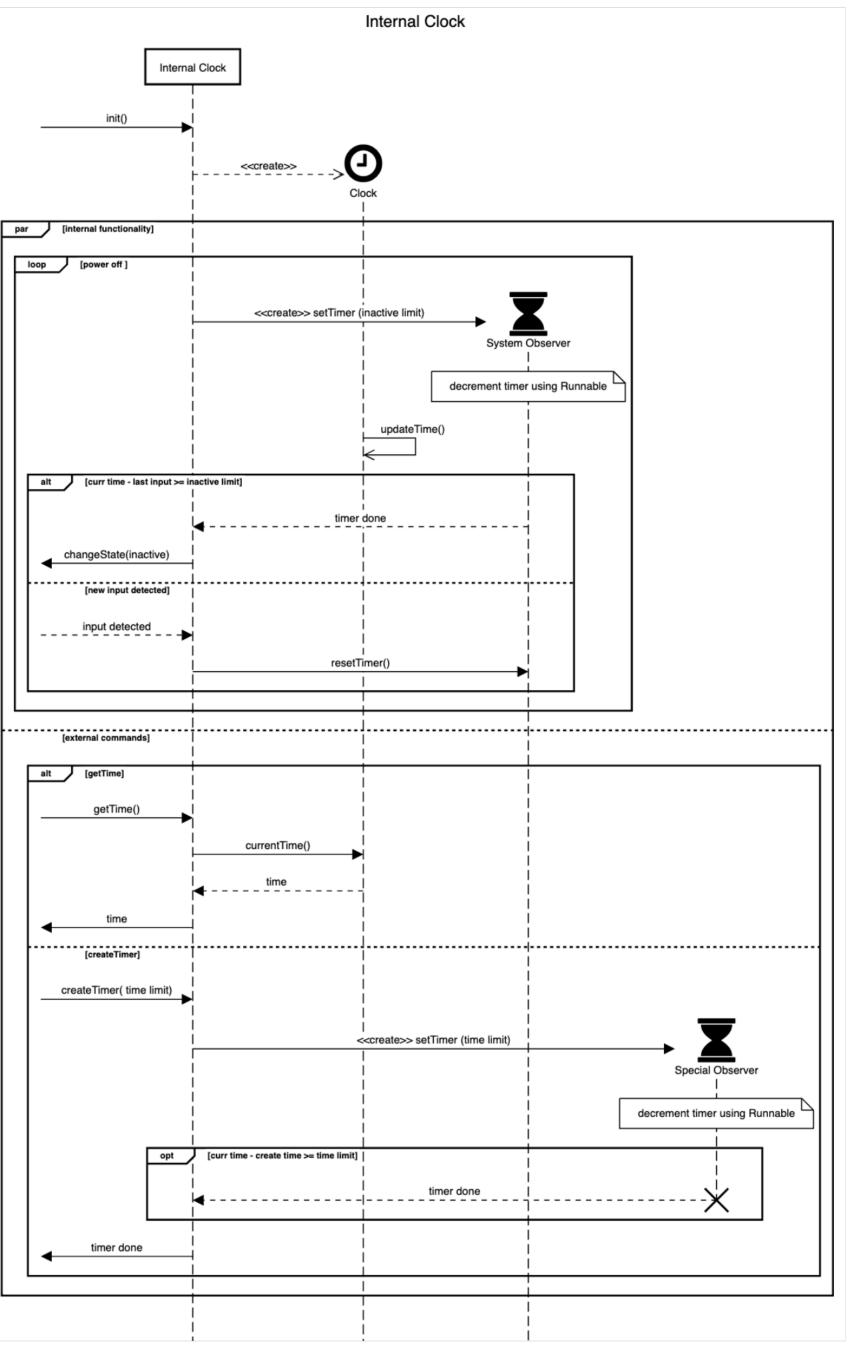
internal_clock



Sequence Diagram