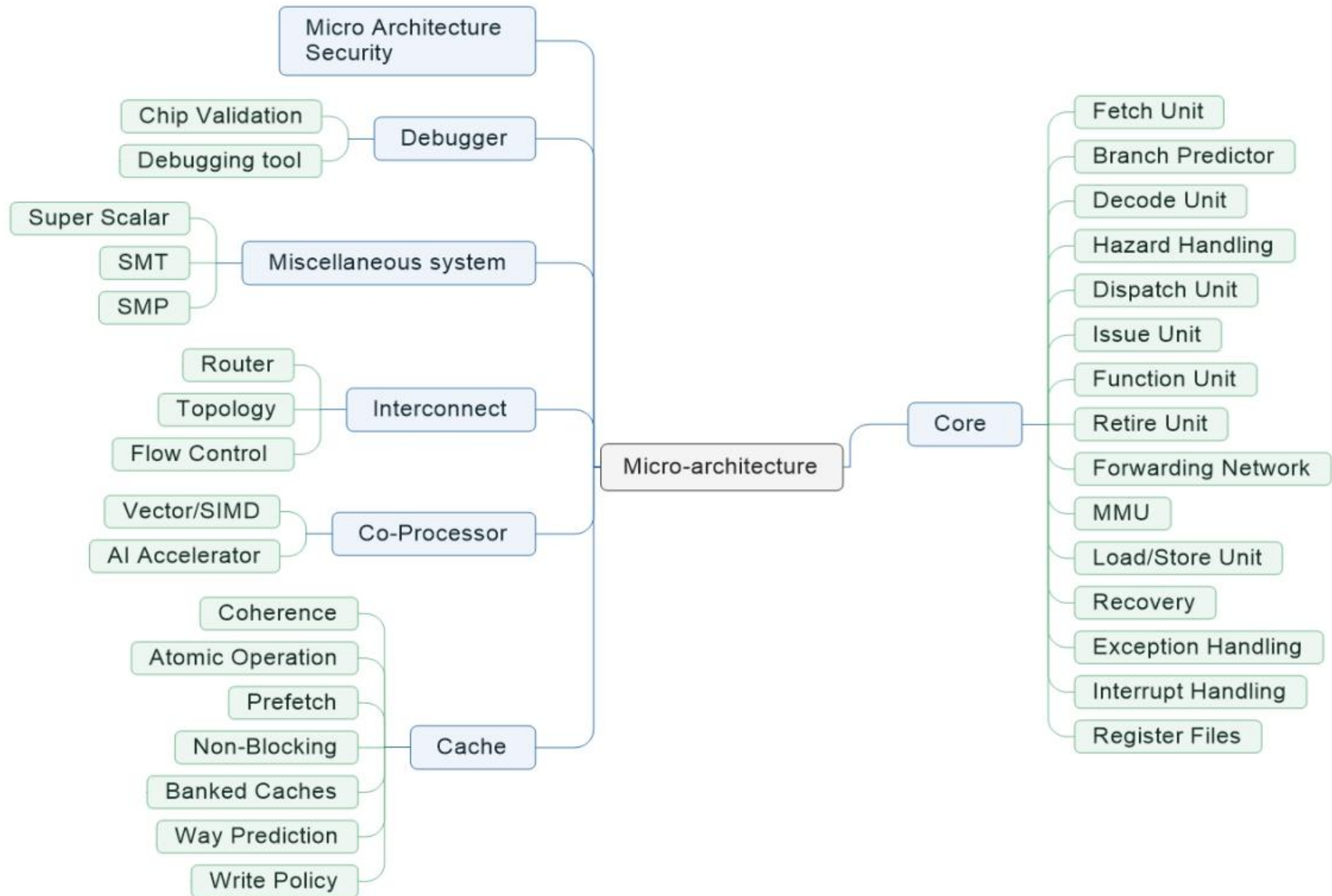


R01 Micro-architecture



R02 Instruction Set Architecture

