

28-16-bit division

Program

LXI B,0000

LHLD 2052

XCHG

LHLD 2050

MOV A,L

AGAIN: SUB E

MOV L,A

MOV A,H

SBB D

MOV H,A

JC START

INX B

JMP AGAIN

START: DAD D

SHLD 2056

MOV L,C

MOV H,B

SHLD 2054

HLT

Output

Registers			Flag		Load me at	
A	06		S	0		
BC	03	00				
DE	06	00	Z	1		
HL	00	00	AC	0		
PSW	00	00				
PC	42	0D	P	1		
SP	FF	FF	C	0		
Int-Reg	00					

Decimal - Hex Conversion			
Decimal		Hex	
<input type="text" value="0"/>		<input type="text" value="0"/>	
<input type="button" value="To Hex"/>		<input type="button" value="To Dec"/>	

I/O Ports			
<input type="text" value="0"/>	-	+	<input type="text" value="00"/>
<input type="button" value="Update Port Value"/>			

Memory			
<input type="text" value="0"/>	-	+	<input type="text" value="00"/>

Assembly Code	
1	LXI B, 0000
2	LHLD 2052
3	XCHG
4	LHLD 2050
5	MOV A, L
6	AGAIN: SUB E
7	MOV L, A
8	MOV A, H
9	SBB D
10	MOV H, A
11	JC START
12	INX B
13	JMP AGAIN
14	START: DAD D
15	SHLD 2056
16	MOV L, C
17	MOV H, B
18	SHLD 2054
19	HLT