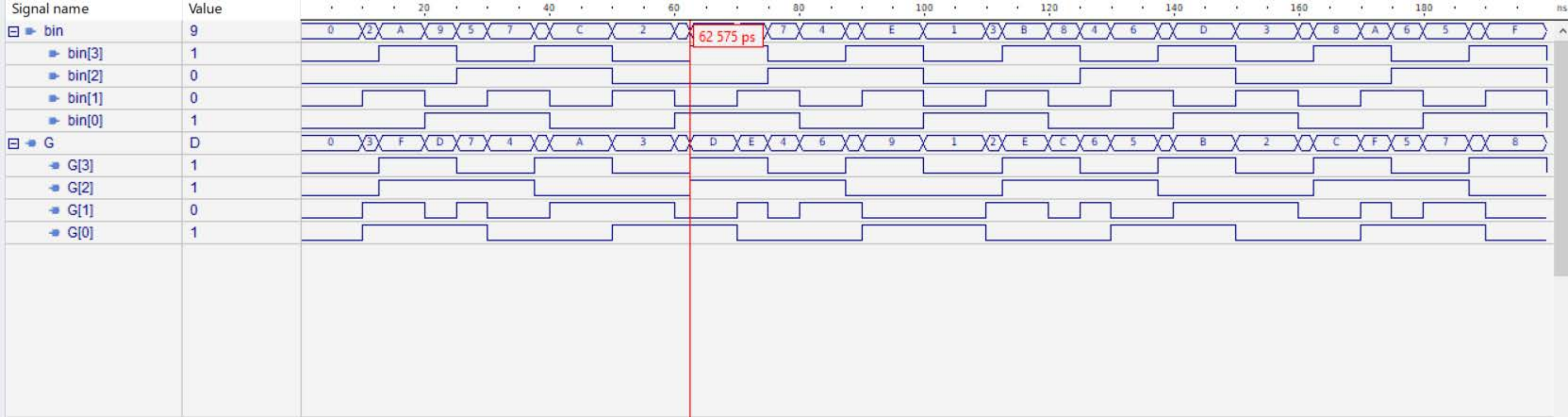


Binary to gray Code Converter

```
1
2  LIBRARY ieee;
3  USE ieee.std_logic_1164.ALL;
4
5  entity bin2gray is
6  port(   bin : in std_logic_vector(3 downto 0);
7         G : out std_logic_vector(3 downto 0)
8         );
9  end bin2gray;
10
11 architecture gate_level of bin2gray is
12
13 begin
14
15 G(3) <= bin(3);
16 G(2) <= bin(3) xor bin(2);
17 G(1) <= bin(2) xor bin(1);
18 G(0) <= bin(1) xor bin(0);
19
20 end;
```



Cursor 1 | 62.575 ps