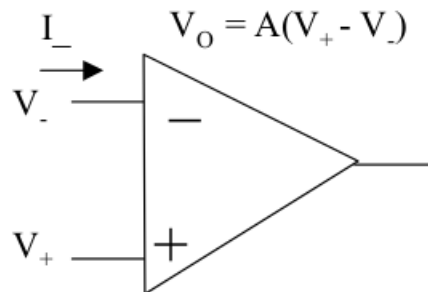


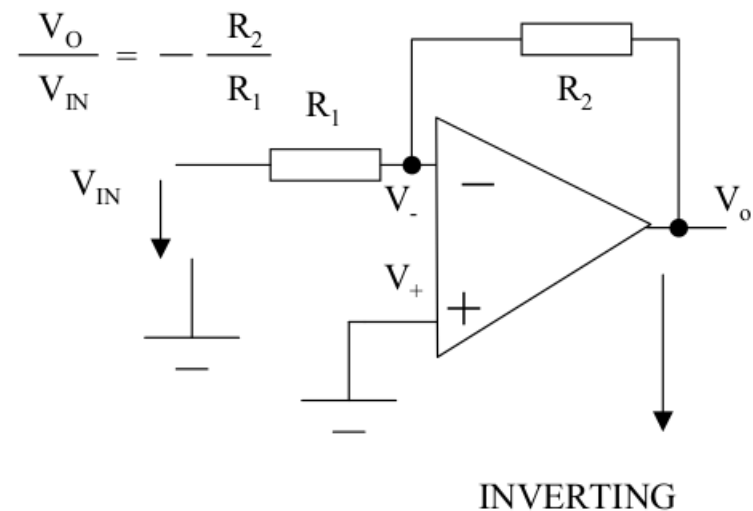
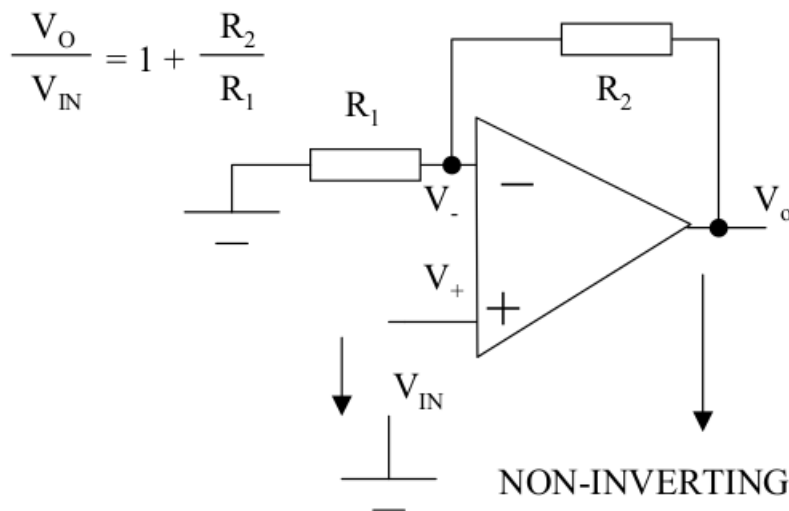
OpAmp Device As a Buffering Stage

Both Analog and Digital Circuit

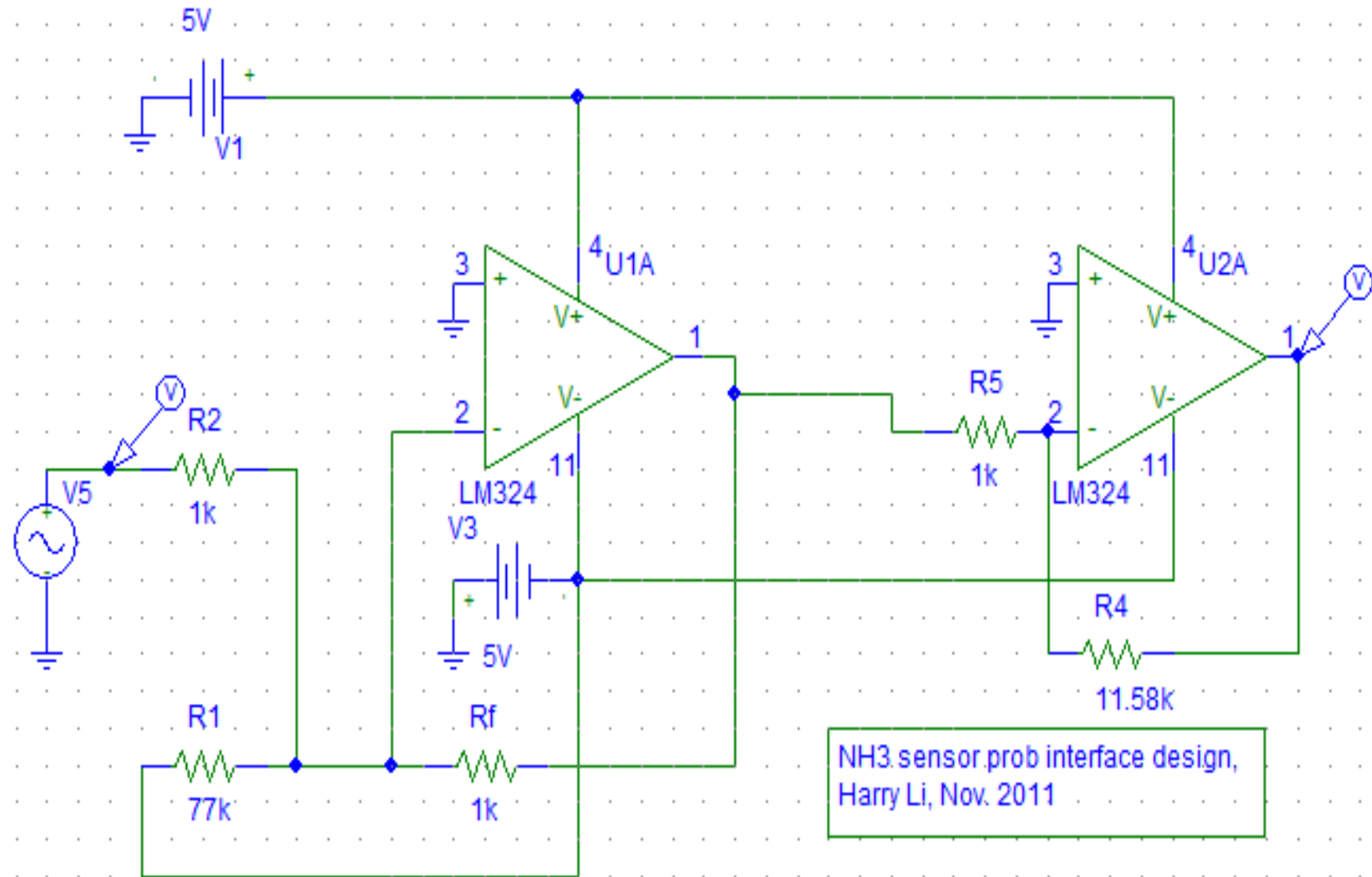


(1) To protect the previous stage's output signal, which is the input to the next stage, while sampling/connecting the signal to its next stage logic circuit. (2) Unit gain non-inverting OpAmp configuration is an excellent choice.

Ideal OpAmp Properties: (1) very large gain, $A \gg M$; (2) draws very little current, $I_- \sim 0$, e.g., very high impedance; (3) $V_O = A(V_+ - V_-)$ is finite range, which leads to $V_+ = V_-$.

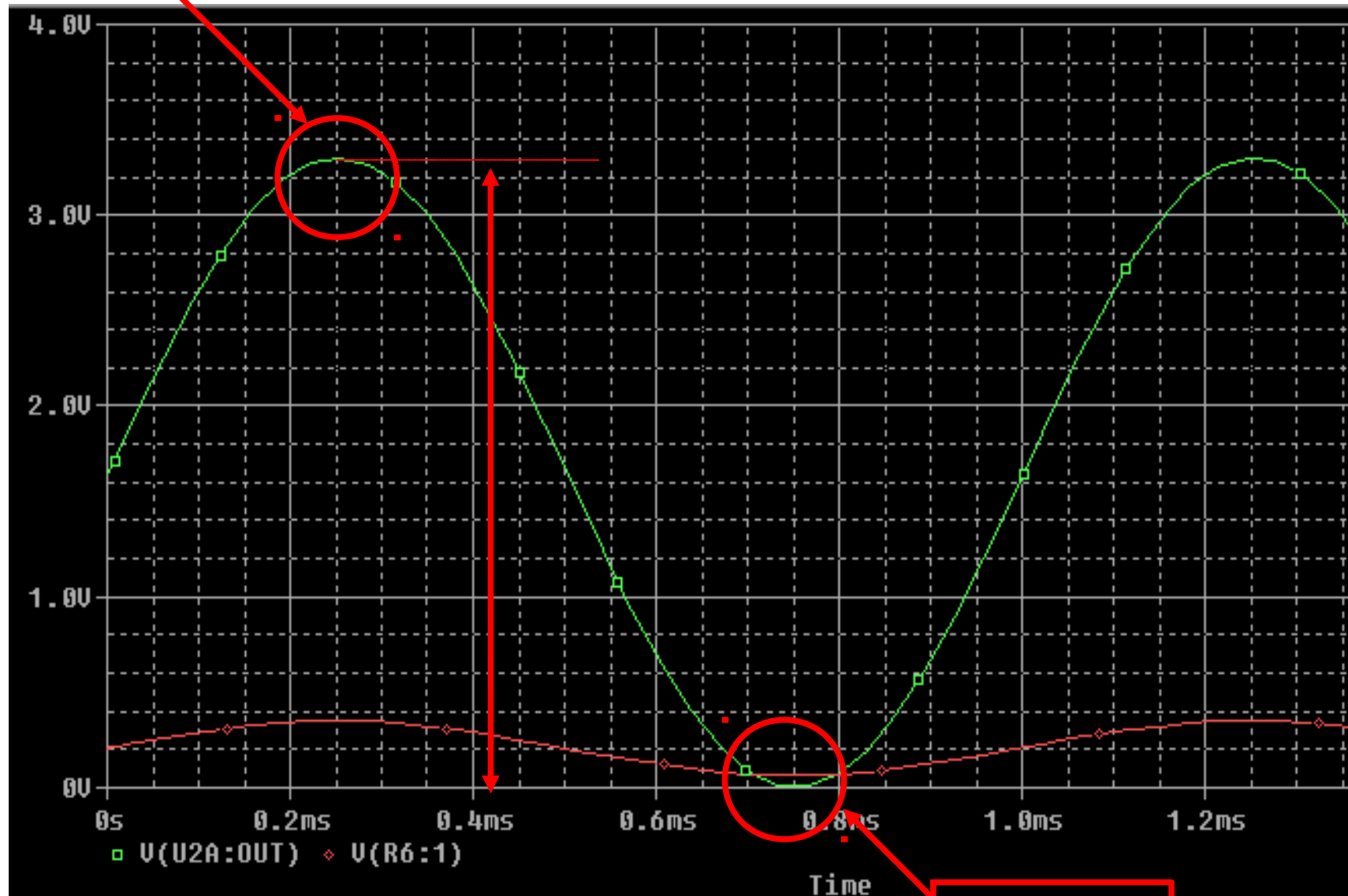


Circuit Design for NH3 Sensor



Simulation Result

The output:
3.3V



The input
offset 65mV