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BADRI VISHAL / SPM HOSPITAL LUCKNOW

04/11/2018 HYPERTENSION & DIABETES MELLITUS TYPE-2

5.166.00

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[illegible]

A diagram of a 5-bit shift register. It consists of five rectangular blocks representing flip-flops, connected in a chain. The first flip-flop has an input labeled 'A' and an output labeled 'A'. The output of the first flip-flop is connected to the input of the second flip-flop, which has an output labeled 'B'. This pattern continues for the third and fourth flip-flops, with outputs labeled 'C' and 'D' respectively. The fifth flip-flop has an input labeled 'E' and an output labeled 'E'. The entire register is controlled by a common clock signal, indicated by a triangle symbol at the bottom.

1.

Chief Medical Officer

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