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Objective :
Highly motivated and detail-oriented post-graduate in Electrical and Electronics Engineering with a strong foundation in embedded systems and programming. Eager to contribute technical skills, creativity, and a passion for innovation to a dynamic team as an Embedded Software Engineer.

Education :
M.Tech – Power Electronics
National Institute of Technology Tiruchirapalli, Tamilnadu | 2020

B.E. – Electrical Engineering
Vishwakarma Government Engineering College, Ahmedabad, Gujarat | 2018

Technical Skills :
- Programming Languages : C, Python, MATLAB, HTML, CSS, Verilog HDL
- Embedded Systems : Microcontrollers(ARM, AVR), Raspberry Pi
- Communication Protocols : UART, SPI, I2C
- Software Development Tools : MATLAB, Arduino IDE, Thonny, Xilinx ISE Design Suite
- Version Control : Git

Experience :
Internship :
- Innovix Pro Private Limited | Oct 2023 – Jan 2024
    - Performed different projects on Arduino, Raspberry Pi, ESP8266
    - Seamless integration of hardware components.
    - Conducted thorough testing and troubleshooting to ensure product reliability.
    - Collaborated with cross-functional teams to troubleshoot and resolve hardware/software integration issues.

- Interactive Warriors Studio Private Limited | June 2023 – Sep 2023
    - Write, test, and maintain Python code in existing client web and game related projects.
    - Collaborate with teams to understand project requirements. Identify and fix bugs or issues in the code.
    - Participate in and conduct code reviews & testing games.

Projects :

- Line Following Robot :
  It's a two wheel rover which follows black line on white surface. Arduino nano was used as controller and Arduino IDE for software. IR sensor and L293D controller was used.

- Pi Pico Retrogaming Gameboy :
  Developed a Raspberry Pi-based retro gaming console, leveraging the single-board computer's capabilities to emulate classic gaming systems and deliver an immersive gaming experience with a variety of supported titles.

- PG Major Project – Reduced carrier PWM scheme with unified logical expressions for reduced switch count multilevel inverters :
  Reduced switches count multilevel inverters with modified reduced carrier PWM scheme using generalised logical expressions. Worked on FPGA, with verilog programming to generate PWM pulses.

Certifications :

- Attended online course from Coursera for "Machine Learning" by Professor Andrew Ng from Stanford University.
- Attended two days workshop on "MATLAB Programming and Applications in Electrical Engineering" organised by IEEE Student Branch, NIT Trichy in 2018.
- Attended two days workshop on "Drone" at Nirma University in 2016.
- One day industrial visit at Kakarapar Nuclear Power Corporation of India Limited in 2017.

Extracurricular Activities :

- Member of organising team in PGION-2K19 at NIT-Trichy.