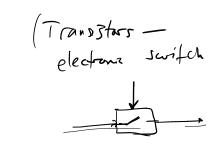
2 transisters - NAND-

NAND	T	F
T	F	T
F	T	T



Clam: NAND & universal, io. we can build anything out of NAND.

Lemma: AND+OR+NOT are universal.

Prof:

Suppose we have an arbitrary Rollan function by Lemma: we can Suild AND, OR, NOT out of NAND.

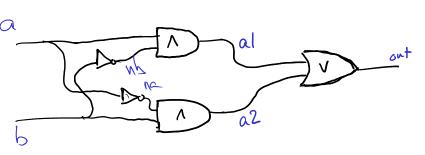
Prof: Project 1.

×	9	f(x,y)	- XDR	write down the inputs for each row that is T using AND, NOT.
T	T	F		
T	P	7	<b>→</b>	× 1 ¬y.
	7 F	T F		7x 1 y. Canbine with OR:
V	1			$(x \wedge 7y) \vee (7x \wedge y),$

Let's build XOR gate!

(1) Come up u/ a bollen expression

& Drawil as a digital logie circuit diagram.



NAND NAND NOR

either a or b 3 capendry on sel.