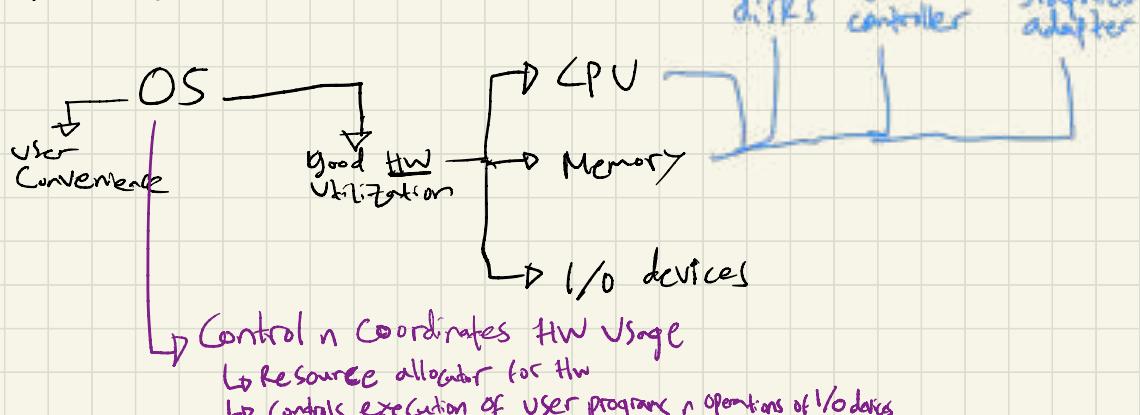


SC2885



Kernel / monitor / supervisor mode / superuser mode

↳ accept commands from users + HW

User mode



Root / administrator
= user account in OS
↳ jobs execute in user mode
↳ may execute code in kernel indirectly by loading kernel module

Types of Computing System

↳ batch (doesn't interact with comp directly)

- reduce setup time by batching similar jobs

- only 1 job in memory at any time

- when job waits for I/O, CPU idles

↳ Time-Sharing (multiprogramming)

- several jobs kept in main mem

- highly interactive, supports multiple online users

- job swap in unit of memo to hard disk

↳ real time

- job must be completed within deadline

real time (phone)

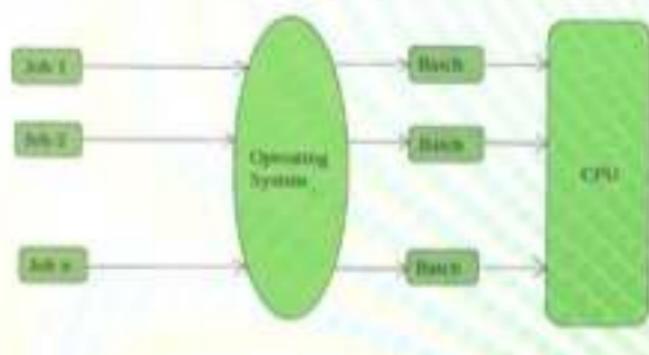
↳ limited memo, slow processors

multi-core processor = 1 processor that execute 1

multi processor = 1C processor that allows simultaneous processing

1. Batch Operating System -

This type of operating system does not interact with the computer directly. There is an operator which takes similar jobs having the same requirement and group them into batches. It is the responsibility of the operator to sort jobs with similar needs.



Advantages of Batch Operating System:

- It is very difficult to guess or know the time required for any job to complete. Processors of the batch systems know how long the job would be when it is in queue
- Multiple users can share the batch systems
- The idle time for the batch system is very less
- It is easy to manage large work repeatedly in batch systems

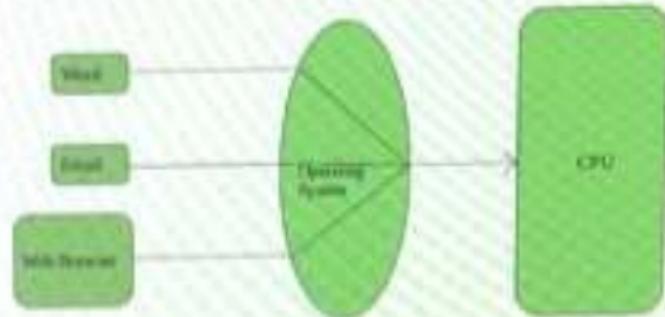
Disadvantages of Batch Operating System:

- The computer operators should be well known with batch systems
- Batch systems are hard to debug
- It is sometimes costly
- The other jobs will have to wait for an unknown time if any job fails

Examples of Batch based Operating System: Payroll System, Bank Statements, etc.

2. Time-Sharing Operating Systems -

Each task is given some time to execute so that all the tasks work smoothly. Each user gets the time of CPU as they use a single system. These systems are also known as Multitasking Systems. The task can be from a single user or different users also. The time that each task gets to execute is called quantum. After this time interval is over OS switches over to the next task.



Needs:

- ↳ memory management
- ↳ CPU Scheduling
- ↳ I/O device scheduling

Advantages of Time-Sharing OS:

- Each task gets an equal opportunity
- Fewer chances of duplication of software
- CPU idle time can be reduced

Disadvantages of Time-Sharing OS:

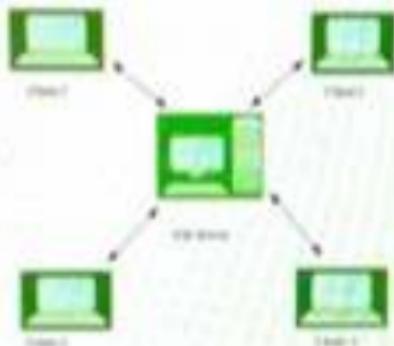
- Reliability problem
- One must have to take care of the security and integrity of user programs and data
- Data communication problem

Examples of Time-Sharing OSs are: Multics, Unix, etc.

→ windows, Mac OS, Unix, Linux, desktops, servers

4. Network Operating System -

These systems run on a server and provide the capability to manage data, users, groups, security, applications, and other networking functions. These types of operating systems allow shared access of files, printers, security, applications, and other networking functions over a small private network. One more important aspect of Network Operating Systems is that all the users are well aware of the underlying configuration, of all other users within the network, their individual connections, etc. and that's why these computers are popularly known as tightly coupled systems.



Advantages of Network Operating System:

- Highly stable centralized servers
- Security concerns are handled through servers
- New technologies and hardware up-gradation are easily integrated into the system
- Server access is possible smoothly from different locations and types of machines

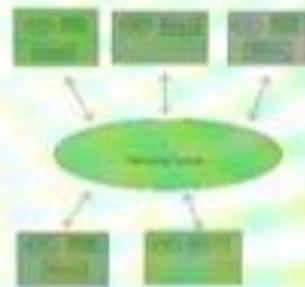
Disadvantages of Network Operating System:

- Servers are costly
- User has to depend on a central location for most operations
- Maintenance and updates are required regularly

Examples of Network Operating System are: Microsoft Windows Server 2003, Microsoft Windows Server 2008, UNIX, Linux, Mac OS X, Novell NetWare, and OS/3, 4%

3. Distributed Operating System -

These types of the operating system is a recent advancement in the world of computer technology and are being widely accepted all over the world and, that too, with a great pace. Various autonomous interconnected computers communicate with each other using a shared communication network. independent systems possess their own memory unit and CPU. These are referred to as **loosely coupled systems** or distributed systems. These systems' processors differ in size and function. The major benefit of working with these types of the operating system is that it is always possible that one user can access the files or software which are not actually present on his system but some other system connected within this network i.e., remote access is enabled within the devices connected in that network.



Advantages of Distributed Operating System:

- Failure of one will not affect the other network communication, as all systems are independent from each other
- Electronic mail increases the data exchange speed
- Since resources are being shared, computation is highly fast and durable
- Load on host computer reduces
- These systems are easily scalable as many systems can be easily added to the network
- Delay in data processing reduces

Disadvantages of Distributed Operating System:

- Failure of the main network will stop the entire communication
- To establish distributed systems the language which is used are not well defined yet
- These types of systems are not readily available as they are very expensive. Not only that the underlying software is highly complex and not understood well yet

Examples of Distributed Operating Systems are- Locus, etc.

These types of OSs serve real-time systems. The time interval required to process and respond to inputs is very small. This time interval is called response time.

Real-time systems are used when there are time requirements that are very strict like missile systems, air traffic control systems, robots, etc.

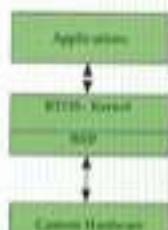
Two types of Real-Time Operating System which are as follows:

- **Hard Real-Time Systems:**

These OSs are meant for applications where time constraints are very strict and even the shortest possible delay is not acceptable. These systems are built for saving life like automatic parachutes or airbags which are required to be readily available in case of any accident. Flash memory is rarely found in these systems.

- **Soft Real-Time Systems:**

These OSs are for applications where time constraint is less strict.



Advantages of RTOS:

- **Maximum Consumption:** Maximum utilization of devices and system, that more output from all the resources.
- **Task Shifting:** The time assigned for shifting tasks in these systems are very less. For example, in older systems, it takes about 10 microseconds in shifting one task to another, and in the latest systems, it takes 3 microseconds.
- **Focus on Application:** Focus on running applications and less importance to applications which are in the queue.
- **Real-time operating system in the embedded system:** Since the size of programs are small, RTOS can also be used in embedded systems like in transport and others.
- **Error Free:** These types of systems are error free.
- **Memory Allocation:** Memory allocation is best managed in these types of systems.

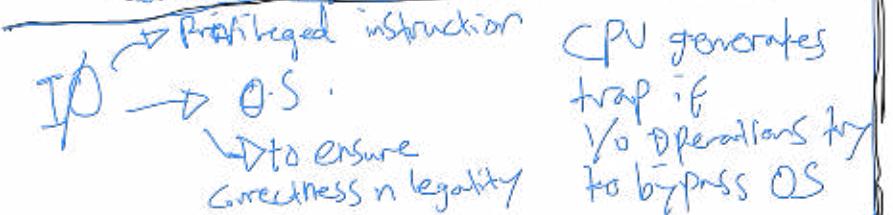
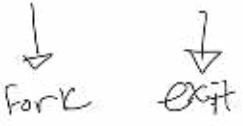
Disadvantages of RTOS:

- **Limited Tasks:** Very few tasks run at the same time and their concentration is very less on few applications to avoid errors.
- **Use heavy system resources:** Sometimes the system resources are not so good and they are expensive as well.
- **Complex Algorithms:** The algorithms are very complex and difficult for the designer to write on.
- **Device driver and interrupt signals:** It needs specific device drivers and interrupt signals to respond earliest to interrupts.
- **Thread Priority:** It is not good to set thread priority as these systems are very less prone to switching tasks.

Examples of Real-Time Operating Systems are: scientific experiments, medical imaging systems, industrial control systems, wireless systems, robots

- interrupt transfer control to Interrupt Service Routine through interrupt Vector-table
- incoming interrupts are disabled while another interrupt is processed to prevent loss of interrupts
- Trap → Software error or request (unhandled exception)
- Interrupt handling
 - ↳ preserves CPU state by storing reg in Program Counter (Context Switch)
 - ↳ determine type of interrupt, then appropriate interrupt Service routine
- DMA → used for high-speed I/O devices (transmit info close to mem speed)
 - ↳ 1 interrupt per block by OS
 - ↳ to move block from buffer to mem by device controller until byte count = 0
 - ↳ granted device acknowledge

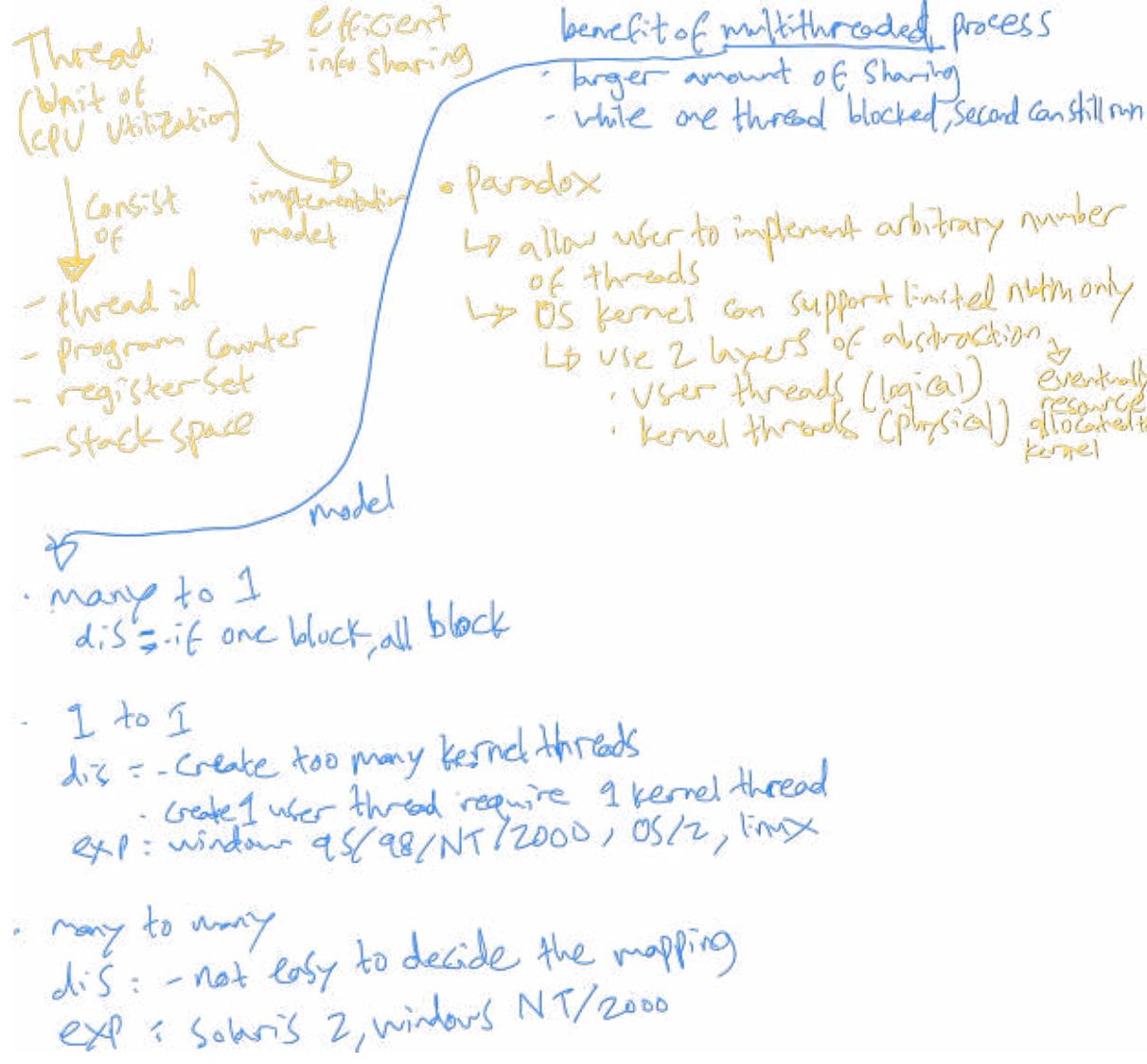
System Call =
 provide interface between user program & operating system
 ↳ assembly language
 or in C/C++
 ↳ require kernel mode



- all queues are stored in MM (kernel memory space)



- Scheduler
- ↳ independent process = not affected by others
 - ↳ cooperating process = mid communication with others by
 - ↳ message passing
 - test memo
 - program control words
- long term → Job
- select processes from disk
n load to MM for execution
the rest store in the memo
- Short term → CPU
- select among processes that are ready to execute, n allocates the CPU to one of them
- Medium-term
- when heavy load
Partially executed process swap from memo to harddisk
- when light load
swap back into MM
- ↳ Shared memo
- read memo
- exclusive access



③ to keep CPU busy \rightarrow No or event wait or terminate

Type of CPU Scheduler

↳ Non preemptive = CPU has been allocated to process till release by CPU terminated (S) or $\frac{1}{n}$, event wait (D) (new admitted job (A), when CPU core idle)

↳ Preemptive = CPU can be taken away from running process at any time by scheduler or happens (I, S, E, F, R)

• System-wide Objectives (Criteria)

↳ Max. CPU Utilization = % time CPU cores are busy = $\frac{\text{Execution time on each core}}{\text{total time} \times \text{number of cores}}$

↳ Max. Throughput = Num of processes completed their execution per unit of time

• Individual Process Objectives:

↳ Min. Turnaround Time (TAT) = amount of time to execute particular process from creation $\xrightarrow{(A)}$ to termination $\xleftarrow{(S)}$

↳ Min Waiting Time (WT) = amount of time process has been waiting in ready state

↳ min response time

↳ Completion Time (CT) = time when process complete its execution
↳ arrival time (AT) = time when process arrived in ready state
↳ burst time (BT) = time required by process for its execution (running)

I/O burst (waiting)

CPU burst (running)

Waiting time (ready)

at the end, there will be a CPU burst to terminate

CPU Scheduler \rightarrow the one that select the process in ready list
Dispatcher \rightarrow module that give control of CPU to process

↳ dispatch latency = time to stop process in Start now

Scheduling algo:

Uniprocessor System

FCFS (First come first served)

- Non preemptive
- Convoy effect → short process suffers in waiting time due to an earlier arrived long process
- background processes



$$\text{Average WT} = \frac{(P_1 + P_2 + P_3)}{n \cdot np}$$

SJF (Shortest job first)

prioritize processes based on their CPU burst lengths (minimizing)

solve FCFS Convoy effect

- Non preemptive (SJF)
 - once process starts must wait till completes its CPU burst
- Preemptive (Shortest Remaining Time First / SRTF)
 - new process with CPU burst length less than remaining current running process, preemption occurs

→ optimal for avg waiting time

Priority-based Scheduling

- highest priority
- Preemptive & Non Preemptive

Prob of Starvation
Lower priority process never get credit
Solutions → as time progresses, (aging) slowly increase priority of process, not able to execute

Round Robin (RR)

Process allocated to CPU every fixed q. time (preempted thereafter and inserted back)

Usually about 10-100 milliseconds

- very small q → go to FCFS
- large q → lot of context switches; high overhead
- Foreground processes

If n processes in ready queue
mean waiting times $\leq (n-1)q$

- higher avg waiting/turnaround time than SJF but better response
- preemptive

Multilevel Queue Scheduling

Foreground (RR) → handling I/O not to be interactive (interactive)

background (FCFS) → not be interactive Sch. Overhead can be low (batch)

- Solution:
- Ready queue is partitioned into several queues, each has their own scheduling algorithm
 - 2 Scheme for inter-queue scheduling (scheduling among queues)
 - Fixed priority scheduling (foreground then background) (backtracking)
 - Starvation for low priority queues
 - Time slice based scheduling (each queue type gets fixed time (e.g. foreground, background))

Global Scheduling

multi processor

- Symmetrical
 - adv: good CPU utilization, fast to all process
 - disadv: not executable, locking needed in scheduler, high completion time, so increased G.C. time
- selection based on global strategy (FCFS, SJF, etc.)
- when any core become idle, CPU scheduler runs and allocates next process
- some process could execute on different cores at diff time (and serialized process to solve)

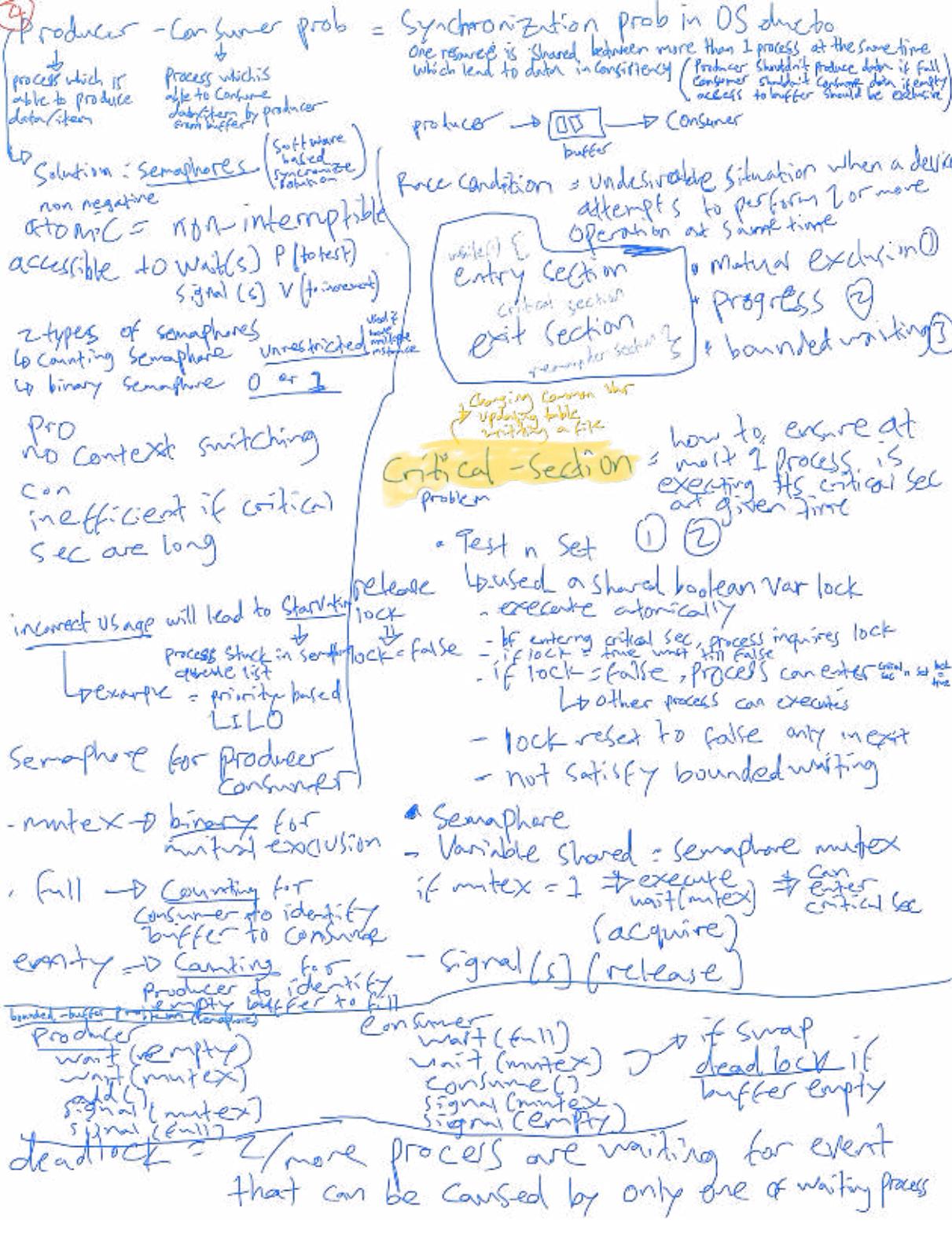
Partitioned Scheduling (AMP)

(asymmetric)

- each process mapped to 1 core
- Separate uniprocessor CPU scheduling for each core

adv:

- easy & simple extension of uniprocessor to multiprocessor scheduling



Readers-writers problem (semaphore)

↳ if writer n (writer or reader) access database

simultaneously \Rightarrow problem

Shared data

readCount \rightarrow track readers in database

- Semaphores

writeX = 1 \rightarrow binary to protects access to readCount

Wrt = 1 \rightarrow binary for mutual exclusion to database

Dining philosophers (semaphore)

↳ number of resources

Semaphore Resource[n]

dining philosophers

wait(RE[i])

wait[(i+1)%n] \rightarrow for nth loop

task 1
g[i] \rightarrow (RE[i])

g[n], (i+1)%n]

to -

↳ initialized as 1 for all resources

for nth loop

deadlock!

\hookrightarrow to prevent:

\hookrightarrow at most 4 philosophers to be hungry at time

\hookrightarrow allow philosopher to pick up only if both chopsticks available

\hookrightarrow use asymmetric solution

\hookrightarrow odd = left \rightarrow right

\hookrightarrow even = right \rightarrow left

disadv of semaphores

\hookrightarrow deadlock or starvation

Spinlock = process spinning in loop cause by busy waiting

writer

wait(wrt);

write()

signal(wrt)

reader

wait(mutex)

readCount ++

wait(wrt)

signal(mutex)

read()

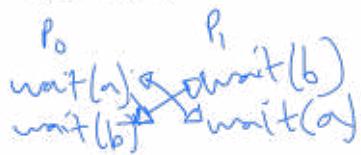
wait(mutex)

readCount --

signal(wrt)

signal(mutex)

⑤ deadlock



Resource-allocation graph

V-Vertices :

$\hookrightarrow P = \text{process}$ ○
 $R = \text{resource}$ instances

E-edges :

\hookrightarrow Request edge $P \rightarrow R$ ○ →

\hookrightarrow Assignment edge $R \rightarrow P$

if graph contain no cycle \rightarrow no deadlock

Contain cycle \rightarrow 1 instance/resource \rightarrow deadlock
 $\hookrightarrow > 1$ instance/resource \rightarrow possible deadlock

deadlock conditions (possible to happen)

- \hookrightarrow mutual exclusion
- \hookrightarrow hold n wait
- \hookrightarrow no preemption
- \hookrightarrow circular wait

Prevent at least 1 of 4 deadlock conditions

- OS ignores deadlock

- System is in Safe State if there exists safe completion route with no deadlock

$\hookrightarrow P.\text{request} \leq \text{available} + \sum_{\text{prev process}} P.\text{hold}$

- Unsafe \rightarrow will Possibly deadlock ($P.\text{request} > \text{available}$)

\hookrightarrow if process releases R before its completion \Rightarrow deadlock

deadlock avoidance (ensure system never enters unsafe state)
↳ Banker's algo \Rightarrow check if satisfaction of resource request would lead to unsafe state

$n = \text{num of process}$

$m = \text{num of resource type}$

$A_{\text{Available}}[i] = \text{Number of instances}$
 $(\text{vector of length } m)$ of Resource R_j

$M_{\text{ax}}[i, j] = \text{number of instances}$
 P_i may request R_j

$A_{\text{Allocation}}[i, j] = \text{number of instances}$
 R_j allocated to P_i

$N_{\text{eed}}[i, j] = \text{number of instances} = M_{\text{ax}}[i, j]$
 P_i need from R_j
to complete its task
 $- A_{\text{Allocation}}[i, j]$

Check need VS Available Process by process

P_i	$\xrightarrow{\text{Allocation}}$	R_j	$\xrightarrow{\text{max}}$	$A_{\text{Available}}$	$\xrightarrow{\text{need}}$
\downarrow					$\xrightarrow{\text{req}}$

every process satisfy \Rightarrow Available = Available + Allocation
if there is sequence \rightarrow safe

Resource - request algo

$\text{Request}_{:,j} = \text{num of instances}$
 P_i want from R_j

Step

① $\underline{\text{Request}_{:,i} \leq \text{need}_{:,i}}$ else raise error

② $\underline{\text{Request}_{:,i} \leq \text{avail}_{:,i}}$ else P_i must wait

③ Pretend allocation to P_i by
 $\text{Avail}_{:,i} = \text{Avail}_{:,i} - \text{request}_{:,i}$
allocations allocation + Request;
 $\text{Need}_{:,i} = \text{need}_{:,i} - \text{Request}_{:,i}$

④ run safety algo
↳ safe \rightarrow resource allocated to P_i
↳ unsafe \rightarrow P_i must wait, restore state

Disk Scheduling

1) FCFS

adv : no indefinite postponement

disadv : doesn't try to optimize seek time

- may not provide + service

- may have wild swings back & forth

2) SSTF

adv - Avg response time of disk
disadv - may cause starvation
- ↑ throughput
- overhead to calculate seek time

3) Scan (1 direction only) (Visit smallest or largest)

adv - high throughput
disadv - long waiting time
- low variance of response time
- Avg response time

4) C-scan (try 1 direction) (Visit 0) (Visit largest)

adv - more uniform wait time
than scan

5) Look & C-look (Same as Scan but not)

Step by Step
from largest
to smallest

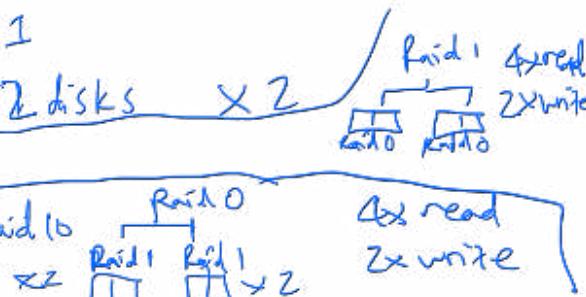
Disk Reliability

Raid 0 multi disks act as 1

Raid 1 (mirroring) write 2 disks X 2

Raid 0+1 (mirror of strip) 01

Raid 1+0 (strip of mirrors) / Raid 10 X 2



- Classic algo (FCFS, SJF, RR) fail cause they don't prioritize deadlines

- Real-time QoS Scheduling (short-term scheduler).

↳ Fixed-priority Scheduling

- priority fixed for each recurrent process

↳ Rate monotonic (RM)

- simple & good for small n popular in real-time CPS industry, Predictability even when over load
- assign priorities based on process periods/minimun release-separation time (T)
- shorter $T \Rightarrow \uparrow$ priority
- Ties broken arbitrarily

con - doesn't always prioritize urgent process

↳ Deadline Monotonic (DM)

- better than RM but cannot change priority across processes
- assign priorities based on deadlines
- shorter D = \uparrow priority
- Ties are broken arbitrarily

↳ Dynamic-Priority Scheduling

↳ Earliest Deadline first (EDF)

- more powerful than RM & DM but harder implementation
- assign priority based on process instance deadline
 - instance with shorter deadline $\Rightarrow \uparrow$ priority
 - $\nabla \neq D$
- Ties are broken arbitrarily

Virtualization = technique that uses software called Hypervisor (VM manager) to create abstraction of HW

↳ HW divide into multiple VM, each VM runs own OS (guest OS)

↳ Function:

- allows more efficient utilization of HW
- fast, effective, agile execution environment
- future IT paradigm

- key tech driving cloud computing

- cost efficiency of platform as service

- enable delivery of platform as service

↳ Challenges:

- require hypervisor layer
- real-time CPS require diff solutions

↳ Levels:

- full virtualization (VMware ESXi, Microsoft Virtual Server)
- para-virtualization (Xen, Virtuware, Kvm)

- para-virtualization (Xen, Virtuware, Kvm)
↳ VM doesn't virtualize hardware to implement parts of
HW instruction set

• function of Hypervisor \rightarrow usually 2 orders of magnitude smaller than general OS

↳ Creation & management of VMs

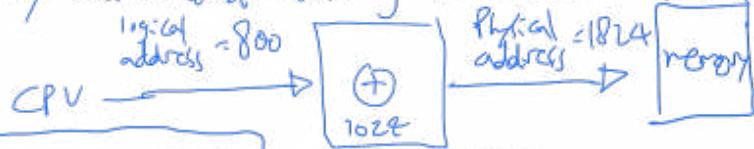
↳ Communication between VMs

↳ VM Migrations

- 7) Compiler = source code to object code
Linker = combine object modules to resolve references
Loader - create process image
- allocate memory
- load process image to allocated memory

memory allocated during compile time is called static memory
(cannot increase / decrease size of memory allocated)

memory allocated during execution time is called dynamic memory



used in process image [relocation register]

absolute address format (in compiletime/Load time binding)

if address generated by CPU < base \Rightarrow error
 \geq base + limit

else memory address = address generated by CPU

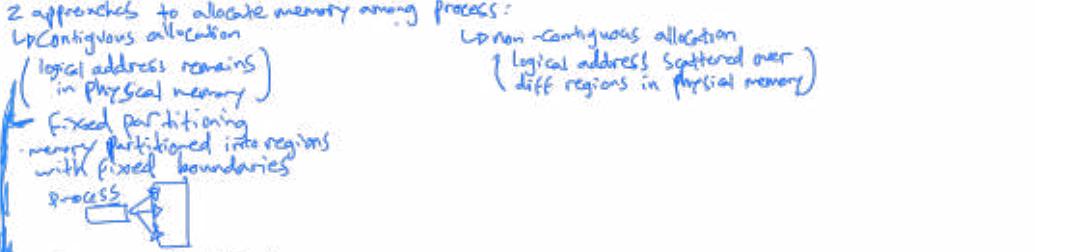
relative address format (in execution time binding)

if address generated by CPU \geq limit \Rightarrow error

else memory address = address generated by CPU + base

- address space = all address accessible by a process
- logical address = address used in code, generated by CPU
- physical address = address used to access physical memory (seen by memory unit)

2 approaches to allocate memory among process:



Dynamic Partitioning

- Hole = block of available memory
- Various size hole scatter in memory, new process get assigned appropriate hole
- OS maintain info about
 - allocated partition
 - free partitions (hole)

Dynamic Storage-allocation Problem:

how to satisfy request of size n from list of free holes:-

- First fit \rightarrow allocate first hole that is big enough
- Best fit \rightarrow allocate smallest hole that is big enough (least overhead)
(produce smallest leftover hole)
- Worst fit \rightarrow allocate largest hole
(produce largest leftover hole)

Fragmentation

\hookrightarrow External = enough total memory space exists to satisfy request but

it is not contiguous

Happen outside a partition

\hookrightarrow reduce by compaction

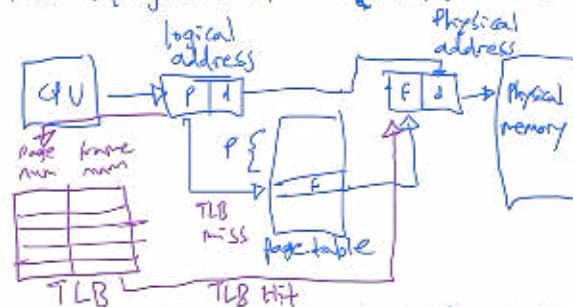
\hookrightarrow shuffle memory contents to place free memory together in 1 large block

\hookrightarrow Possible only if re-locatable address format is used in process image
n binding done during execution time

\hookrightarrow Internal = allocated memory slightly larger than requested memory

• Paging

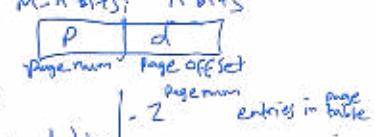
- frames = divide physical memory into fixed-sized blocks
- pages = divide logical memory into blocks of same size
- page table = use for translate logical to physical addresses
- external fragmentation eliminated
- internal fragmentation possible (last page may not occupy the entire frame)



page size = 2^n byte

logical address space = 2^m byte

Number of pages = 2^{n-m} byte
m-n bits; n bits



- Page table base register (PTBR) points to page table

- Page table length register (PTLR) indicates size of page table

- effective memory access time = 2^μ

 | for PageTable &
 | for data/instruction
 |
 | To memory cycle
 | time

→ Can be reduce by use of a fast lookup HW Cache called associative req or
translation look-aside buffer (TLB)

- TLBs lookup = E time unit

- Hit ratio = α = % of time TLB hit

- Effective Access Time (EAT) = $(\mu + \varepsilon)\alpha + (2\mu + \varepsilon)(1-\alpha)$
 $\Rightarrow (2-\alpha)\mu + \varepsilon$

1 byte = 8 bits

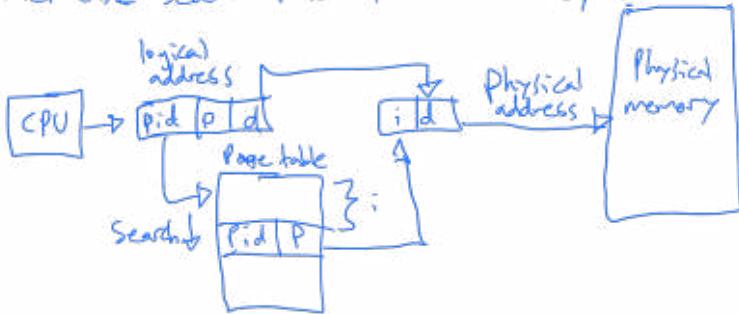
1 Kbyte = 2^{10} byte = 1024 bytes

1 Mbyte = 2^{20} byte

1 Gbyte = 2^{30} byte

1 Tbyte = 2^{40} byte

- Inverted Page Table
- Single table with 1 entry for each physical frame <Process-id, page no>
logical address <Process-id, page no, offset>
- increase search time: table sorted by physical address but lookups on logical address



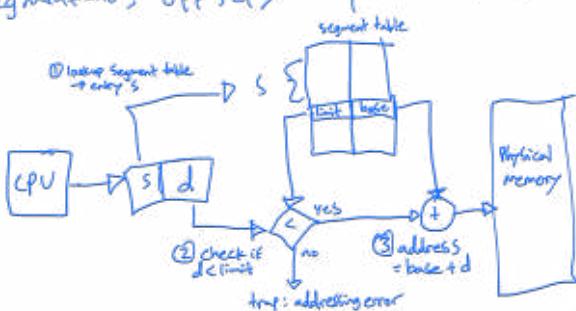
Segmentation

↳ memory management scheme that break program up into its logical segments n allocating space for each segments

↳ can be variable size

Address Translation
<segment-no, offset>

base = contains starting physical address
limit = specifies length of the segment



STBR (segment-table base reg) → points to segment table's location in memory
STLR (segment-table length reg) → indicates num of segments used by a program
Suffer external fragmentation!