**Hot Atom Damage (HAD) Limited TDDB Lifetime of Ferroelectric Memories** Muhammad Masuduzzaman\* and Muhammad Ashraful Alam+   
\*Email: masuduzzaman@ieee.org, +Email: alam@purdue.edu, Phone: 765-494-5988, Fax: 765-494-2706 Department of ECE, Purdue University, West Lafayette, IN 47907, USA

**Abstract**

The AC/DC dielectric lifetime (TDDB) of ferroelectric materials have traditionally been interpreted similar to a classical gate oxide. In this paper, we demonstrate a fundamentally different kinetic mechanism of damage involving hot atom (HAD) in ferroelectric materials to interpret the severe reduction of AC lifetime, coupled with the counterintuitive increase in the Weibull slope. We show that, beyond a critical operating condition, the atoms at the domain walls are heated by the AC field, as they shuttle across the double-well energy landscape of such materials. An elegantly simple analytical model (i) interprets critical TDDB experiments, (ii) suggests strategies (e.g., pulse shaping) to minimize HAD significantly, and (iii) predicts device lifetime at arbitrary operating conditions.

**Introduction**

Dielectric materials with memory functionalities have recently gained interest in much broader area of applications. This includes emergent non-volatile memories [1], novel phase change switches [2], and neuromorphic logic applications, such as adaptive oxide electronics [3]. Ferroelectric, being one such memory material, is of no exception, and its potential use has extended beyond the memory (FeRAM) [4] and actuators [5], to the gate-stack of transistors (NCFET) [6]. Although the reliability of the switchable polarization of ferroelectric materials have been studied extensively, and characterized by fatigue, imprint, endurance, etc. [7, 8], the stress induced leakage current (SILC), the time dependent dielectric breakdown (TDDB) and failure distribution of ferroelectric materials have only been characterized and interpreted traditionally, as if it was a classical gate oxide [9-11].

In this paper, we compare the DC and AC TDDB lifetime of ferroelectric capacitors, and find that the traditional breakdown theories for gate oxides are not sufficient to explain the AC TDDB characteristics of materials with double-well energy structure. We demonstrate that the defect generation is accelerated by the hot atom at the domain walls in ferroelectric materials generated under certain operating conditions, which explains critical TDDB experiments including the reduction of AC lifetime (Fig. 3,7,8), and a corresponding increase in the Weibull slope (Fig 9). We also propose the theoretical model which combines the defect generation theory of classical dielectrics and the phase change theory of ferroelectric materials (Fig. 10). The model suggests the use of pulse shaping to reduce

such damage which has also been verified experimentally (Fig. 12). Finally, we discuss on the TDDB lifetime for an arbitrary pulse shape by decoupling the AC and the DC damage modes (Fig. 13).

**Device Characterization**

The structures studied are metal-ferroelectric-metal capacitors [12] with the ferroelectric materials composed of industrial, state-of-the-art PZT (Pb(Zr,Ti)O3) with grain-size of ~100nm (Fig. 1c). The samples were either 70nm or 450nm thick. For 70nm samples, the polarization-voltage (P-V) and capacitance-voltage (C-V) hysteresis characteristics are shown in Fig. 1(a,b). Conductive AFM measurements show that grain boundary (GB) regions have (i) reduced thickness (ΔTOX~10nm), and (ii) higher leakage (IGB) due to higher pre-existing defects (Fig. 1c). To decouple the relative contributions of ΔTOX vs. pre-existing defects in IGB, we created a square trench on the oxide using an AFM tip (Fig. 1d). Even for a 20nm-deep trench (i.e., TOX� 70 � 20 �50nm), the oxide leakage (IOX) remains smaller than IGB outside the trench region (Fig. 1e), confirming the importance of pre-existing defects in defining GB leakage.

**DC Lifetime is Nothing Unusual**

Once the oxides are stressed at DC, we find that the TDDB characteristics are very similar to that of a traditional oxide: the TDDB lifetime (TBD) follows a power law with voltage, with a temperature-dependent exponent which implies activated defect generation (Fig. 2a). Indeed, at relatively low frequencies, the lifetime scales perfectly with duty cycle (�), indicating the absence of any relaxation (Fig. 2b). However, the Weibull slope (���) is remarkably small (~1.5) for a 70 nm thick capacitor. We have recently demonstrated that [13] the (DC) BD-spot is always located within the GBs (Fig. 4) and the low ��� reflects ‘electrical thinning’ of GB region due to the large number of pre-existing defects.

**The Puzzle of AC Lifetime and Hot Atom Damage**

Once the stress is switched to an AC pulse, TBD decreases dramatically, see Fig 3.For most reliability phenomena (e.g., TDDB, BTI, SILC), typically the AC lifetime is either larger (with recovery) or equal (when recovery is absent) to the derated DC lifetime of the capacitor [14-16]. In ferroelectrics, however, the AC TDDB lifetime is orders of magnitude *smaller* than that at DC bias (Fig. 3), indicating the presence of some additional damage

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mechanism during the AC stress. We emphasize that, here we refer to the catastrophic *dielectric breakdown*, which is different from *fatigue* (loss of switchable polarization) at AC stress[8].

A fundamental difference between a traditional dielectric and a ferroelectric material is the double-well energy landscape of the latter [17], as a function of polarization (Fig. 5). For a complete switching of the memory state, the applied voltage (����) must exceed the coercive voltage (VC) so that the center (Zr/Ti) atom can cross the potential barrier. Hypothetically, if the AC TDDB damage is related to the repeated crossing of this barrier, the damage should be proportional to the *number* of the switching cycles (NCY). Fig. 6 confirms that it is indeed true, i.e., the stress induced leakage current (SILC) is determined by NCY, regardless the frequency (�).

At very *low* � (at 50% d.c.), however, NCY is small as compared to the effective DC stress time. Hence we notice a shift in lifetime towards DC lifetime (Fig. 7), since the damage mode is dictated by the DC stress at such condition. To isolate the contributions of cyclic (switching) stress, we use an extremely narrow pulse train so that the DC damage is avoided. Remarkably, as the **second** confirmation of the HAD theory, the lifetime now simply scales linearly with frequency.

**Third**, to find the effect of pulse amplitude, we use different pulse shapes as in Fig. 8a. As mentioned, for repeated switching, the AC pulse must include both ���levels (case iv, v). Otherwise, for a non-switching AC (case ii, iii), the center polarizing atom will just oscillate within a single well, effectively behaving like a traditional high-k dielectric under AC stress. For such cases (ii, iii), the lifetime is comparable with DC stress (Fig. 8b). However, as soon as the pulse becomes switching (case iv, v), there is a sharp decrease in lifetime, again relating the damage to the repeated crossing of atom across the barrier.

Theoretically, during polarization switching (���� ���), the energy gained by the atoms at the domain walls, not only allows it to cross the central energy barrier, but also leads to an overshoot in the *local polarization*, until the energy is dissipated to the surroundings (Fig. 10). These transient polarization overshoots stretch the bonds around the domain walls and reduce the thermal activation energy [18] required for bond breakage (Fig. 11), and thus reduce TBD.

**Finally**, unlike DC breakdown which is localized at the GB (with the consequence of low Weibull) [13], AC switching of atoms is inefficient at the GB edges due to the presence of defects. Rather, the switching of atoms occurs at the *domain walls* within the grains. Since the domain walls propagate across the grain regions during switching, the defect generation also happens along with the domain wall motion throughout the grain regions (Fig. 9a). As the pre-existing defect density at *grain* is considerably smaller than that of the GB, we see a dramatically sharper Weibull slope for an AC stress (Fig. 9b). The results are supported by Monte Carlo simulation [13].

**Optimization of Device Operation**

To minimize HAD, the AC pulse must be shaped to reduce the polarization overshoot. For example, one would expect a lower HAD using a pulse with slower transitions time. This is confirmed in Fig. 12a where the lifetime increases by *order of magnitude*, if the pulse has an increased transition time. In fact, the slower transition of the atoms allows lesser polarization overshoots and reduced bond stretching (increased lifetime). Second, since the polarization is maximized at resonant frequencies which would lead to increased HAD (Fig. 12b), this situation must be carefully avoided.

Finally, since the DC and the switching damages are dictated by independent stress modes, one can characterize the Weibull distributions separately to find the lifetime for an arbitrary shape of the pulse (�, �). The final lifetime will be governed by the prorated DC and switching lifetime based on � and �, respectively, as shown in Fig. 13.

**Conclusion**

We have established a new degradation mechanism associated with the hot atom damage through a series of probing experiments, and offered an intuitively simple and consistent theory to interpret the results, and to establish TDDB lifetime of ferroelectric memories as a function of optimized bias, pulse shape, and frequency.

**Acknowledgement**

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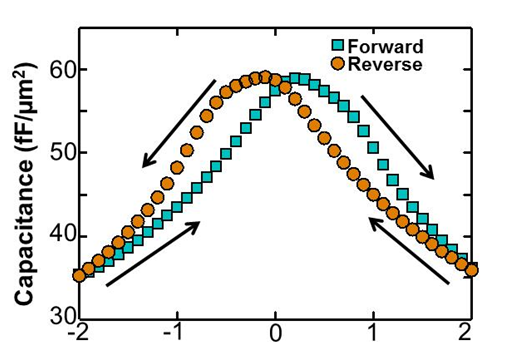
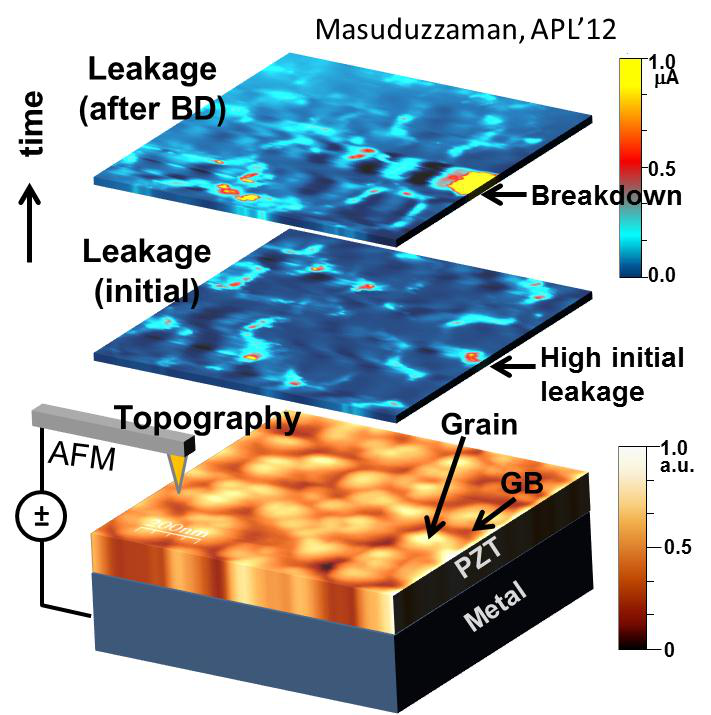
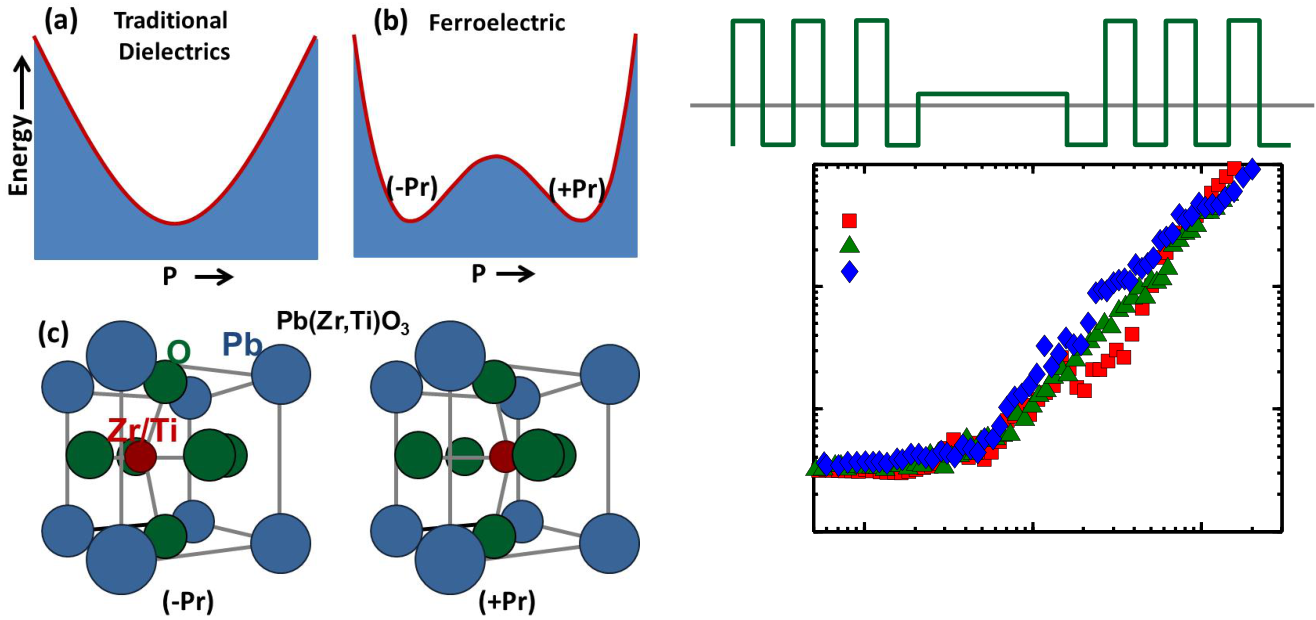
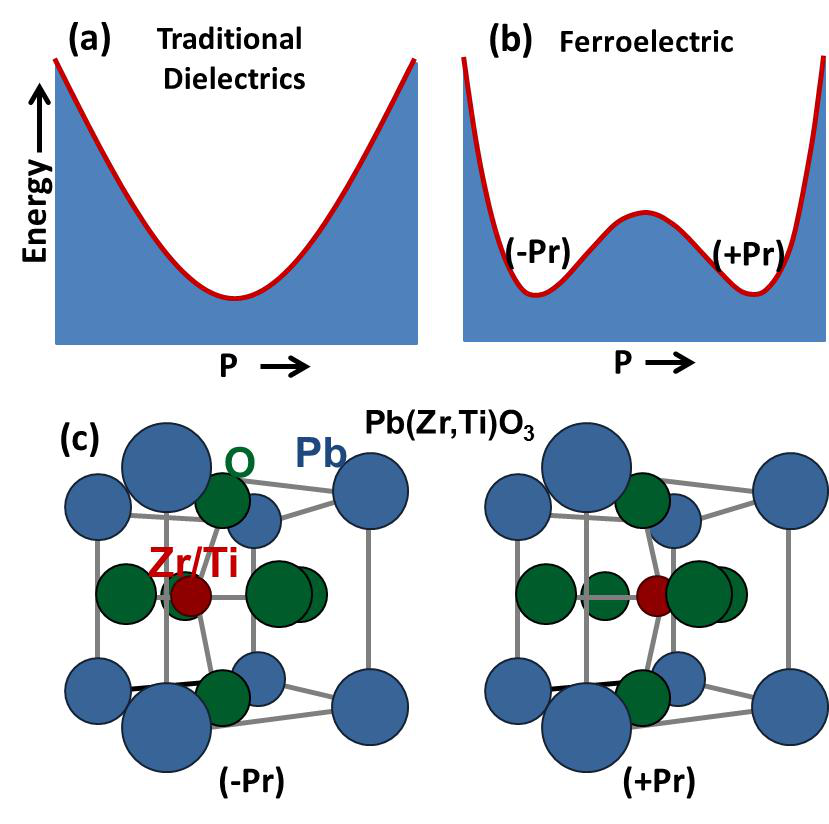
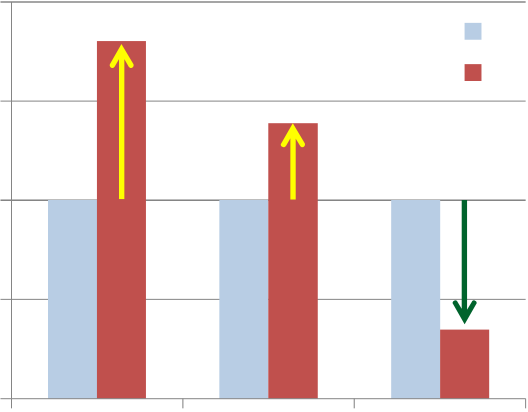
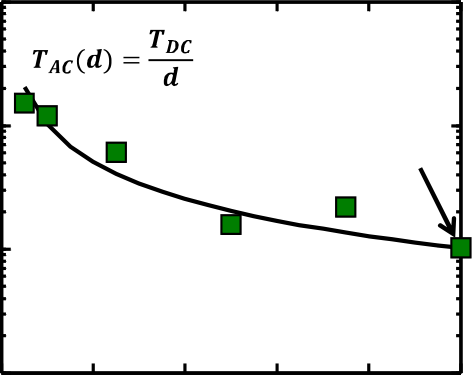
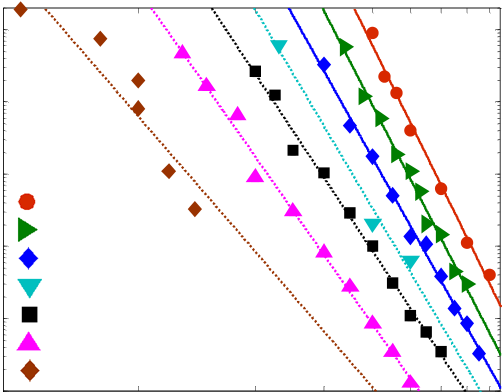
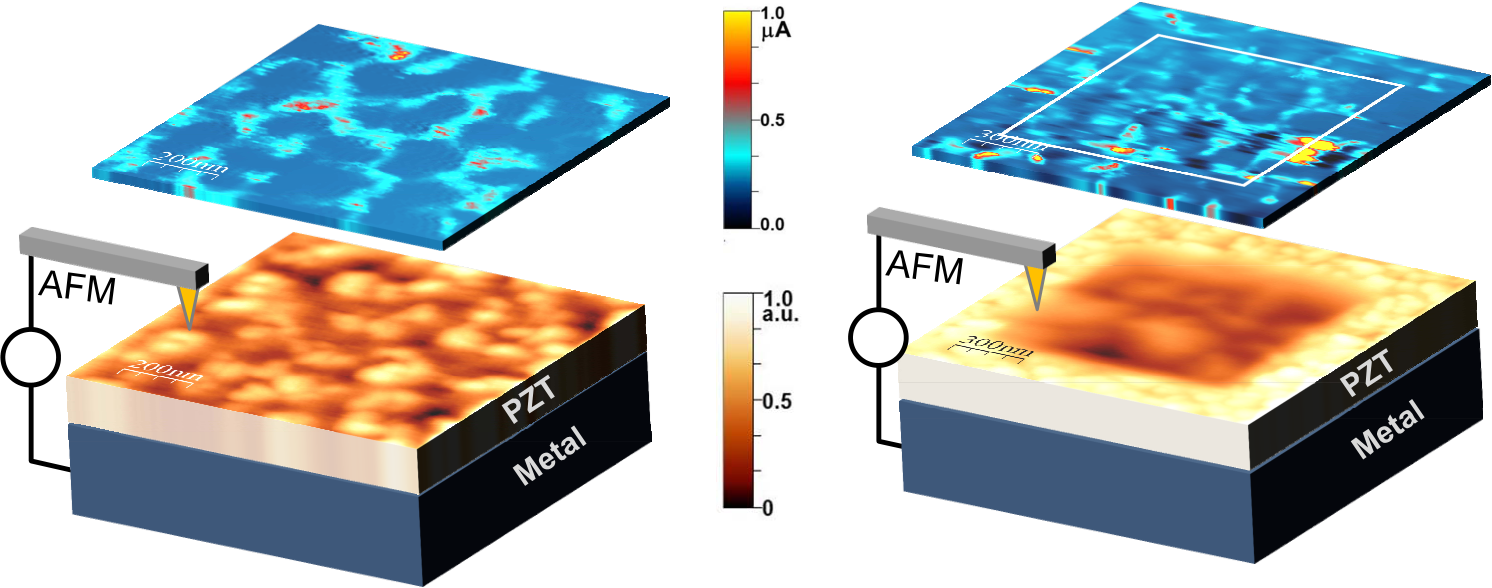
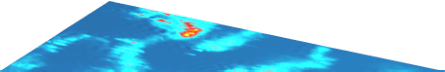
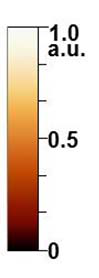
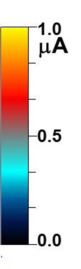
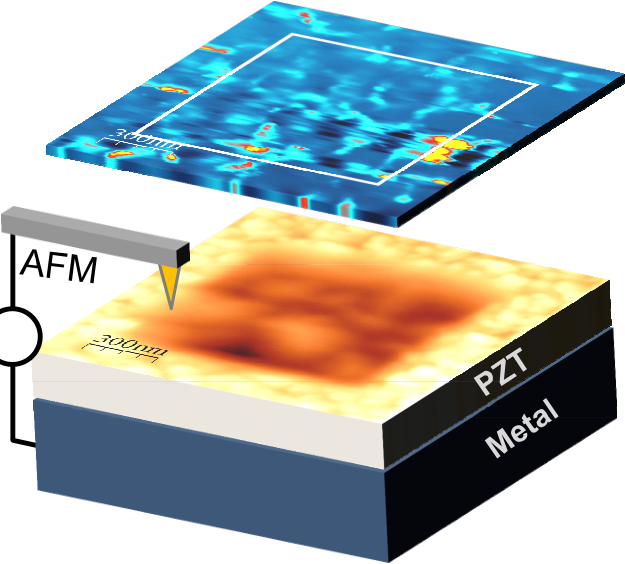
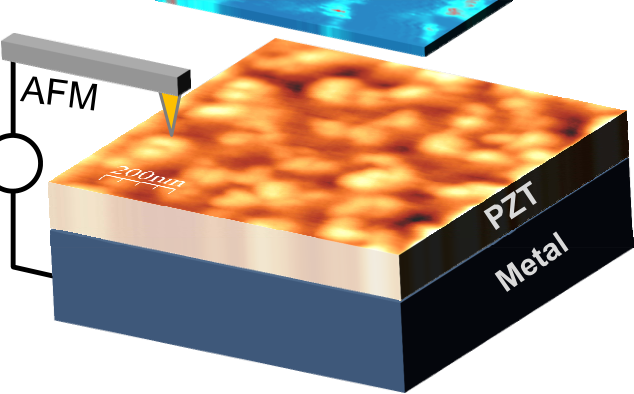
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| --- | --- | --- | --- | --- | --- | --- |
| **(a)****(b)** | | | | **(d)** |  | Fig. 1: Initial characterizations: |
|  |
| (a) The polarization-voltage (P- |
|  | | | | V) [12] and (b) the capacitance- |
| voltage (C-V) hysteresis curves |
| are shown for a 70nm PZT |
| sample. (c) The topographic |
| (bottom) and the leakage (top) |
| maps are taken by scanning the |
| oxide surface by a conductive |
|  | |  | |
| AFM tip with an applied bias. |
| **(c)** | **Leakage** | | **(e)** | **Leakage** | The leakage current shows a |
| clear correlation with the GB 2  regions. (d) A 1μm trench |
| (20nm depth) is created by |

repeated scanning of an AFM   
tip. (e) The current map shows

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **±** | **Topography** | **±** | **Trench** | **Topography** | that IGB, even at the outside of | | | |
| the | trench | (70nm | oxide |
| thickness), is higher than IOX | | | |
| (non-GB regions) within the | | | |
| trench (50nm oxide thickness), | | | |

implying that the influence of

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **TBD (a.u.)** | 105 | **T (oC)** | | 10 | 20 | 30 40 | **(a)** | 105 | 0.2 | 0.4 | 0.6 | **(b)** | | **TBD /TBD (DC)** | **100** | pre-existing defects is greater | | | | |
| than | ΔTOX | in | GB | leakage |
| current. | | | | |
| **[Ref-14]** | | | **DC** | |
| 104 | 104  **TBD (a.u.)**  103 | **10** | **[Ref-15]** | | | **AC** | |
| **TDC** | |
| **This work** | | | | |
| 103 |
| **1** |
| **50** | |
| 102 | **75** | |
| **100** | |
| 101 1 | **125** | | 50 60 7080 60 | 0.8 | 1.0 | **0.1** | **TDDB (SiO2) NBTI (SiO2) TDDB (Ferro)** | | | | |
| **150** | |
| 102   0.0 |
| **200** | |
| 100 |  | **250** | **0.01** |
| **Voltage (V)** | | **Duty Cycle (d)** | |

Fig. 2: (a)The lifetimes for DC stress (TOX=450nm) at different voltages and temperatures (T) are shown. Once fitted with a power law, the voltage exponent decreases with T, indicating thermally activated defect generation. (b) The DC and the pulsed AC lifetime are shown (symbol) as a function of duty cycle (�). The line is drawn by scaling the DC lifetime with �.

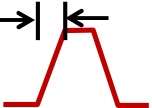
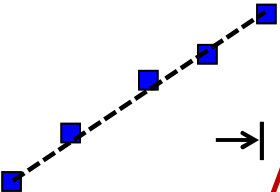
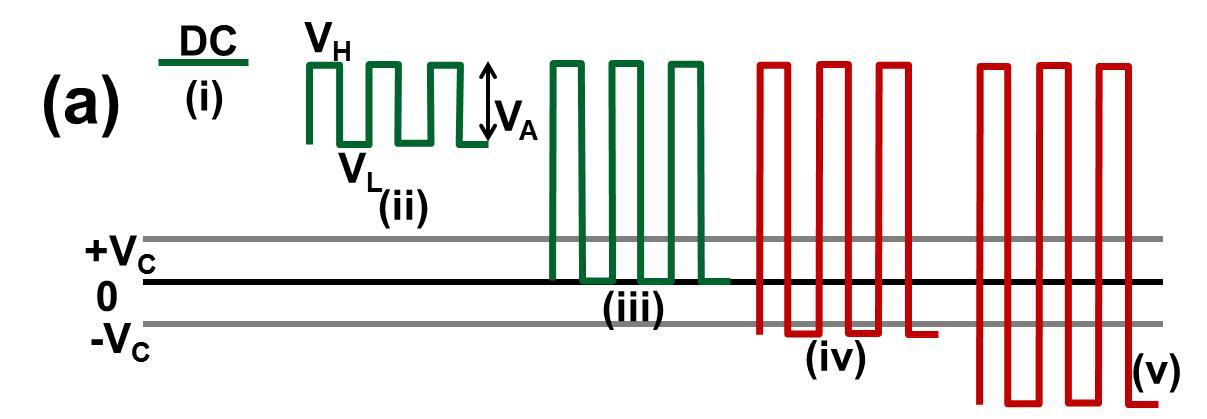
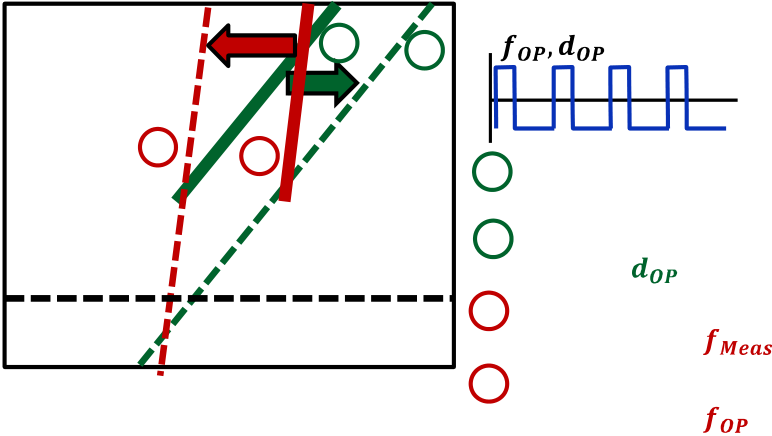
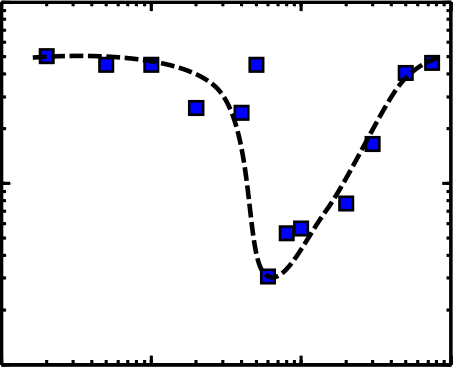
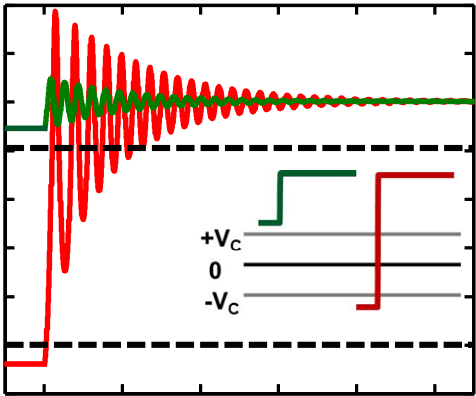
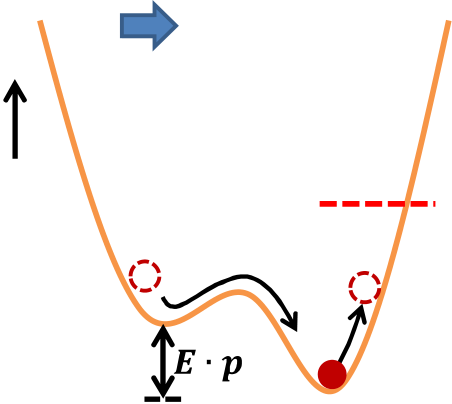
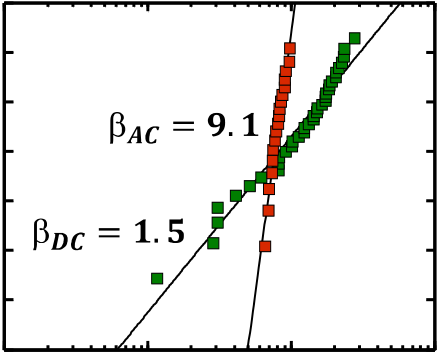
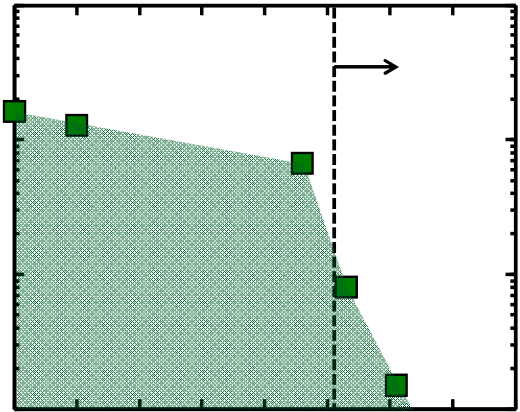
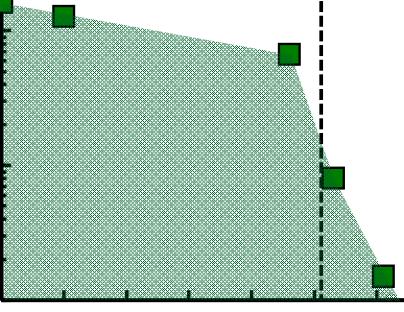
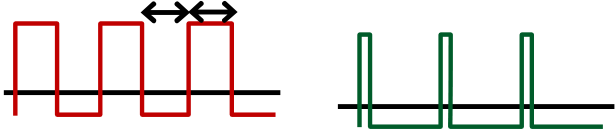
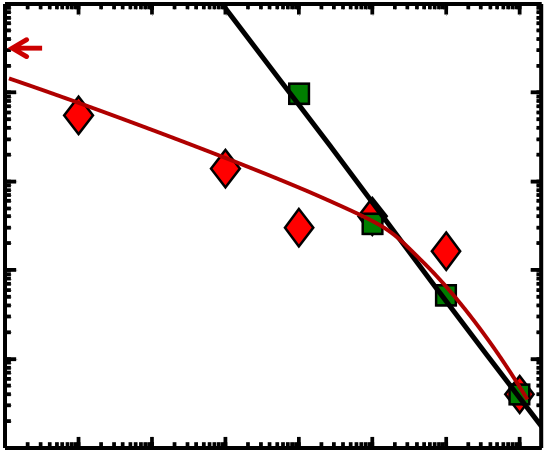
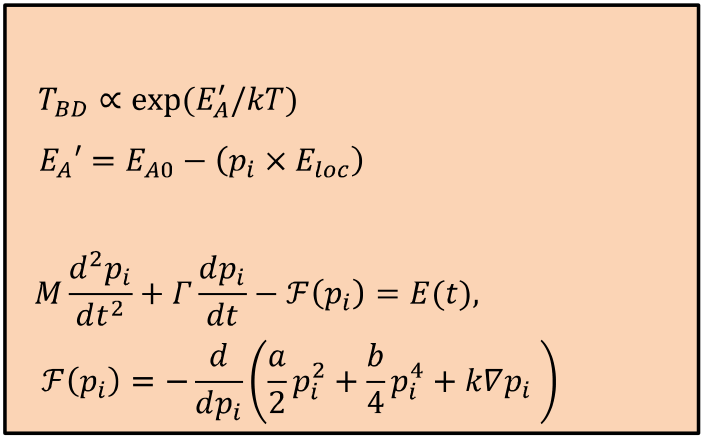
Fig. 3**:** Traditional dielectrics typically have higher AC lifetime than DC lifetime. In contrast, Ferroelectric have much lower AC lifetime.

|  |  |  |
| --- | --- | --- |
| **Stress Pulse** | **Interrupted** | **Stress Pulse** |

**to measure**

|  |  |  |  |
| --- | --- | --- | --- |
| **leakage** | | 108 | **0V** |
| 10-9**Frequency (kHz)** | |
| **Leakage (a.u.)**   10-10  10-11 | **2**  **10**  **20** |
| 10-12 | 106 107  **No. of Cycle, NCY** |

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Fig. 4: Our previous study [13] shows that during | Fig. 5: Potential energy landscape as a function of | Fig. 6: The leakage evolution (a proxy of | | | | | |
| polarization (P) for (a) traditional dielectric, and (b) | defect generation) at a sense voltage | | | | | |
| a ferroelectric material. The later has a double- | shows almost identical results as a | | | | | |
| well characteristic that enables two distinct states | function of the number of stress cycles. | | | | | |
| DC stress, breakdown always occurs through |
| (���), as shown in (c) for PZT. The center Zr/Ti | This indicates that the internal damage is | | | | | |
| the GB regions due to large pre-existing defects |
| atom needs to get a certain energy (represented | dictated | by | the | switching | of | the |
| therein. This also causes very low Weibull slope |
| at DC stress. Later (Fig. 9), we will compare this | by the coercive voltage in the applied potential) to | polarization at this frequency range. | | | | | |
| overcome the energy hump. | IEDM13-568 | | | | | |
| with the Weibull distribution of AC stress. |
| 21.4.3 |



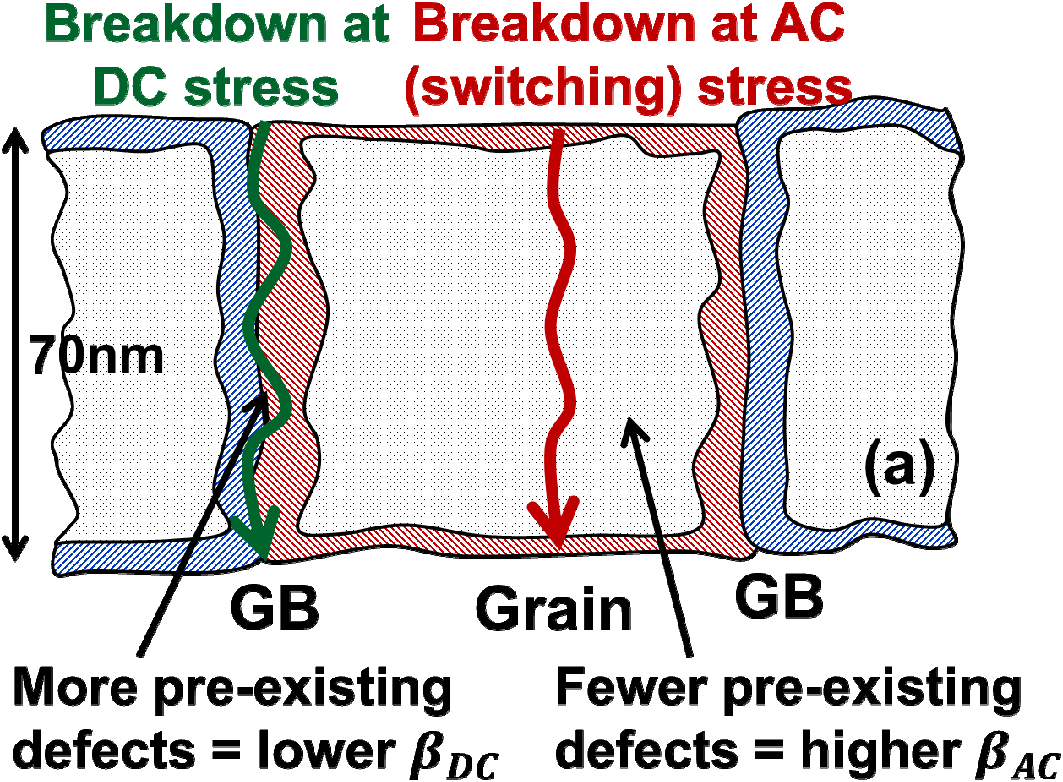
**105**

**104**

**103**  **Square wave**   
 **2\*DC Lifetime**   
 **(V=VH)**   
 **VL**

**Pulse train**   
**(500ns width)**

**slope ~1**   
 **(b)** 105

104 **(i)**  **VH =4.6V**  **(ii)**  **(iii)**   
 **VL<-VC** 

2   
 1 **AC 50% duty cycle**  **Pulse train, very**   
**VH**  **low duty cycle**   
 **tL**   
**tH**  **0V**

**TBD (a.u.)**   
 **TBD (a.u.)**   
 **W=ln{-ln(1-F)}**   
 **102**  0 **(switching)**

103 **(iv)** -1   
**101** -2 **DC**

**VH =4.6V, VL=-1.5V**  **(v)** -3   
**100**  **100**  **102**  **104**  **106**  102 0 2 4 6 8 -4 **(b)**  **Frequency (Hz)**  **Pulse Amplitude, VA (V)** -5101 102 103 104

Fig. 7: Two waveform types- square wave and Fig. 8: For the five different waveforms (a), the corresponding lifetimes are shown in (b). Case Fig. 9: (a) Since switching is an intrinsic **TBD (a.u.)**   
pulse trains- are used to isolate the DC and AC (i) is DC. Although cases (ii)-(iii) are AC, but they property, the corresponding damage, and damage. For narrow pulse train, the lifetime do not crosses both ���, and hence can not breakdown occurs throughout the grains of scales with � (only depends on the number of switch the memory states, and the lifetime is the material. This is in contrast to the DC stress where we have seen GB dominated cycle). But for a square wave, as � goes down,   
 comparable to DC. Only for cases (iv)-(v), the the DC stress becomes dominant, and the memory states switch in every cycle. The lifetime breakdown (Fig. 4). (b) Such fact is reflected lifetime starts to merge with the derated DC is drastically reduced in these cases. in the significantly sharper Weibull slope in AC breakdown. lifetime.

**(a)**   
 **Electric field**

**breaking**  **energy**  **Bond**  

1   
2 **Switching**

**Non-switching**   
 **(b)**  Breakdown … (1)

… (2) **) ( Normalized**   
**Energy**   
 **Polarization**   
 **Previous**  Polarization   
 **location**  **Transient**  0 … (3) **overshoot**  
 -1

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| **Central** | **New** | 0 | 1 | 2 | 3 | 4 | 5 |
| **equilibrium** |
|  |
| **atom(Ti/Zr)** |  |  | **Time (a.u.)** | | |  |  |
|  |

Fig. 10: (a) Once a bias (� ��) is applied, the center atom gets enough energy to cross the energy barrier in the middle. Once it crosses the barrier, the energy must be dissipated so that the atom settles in the new equilibrium. (b) Simulation shows that, for a switching pulse (inset, red), there is a significant transient overshoot in local polarization before this excess energy is dissipated. The activation energy for bond breakage is reduced during this time (see Eq. (2) in Fig. 11), which leads to lower AC lifetime.

Fig. 11**:** Two sets of equations are combined to model the breakdown as a function of switching. In Eq.(1), EA is the activation energy for bond breakage, which reduces with the local polarization, �� as in Eq.(2) [18]. To solve the polarization dynamics for an applied external field (E(t)), Eq.(3) is used, where �����is the internal field originating from the double-well potential profile, characterized by *a*, *b*, *k* [17]. �, � are the inertial and damping coefficients.

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| 109 **Lifetime in NCY (a.u.)**   108  107  106 | |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | | **(a)** | | | | | | | | | | | | | | | | | | | | | | | | **Tr** | | | | | | | | | | | | | | | | | | | | | | | |  | | | | | | | | | | | | | | | | | | | | | | | | | | 108 **Lifetime in NCY (a.u.)**   107  106  103 | **(b)** | | **Actual Input pulse** | | | | | | |
| **W=ln{-ln(1-F)}** | **4** | **1** | **3** | **(** | | **)** |
| **Resonance** | | **2** | | **1** | | **Measured lifetime** |
| **for DC stress** |
| **3** | |
| **Calculated lifetime** |
| **Failure criterion** | |
| **for DC at** |
| **2** | |
| **Measured lifetime due** |
| **to switching at** |
| 102 | 103 | 104 | 104 105  **Frequency (Hz)** | 106 | **TBD** | | | **4** | | **Calculated lifetime due** | |
| **to switching at** | |
| **Transition time, Tr (ns)** | | |
| Fig. 13**:** The scaling of lifetime with duty cycle (�� for DC stress (Fig. 2b), and with frequency (�� for switching stress  (Fig. 7), allow us to calculate the lifetime for a input pulse  with arbitrary ���and ���. Lines (3) and (4) are calculated by shifting lines (1) and (2) by �� ����� and �� ������/����, respectively. Based on the fail-fraction criteria, the ultimate  lifetime could be determined by both of the DC and the  switching stress, and needs to be optimized accordingly. | | | | | | |
| Fig. 12**:** (a) The AC TDDB lifetime is shown for different transition/rise time (��) of the switching pulse. As �� increases, the lifetime increases significantly with a slope close to unity in a log-log plot. Clearly, the slower switching causes higher damping of the switching energy and lesser HAD results. This offers an effective control knob of the AC TDDB lifetime. (b) The polarization overshoot is maximum around resonant frequency and a notch in lifetime is observed. This condition must be avoided in the design of the operating pulse. | | | | | | |
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