

ECE 284 FA25 Final Progress Report - Team Velociraptor

Item	Current Status	Status during Poster Presentation	Note (location in Repo, also found in README.md)
Part1	Complete	Complete	software/part1 hardware/part1
Part2	Complete	Complete	software/part2 hardware/part2
Part3	Complete	Complete	software/part3 hardware/part3
Alpha 1	Complete	Complete	Activation function reconfigurability in SFP using LUTs for computation hardware/part1_sfp
Alpha 2	Complete	Complete	Physical Pynq FPGA board systolic array mapping for weight stationary, 4-bit quantization hardware/part1_pynq
Alpha 3	Complete	Complete	VGG16 QuantConv2d layer activation quantization sensitivity study (4-bit to 2-bit) software/part4
Alpha 4	Complete	Complete	Synthesis and PnR Flow using OpenROAD hardware/openroad_syn_pnr/part1