MIPS Load & Stores

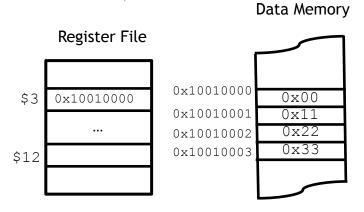**lw rt, offset(rs)**

address: offset + R[rs]
$rt <- Memory[Addres]
Loads a word of 4 bytes

lw $12, 0($3)

Register File

Data Memory

$3	0x10010000	0x10010000	0x00
	...	0x10010001	0x11
		0x10010002	0x22
$12		0x10010003	0x33

lbu rt, offset(rs)

address: offset + R[rs]
$rt[0:7] <- Memory[address]
$rt[8:31] <- 24'b0

lbu $12, 2($3)

Register File

Data Memory

$3	0x10010000	0x10010000	0xF1
	...	0x10010001	0xF1
		0x10010002	0xF2
$12		0x10010003	0xF3