7.13 Concluding remarks

(Original section[1])

This appendix introduces the basics of logic design. If you have digested the material in this appendix, you are ready to tackle the material in COD Chapters 4 (The Processor) and 5 (Large and Fast: Exploiting Memory Hierarchy), both of which use the concepts discussed in this appendix extensively.

Further reading

There are a number of good texts on logic design. Here are some you might like to look into.

Ciletti, M.D. [2002]. *Advanced Digital Design with the Verilog HDL*, Englewood Cliffs, NJ: Prentice Hall.
A thorough book on logic design using Verilog.

Katz, R.H. [2004]. *Modern Logic Design*, 2nd ed., Reading, MA: Addison-Wesley.
A general text on logic design.

Wakerly, J. F. [2000]. *Digital Design: Principles and Practices*, 3rd ed., Englewood Cliffs, NJ: Prentice Hall.
A general text on logic design.

(*1) This section is in original form.

⚑ **Provide feedback on this section**