H3nghean

**May 2016 – August 2016**

**IOT application in collecting sensors signal measurements**

Using Arduino KIT for (collect sensor data, monitor, feedback control) over the internet using the ThingSpeak server. (have finished products)

**November 2016 – August 2017**

**Human detection by Histogram of Oriented Gradient**

Objectives: performing a hardware design of the operation unit as a theme of “human detection by HOG”.

* HOG software simulation in Matlab, C/C++ (not OpenCV).
* Designing a HOG hardware architecture and simulating a part in VHDL.
* My partner and I wrote a paper about improving HOG algorithm performance.

**My objective becomes a digital design engineer.**

**HUY HUNG HO**

**INTERESTS**

**2014 - 2018**

**VNU University of Engineering and Technology (VNU-UET)**

The senior student in Faculty of Electronics and Telecommunication. Key objects: Digital Signal Processing, C/C++ programming, Digital Design, Real-time Embedded System, Integrated Circuit Design...

Prizes: good student scholarship, 3rd prize in the Faculty-level Student Research Contest. (The academic year 2016-2017)

Cumulative GPA: **3.11** / 4.0

**EDUCATION**

**ENGLISH**

**LINUX**

**C/C++**

**VHDL**

**SKILLS**

**PROJECTS**

**WORK EXPERIMENT**

**March 2016 – Present**

**Internship at VNU Key Laboratory of Smart Integrated Systems (SISLAB)**

Researching about the low power for multi-core chip.

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**INFORMATION**

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