

7. Schematic Diagrams

7-1 All block Diagram -----	7-2
7-2 Power-----	7-3
7-3 S.M.P.S (S.M.P.S PCB)-----	7-5
7-4 Anlog Audio (2ch)/Video (CVBS, Component)(Main PCB)-----	7-6
7-5 NAND Flash, FlexOneNand (Main PCB) -----	7-8
7-6 HDMI Transmitter (SIL9134)(Main PCB) -----	7-9
7-7 7440 Boot Strap Option (Main PCB) -----	7-10
7-8 7440 EBI_ADDR, EBI_DATA (Main PCB) -----	7-11
7-9 7440 Clocks, BBS (Main PCB) -----	7-12
7-10 7440 Power, Decoupling (Main PCB)-----	7-13
7-11 DDR2 BANK0, BANK1 (Main PCB)-----	7-14
7-12 Front Micom (Main PCB)-----	7-15
7-13 Ethernet, USB, S-ATA, P-ATA (Main PCB) -----	7-16
7-14 GPIO Block (Main PCB)-----	7-17
7-15 Main Power (Main PCB)-----	7-18
7-16 BCM7620 (F/E)(Main PCB)-----	7-19
7-17 Front Key (Front PCB)-----	7-20
7-18 Touch Key PCB (Touch PCB)-----	7-21

Note

- For schematic Diagram
- Resistors are in ohms, 1/8W unless otherwise noted.

Special note :

Most semiconductor devices are electrostatically sensitive and therefore require the special handling techniques described under the "electrostatically sensitive (ES) devices" section of this service manual.

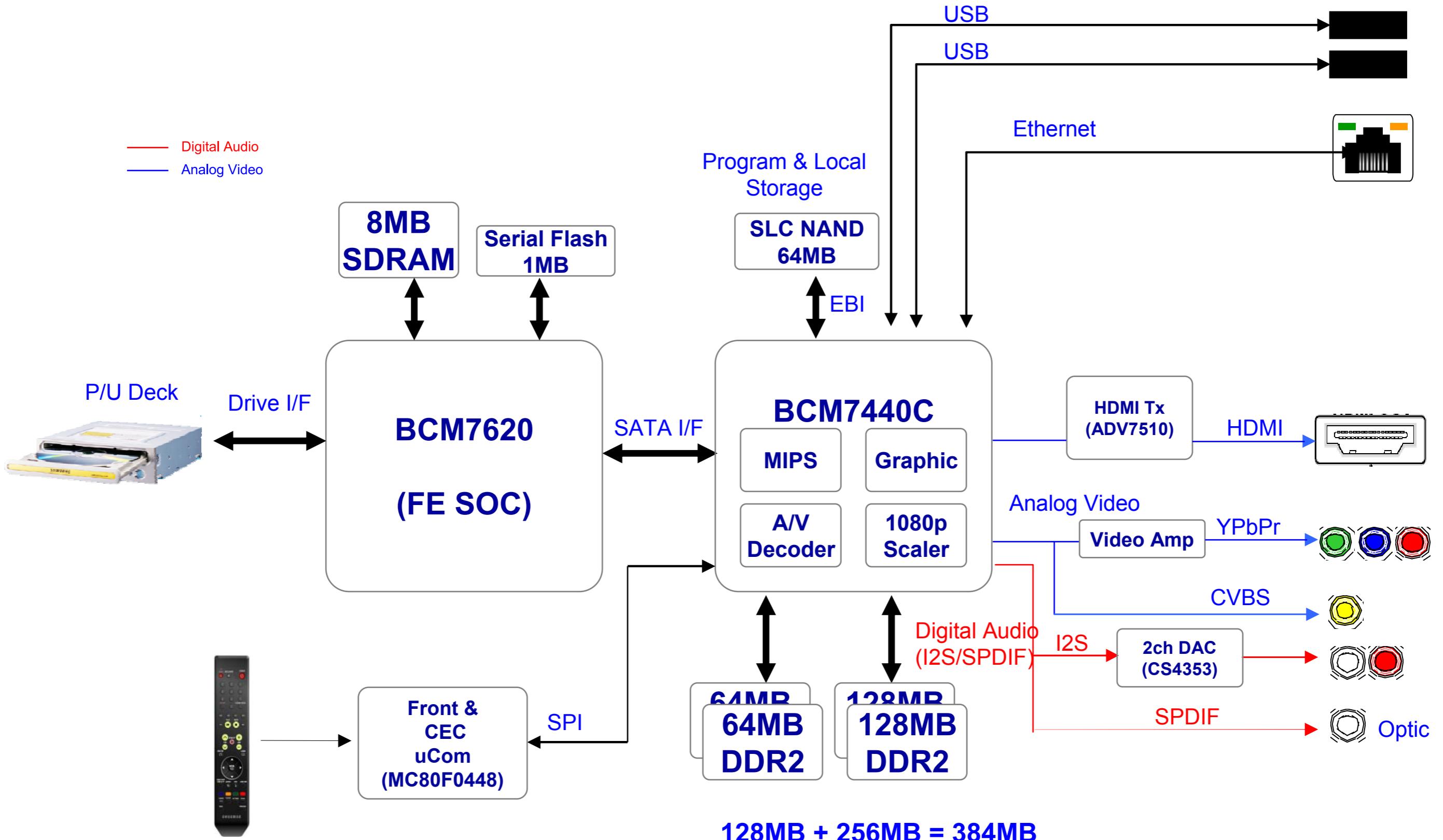
Note :

Do not use the part number shown on this drawing for ordering. The correct part number is shown in the parts list (may be slightly different or amended since this drawing was prepared).

Important safety notices :

Components identified with the mark  have the special characteristics for safety. When replacing any of these components. Use only the same type.

7-1 All block Diagram



7-2 Power

7-2-1 About S.M.P.S (Ringing Choke Converter Method)

Terms

- 1) 1st : Common power input to 1st winding.
- 2) 2nd : Circuit follows output winding of transformer.
- 3) f (Frequency) : Switching frequency (T : Switching cycle)
- 4) Duty : $(Ton/T) \times 100$

7-2-2 Circuit description [FLY-Back RCC(Ringing Choke Converter) Control]

(a) AC Power Rectification/Smoothing Terminal

- 1) A01~04 : Convert AC power to DC (Wave rectification).
- 2) CIS01 : Smooth the voltage converted to DC.
- 3) LIS01, LISO2, CIS04, CIS05 : Noise removal at power input/output.
- 4) RIS04 : Rush current limit resistance at the moment of power cord insertion.
Without PLRU1, the bridge diode might be damaged as the rush current increases.

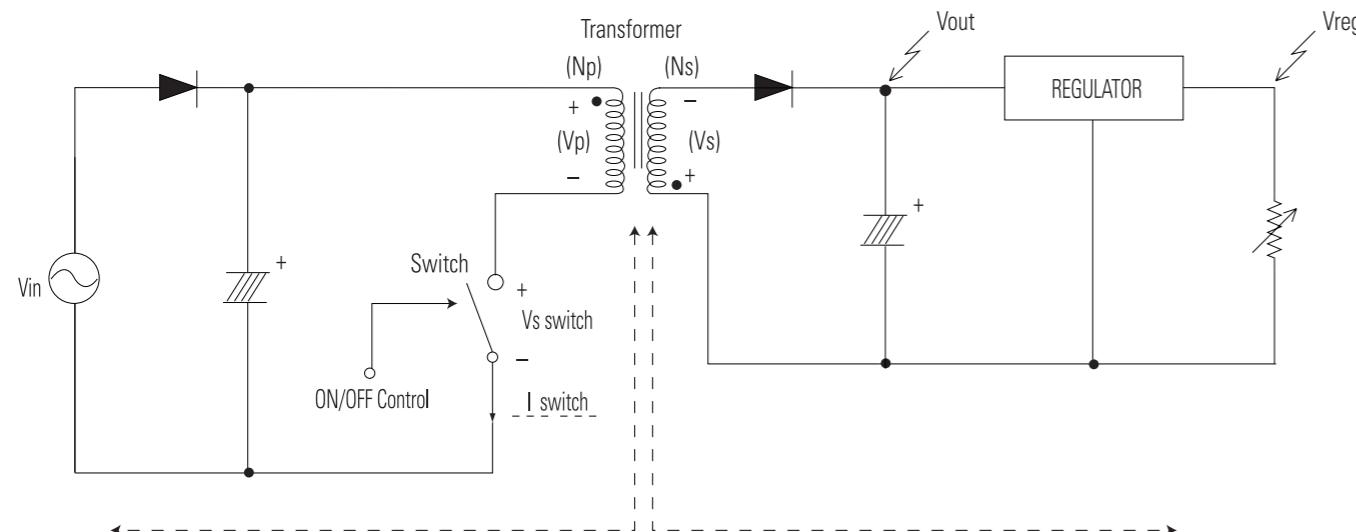


Fig. 7-1

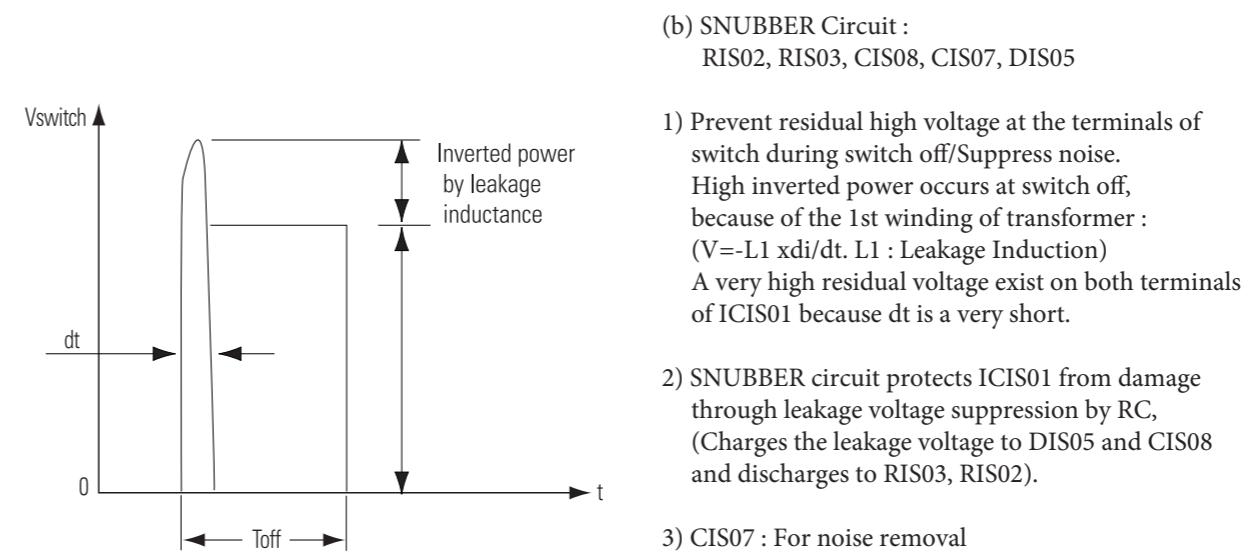


Fig. 7-2

Schematic Diagrams

(d) Feedback Control Circuit

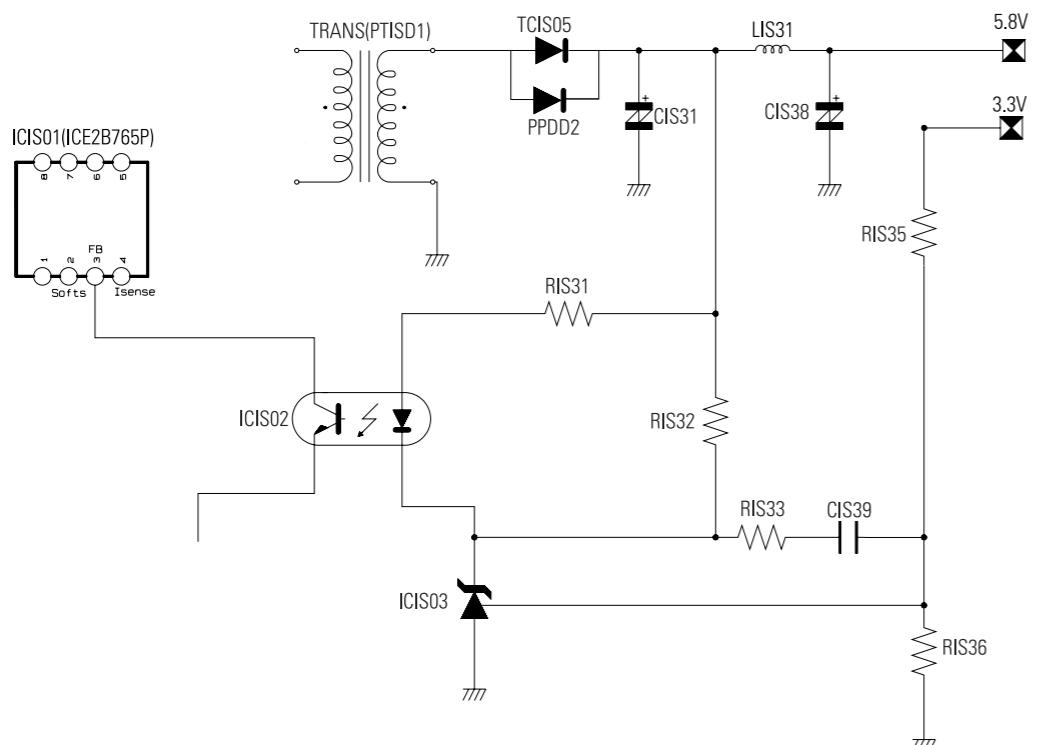


Fig. 7-3

- 1) F/B terminal of PQIZ1 determines output duty cycle.
 - 2) C-E (Collector-Emitter) of PQIZ1 and F/B potential of PQIZ1 are same.

7-2-3 Internal Block Diagram (Internal Block Diagram of S.M.P.S Circuit)

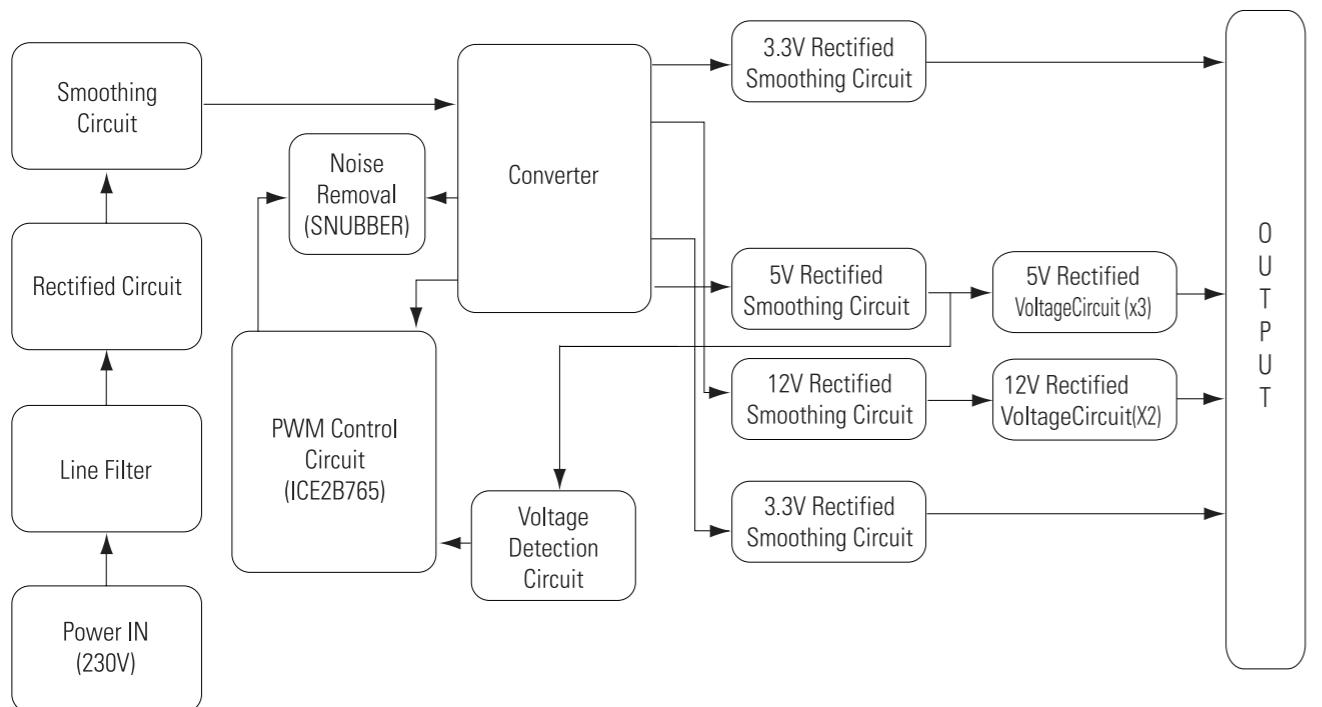
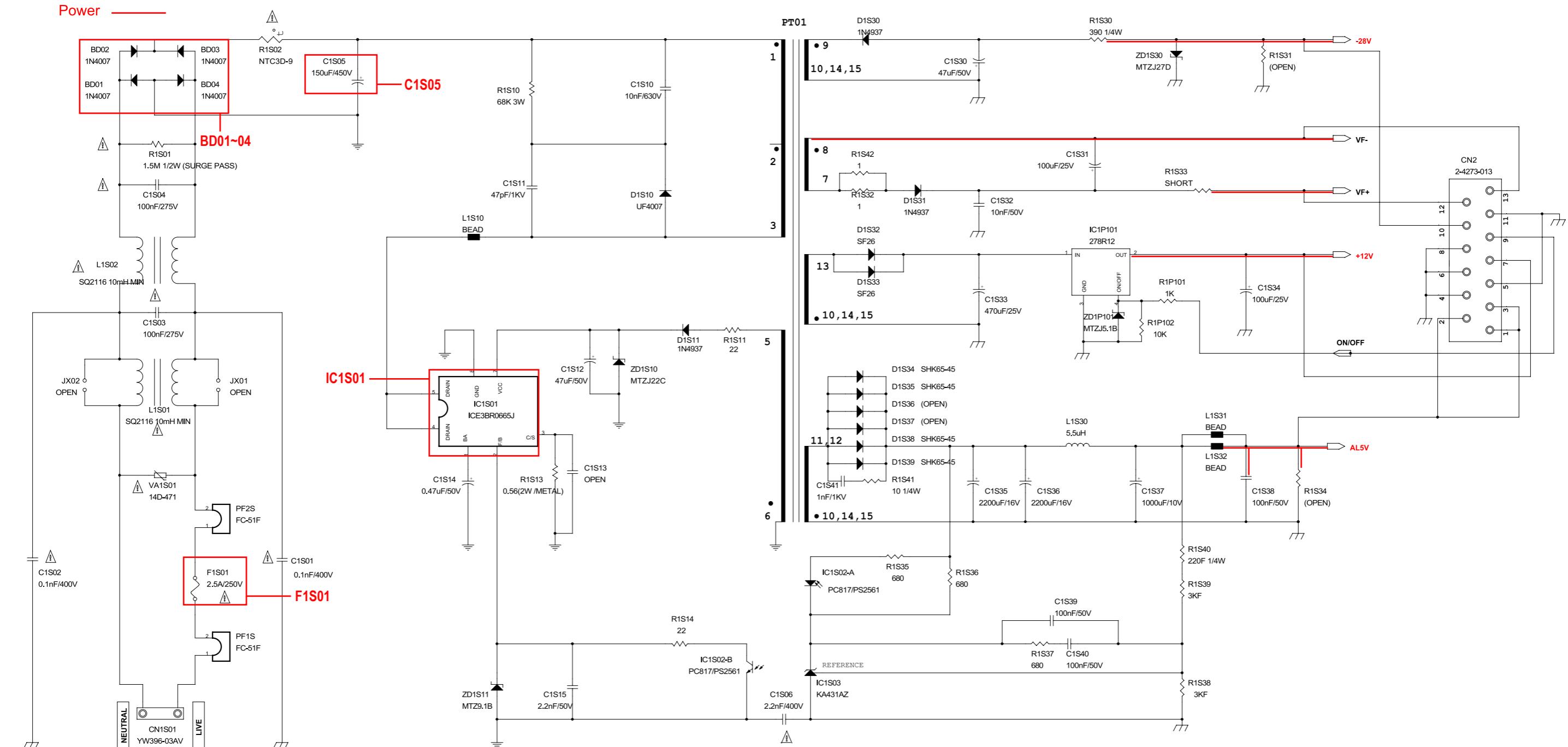
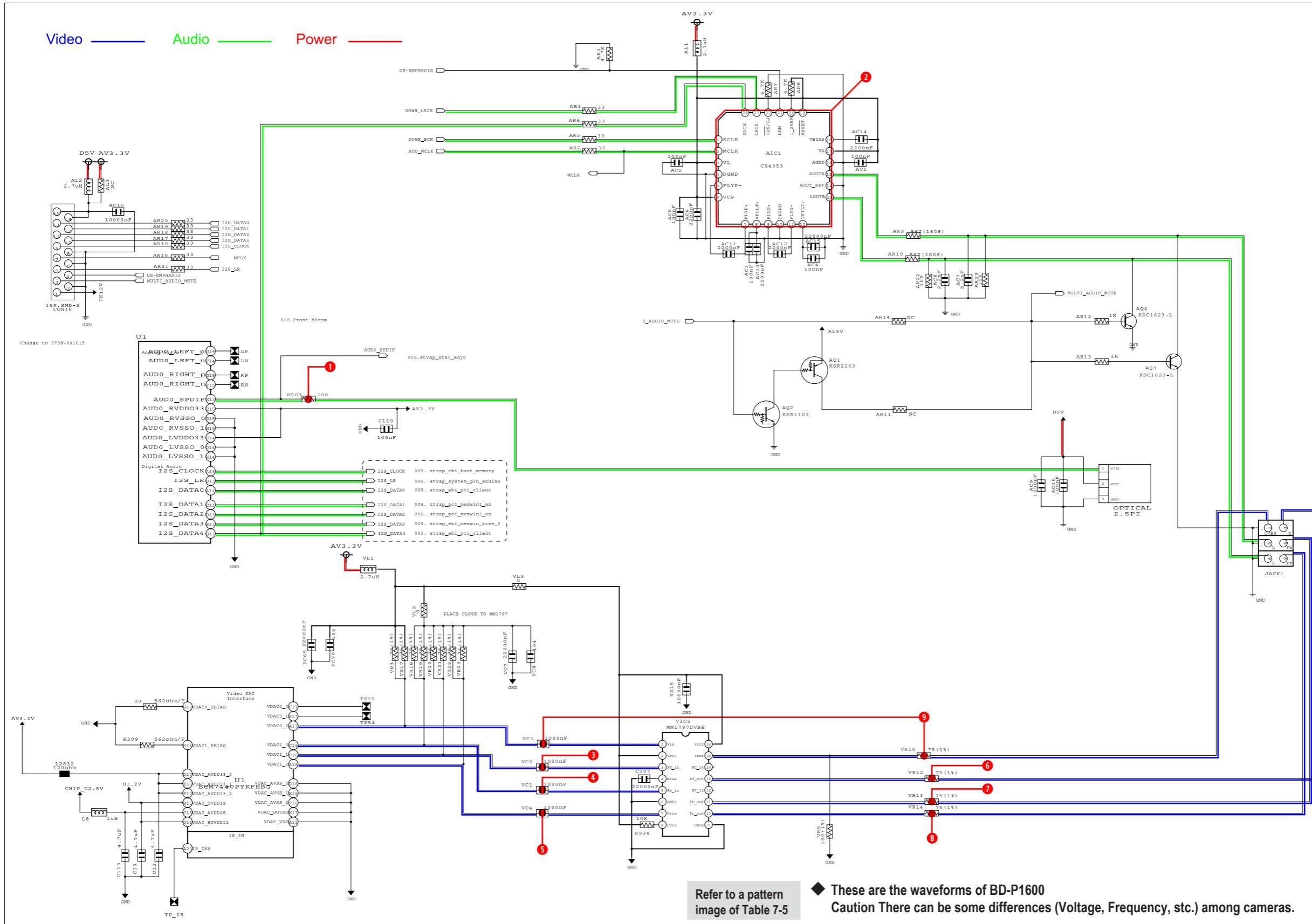


Fig. 7-4

7-3 S.M.P.S (S.M.P.S PCB)



7-4 Anlog Audio (2ch)/Video (CVBS, Component)(Main PCB)



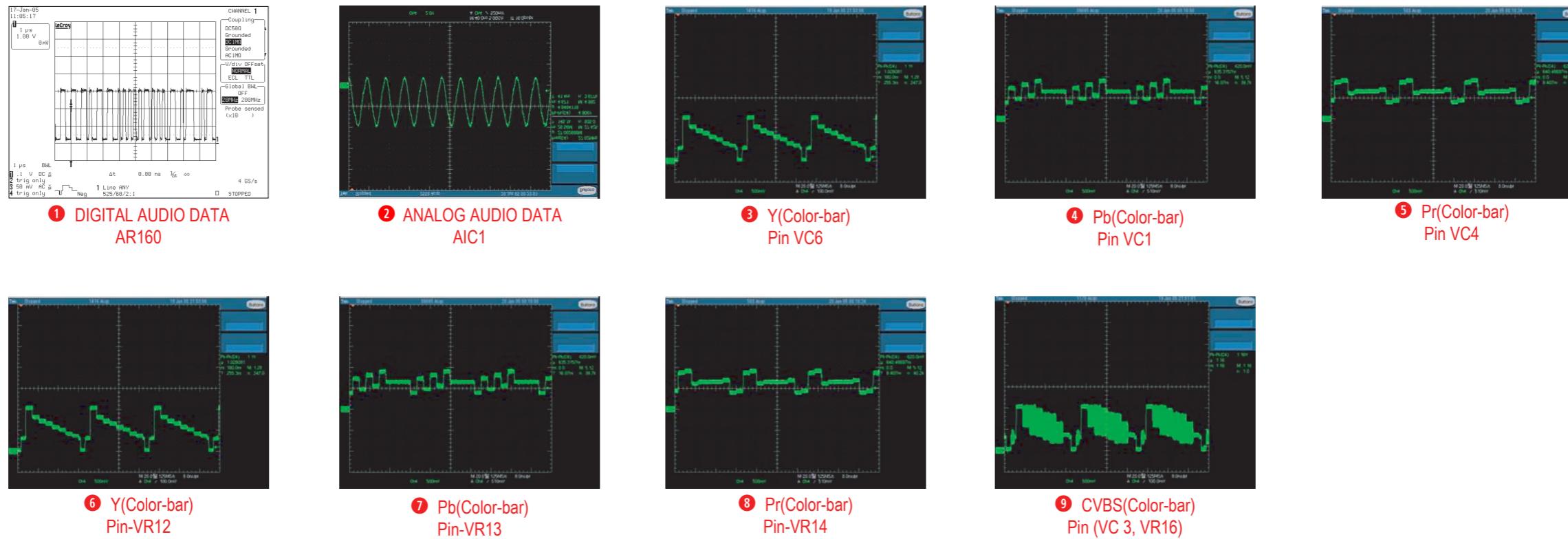
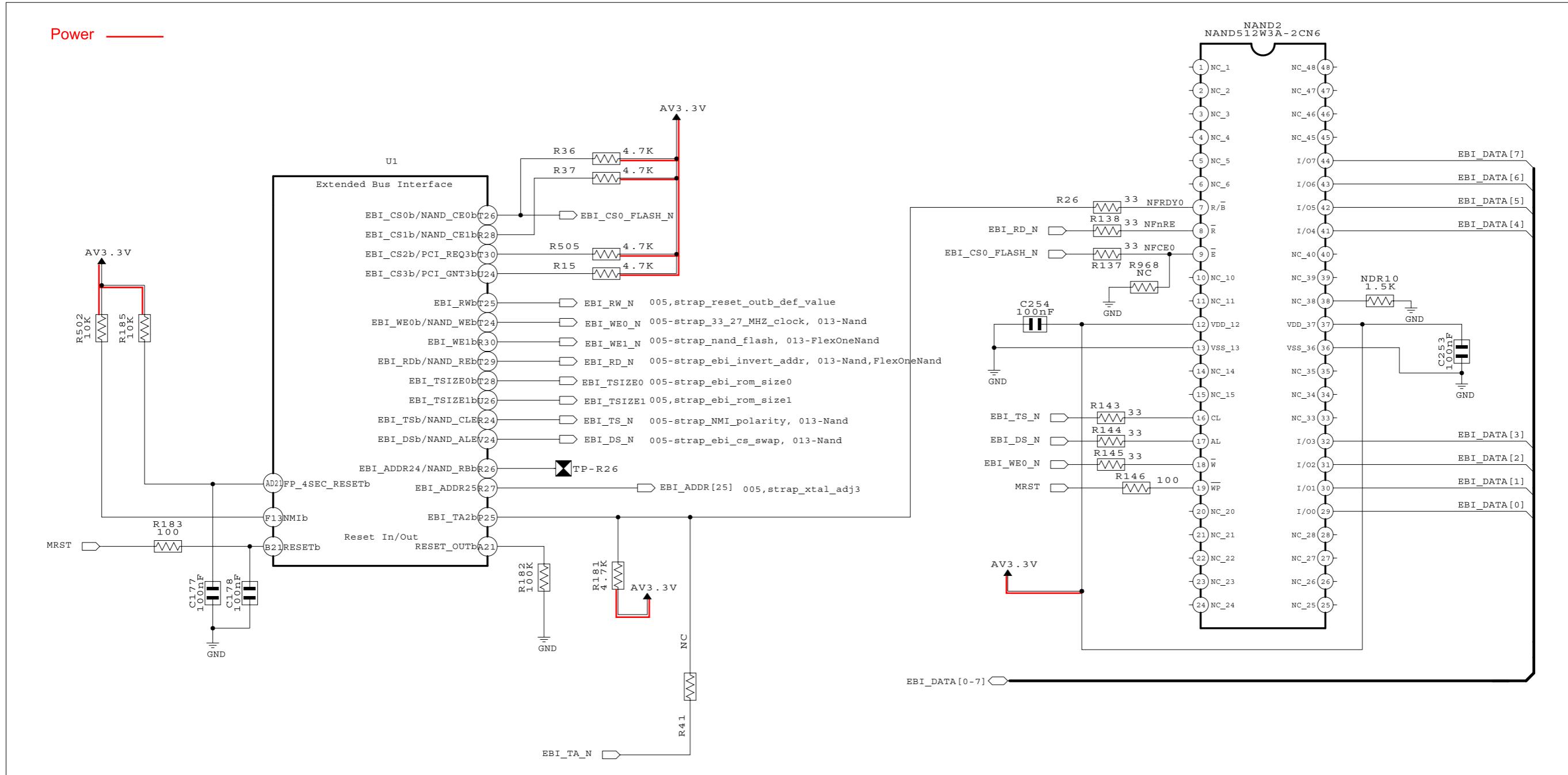
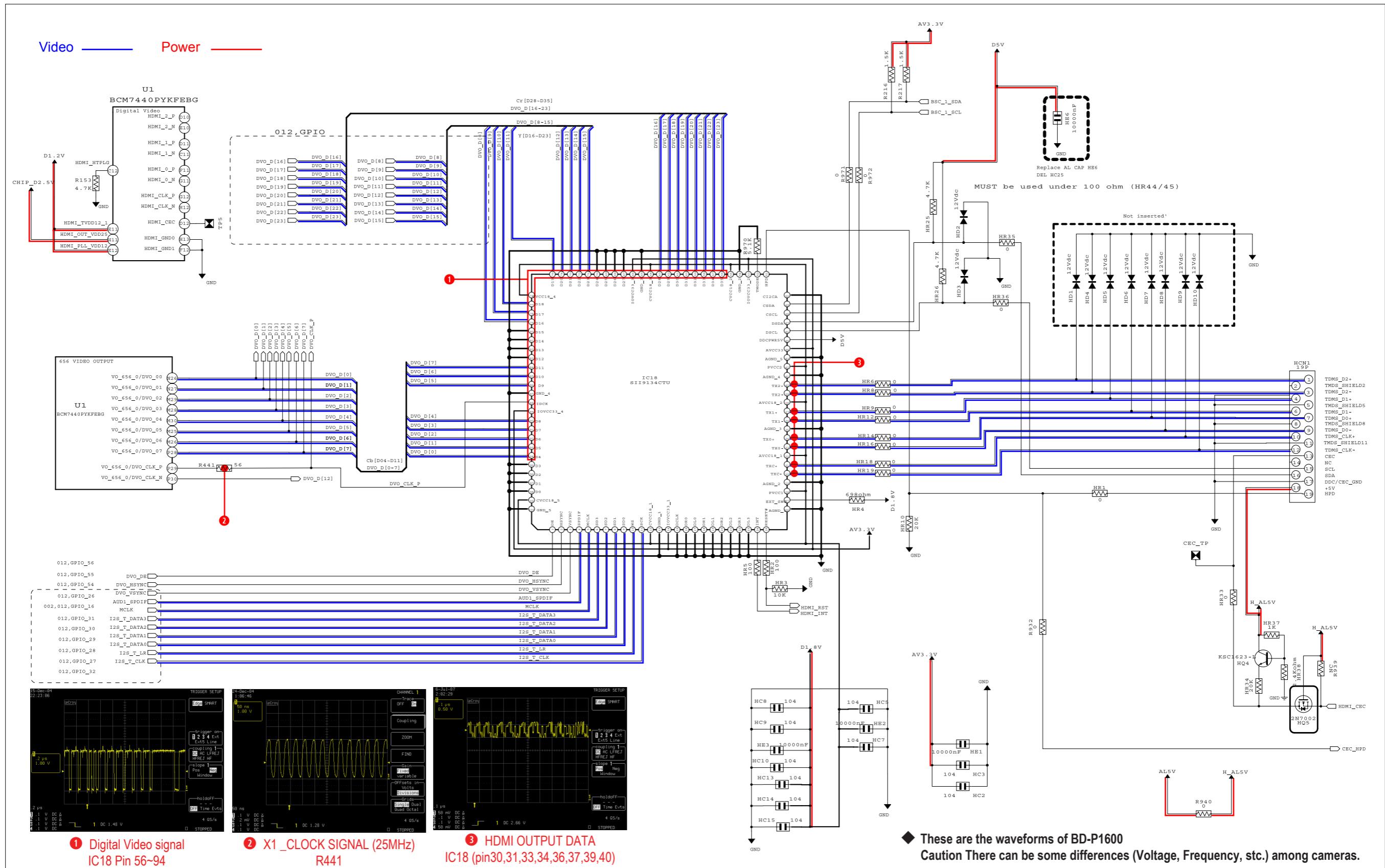


Fig. 7-5

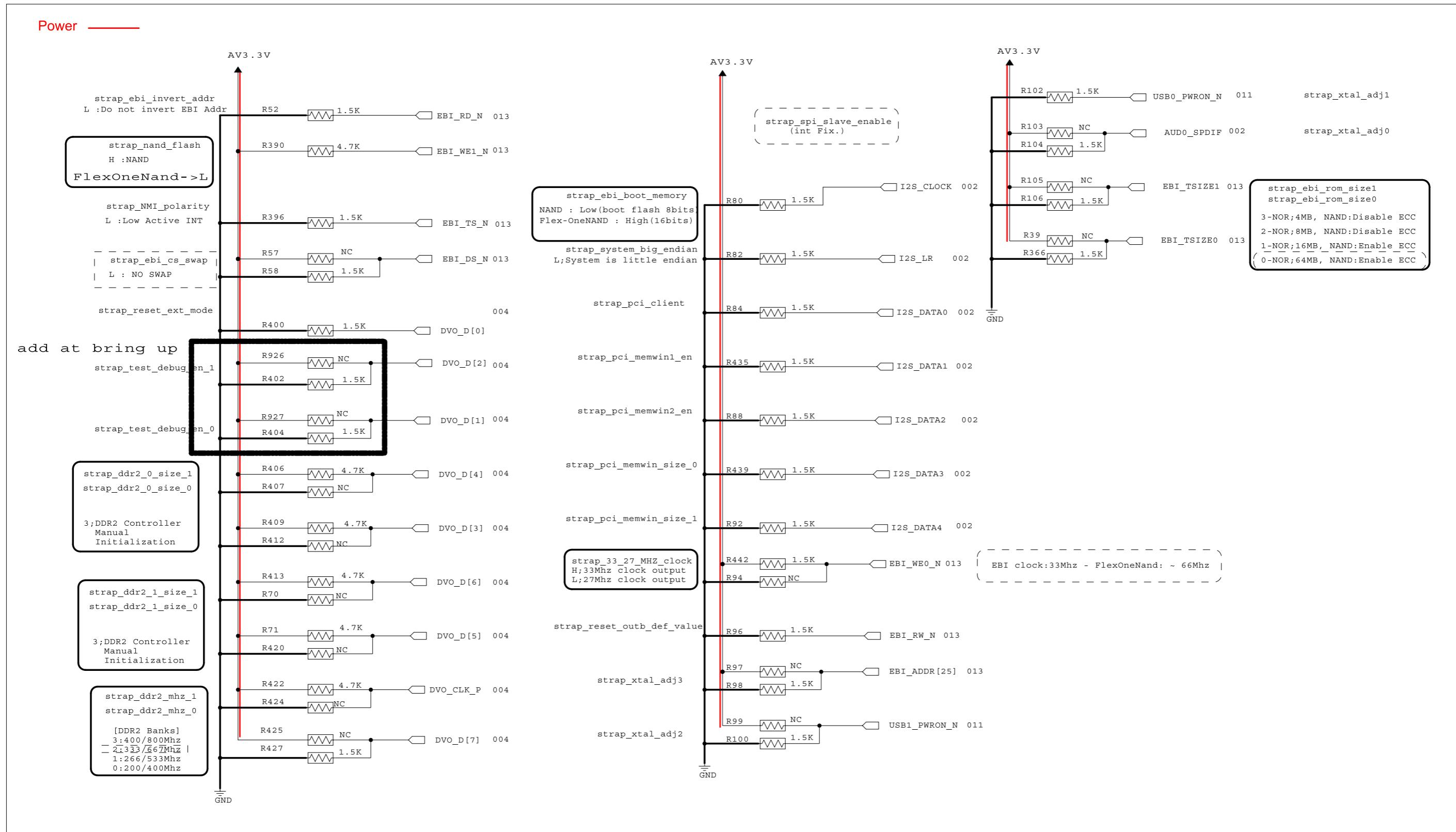
7-5 NAND Flash, FlexOneNand (Main PCB)



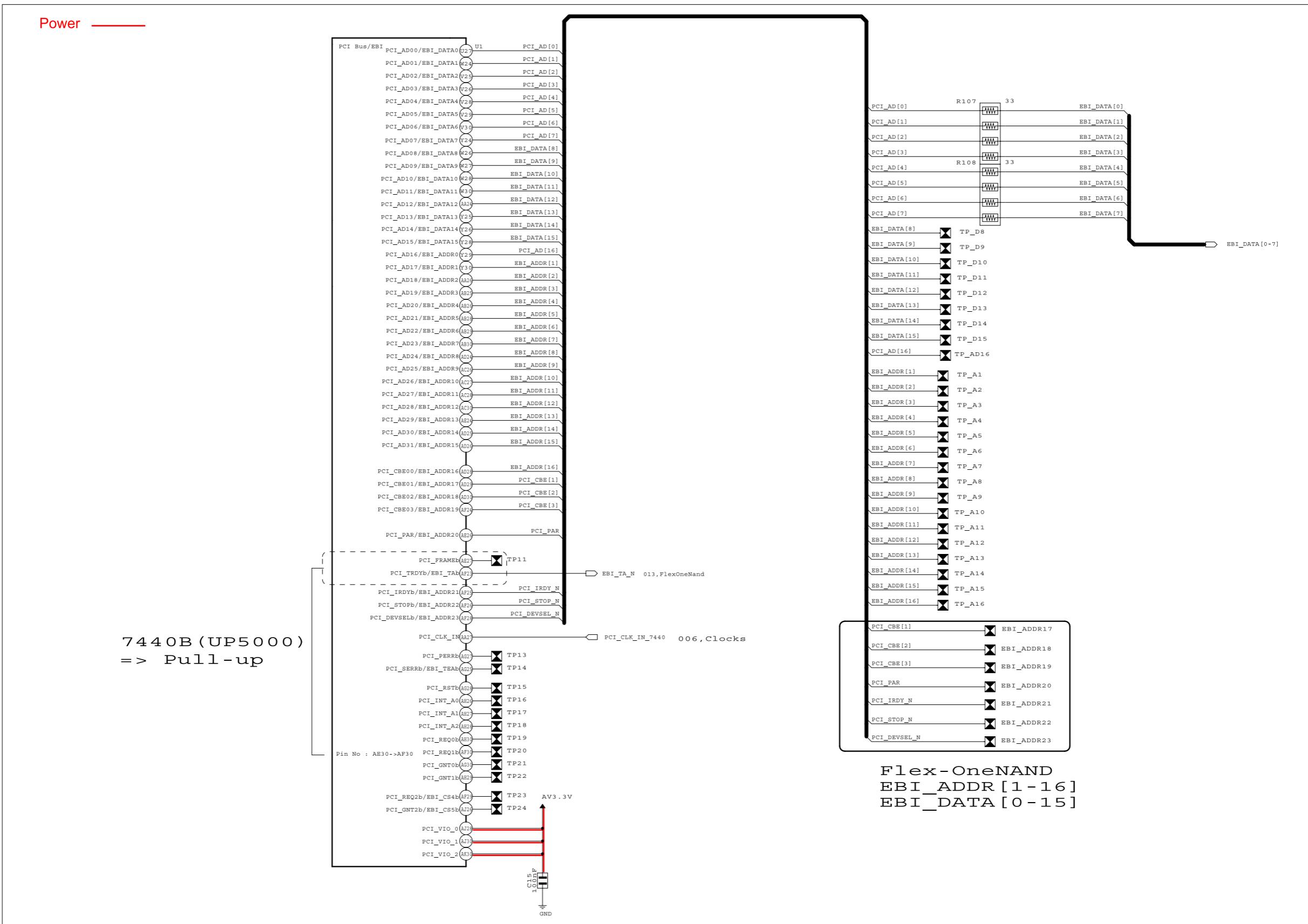
7-6 HDMI Transmitter (SIL9134)(Main PCB)



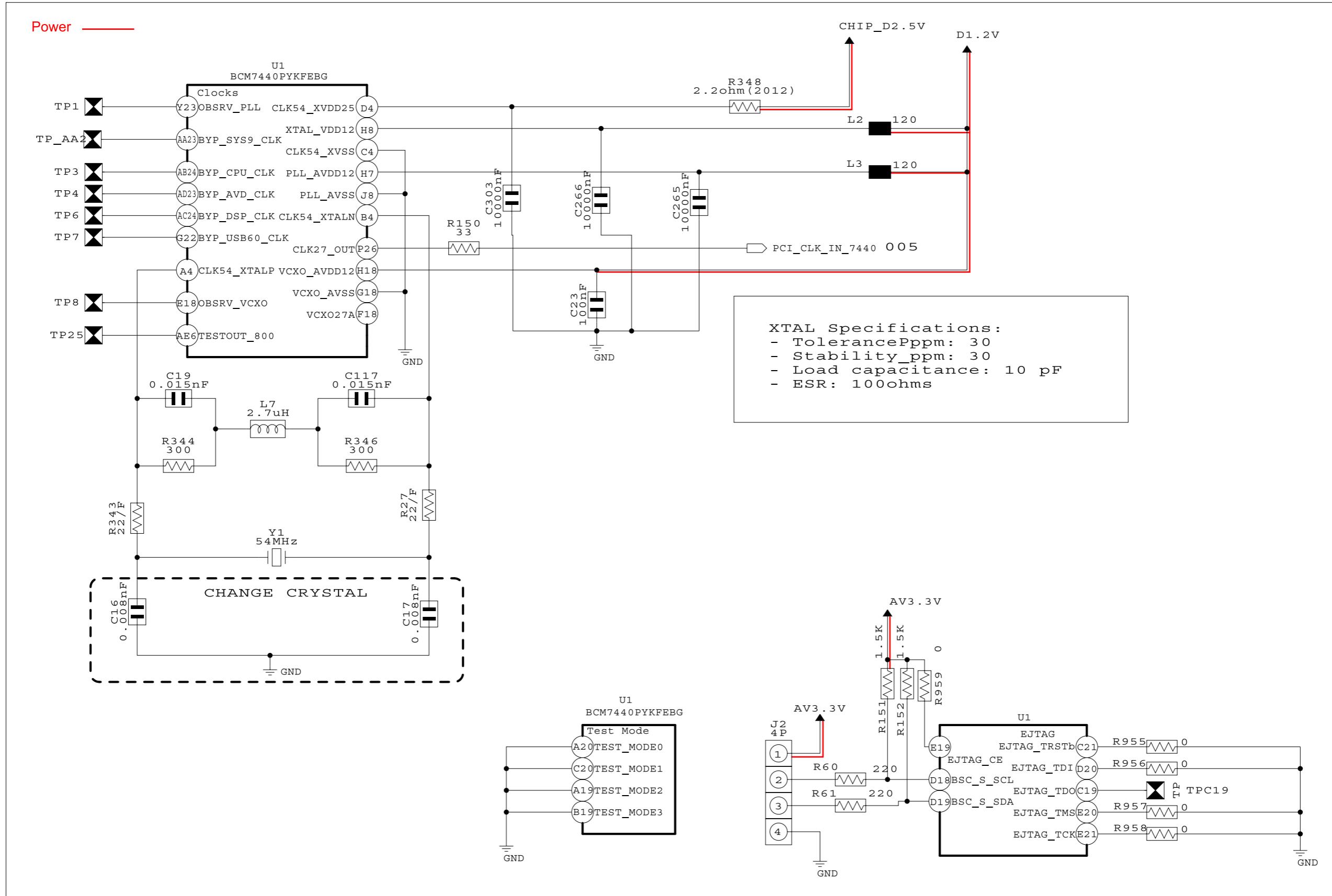
7-7 7440 Boot Strap Option (Main PCB)



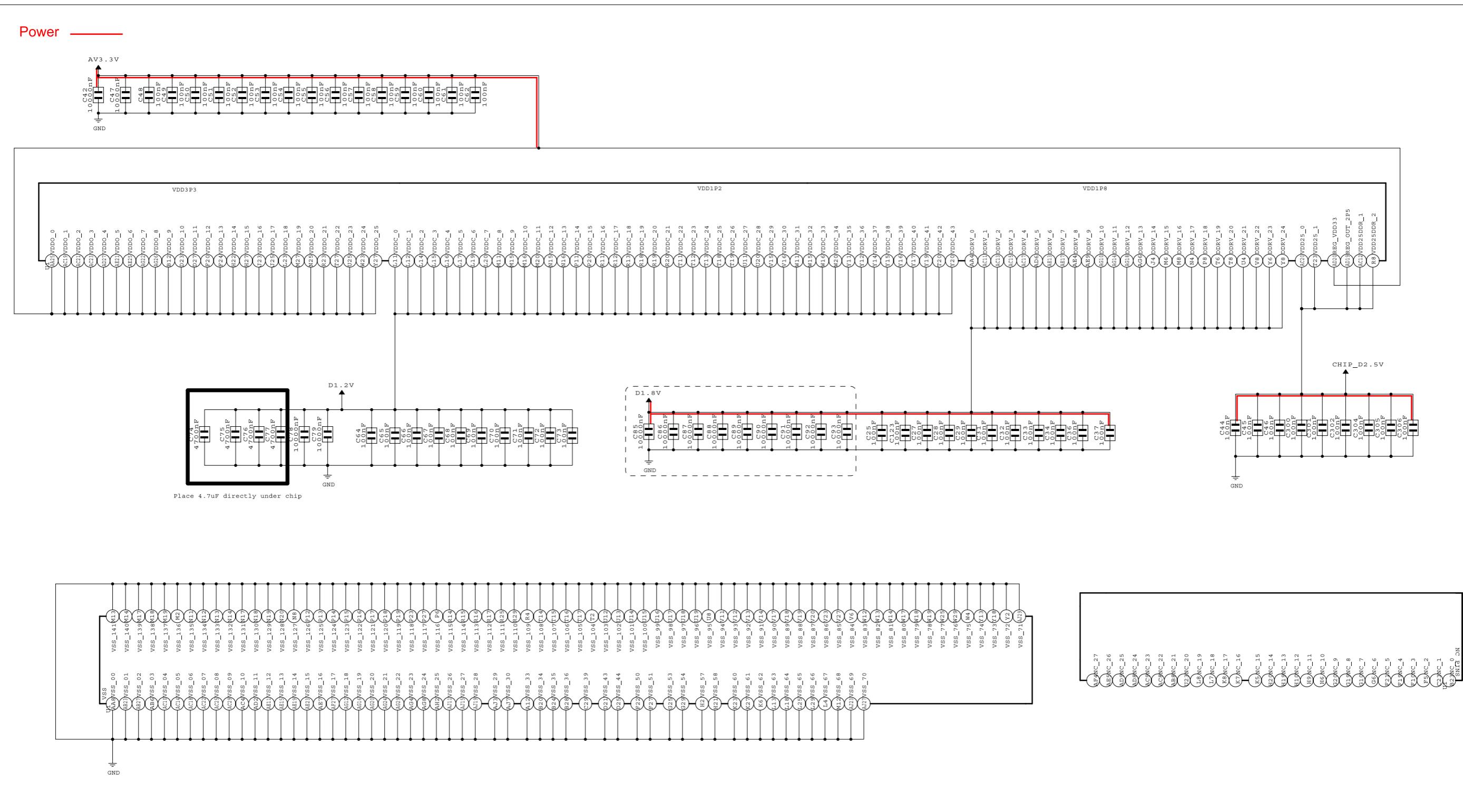
7-8 7440 EBI_ADDR, EBI_DATA (Main PCB)



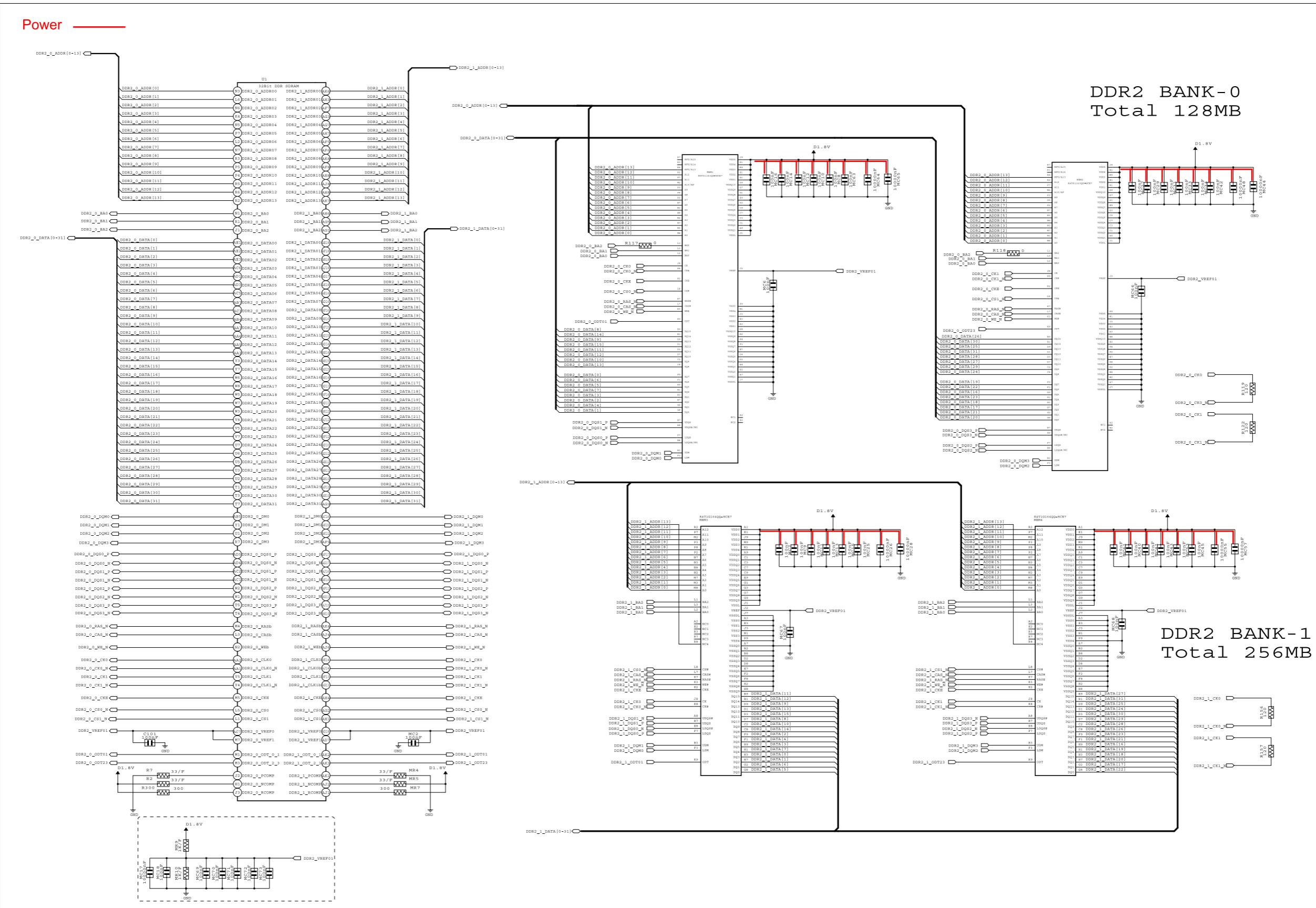
7-9 7440 Clocks, BBS (Main PCB)



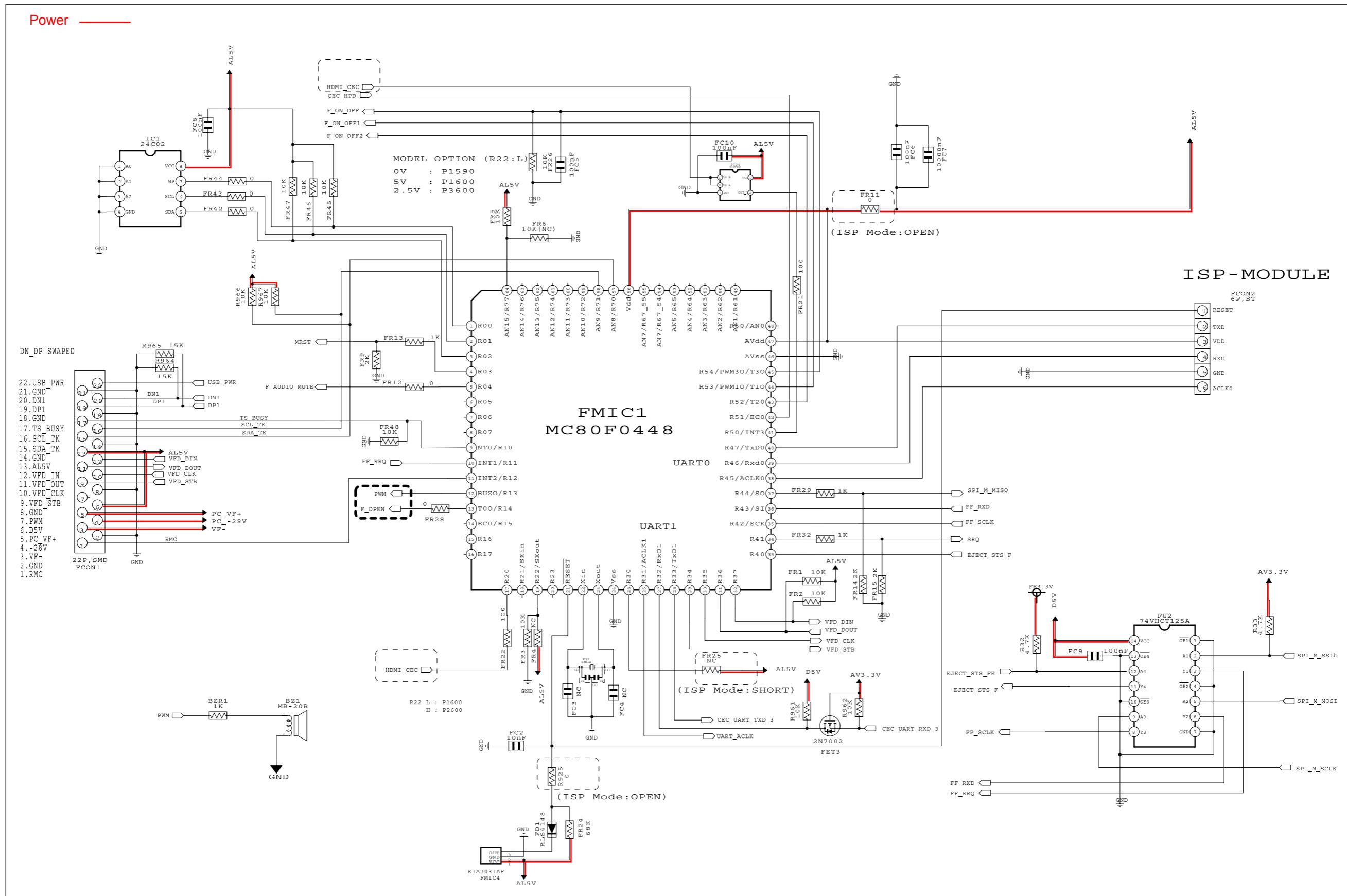
7-10 7440 Power, Decoupling (Main PCB)



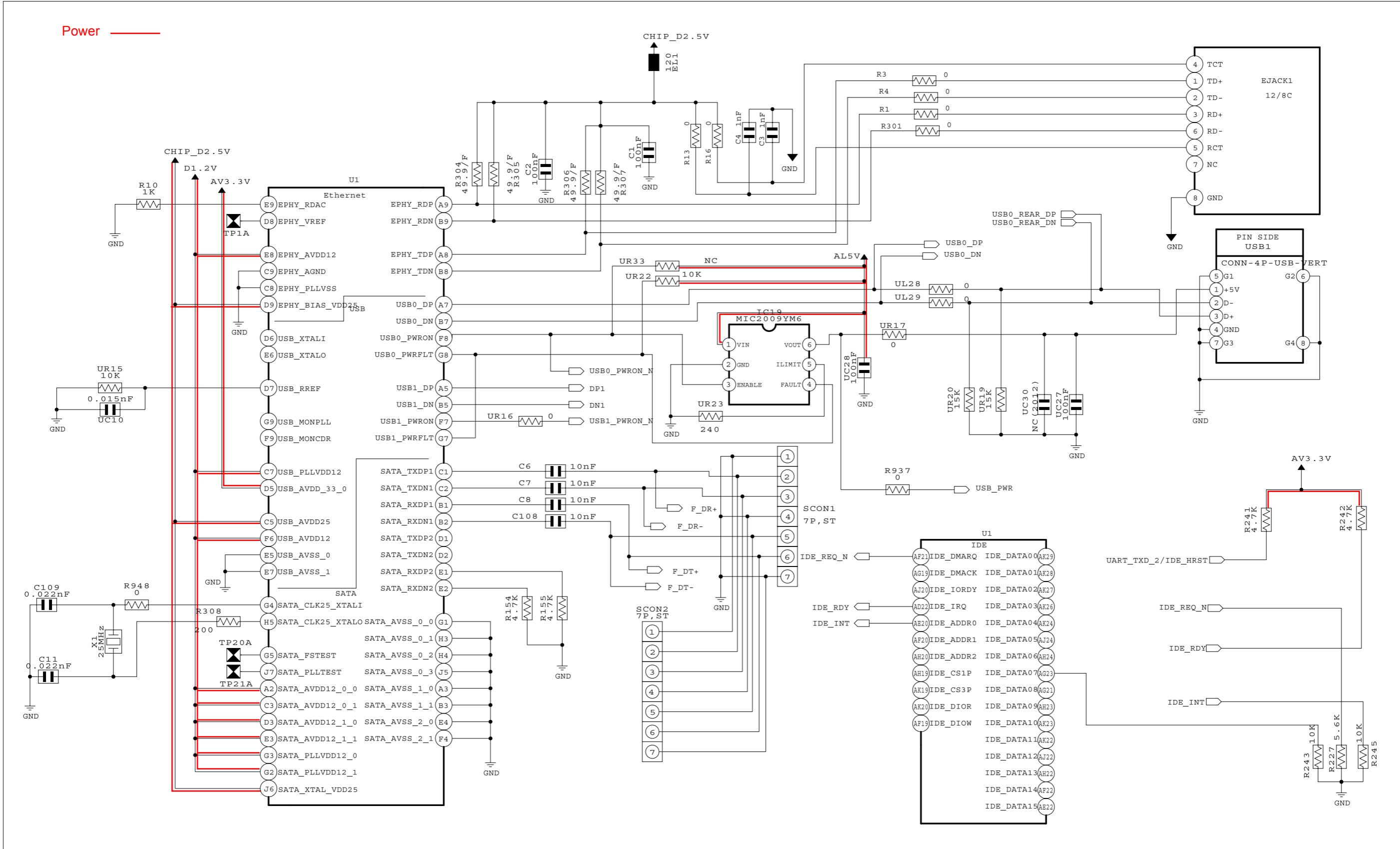
7-11 DDR2 BANK0, BANK1 (Main PCB)



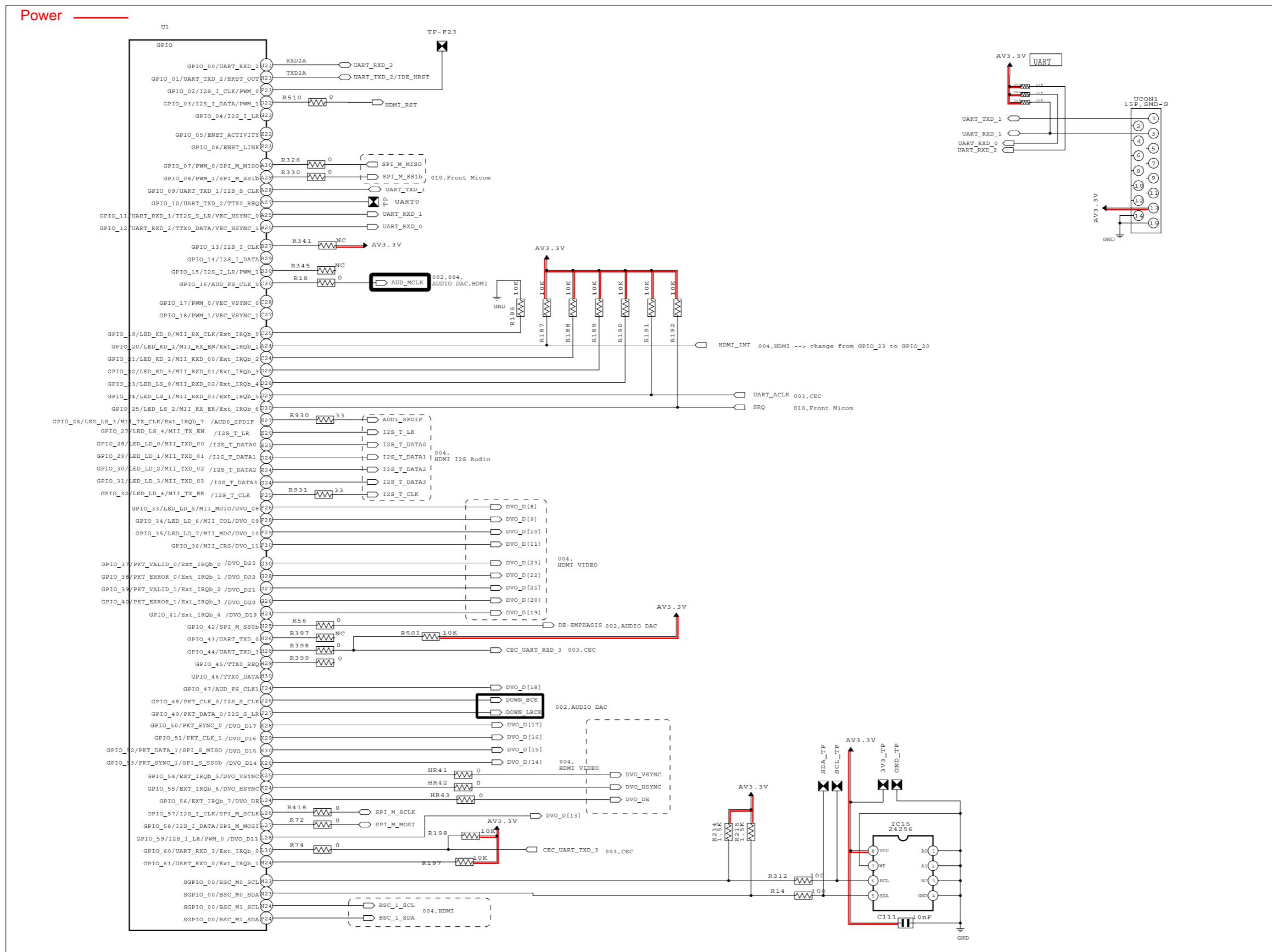
7-12 Front Micom (Main PCB)



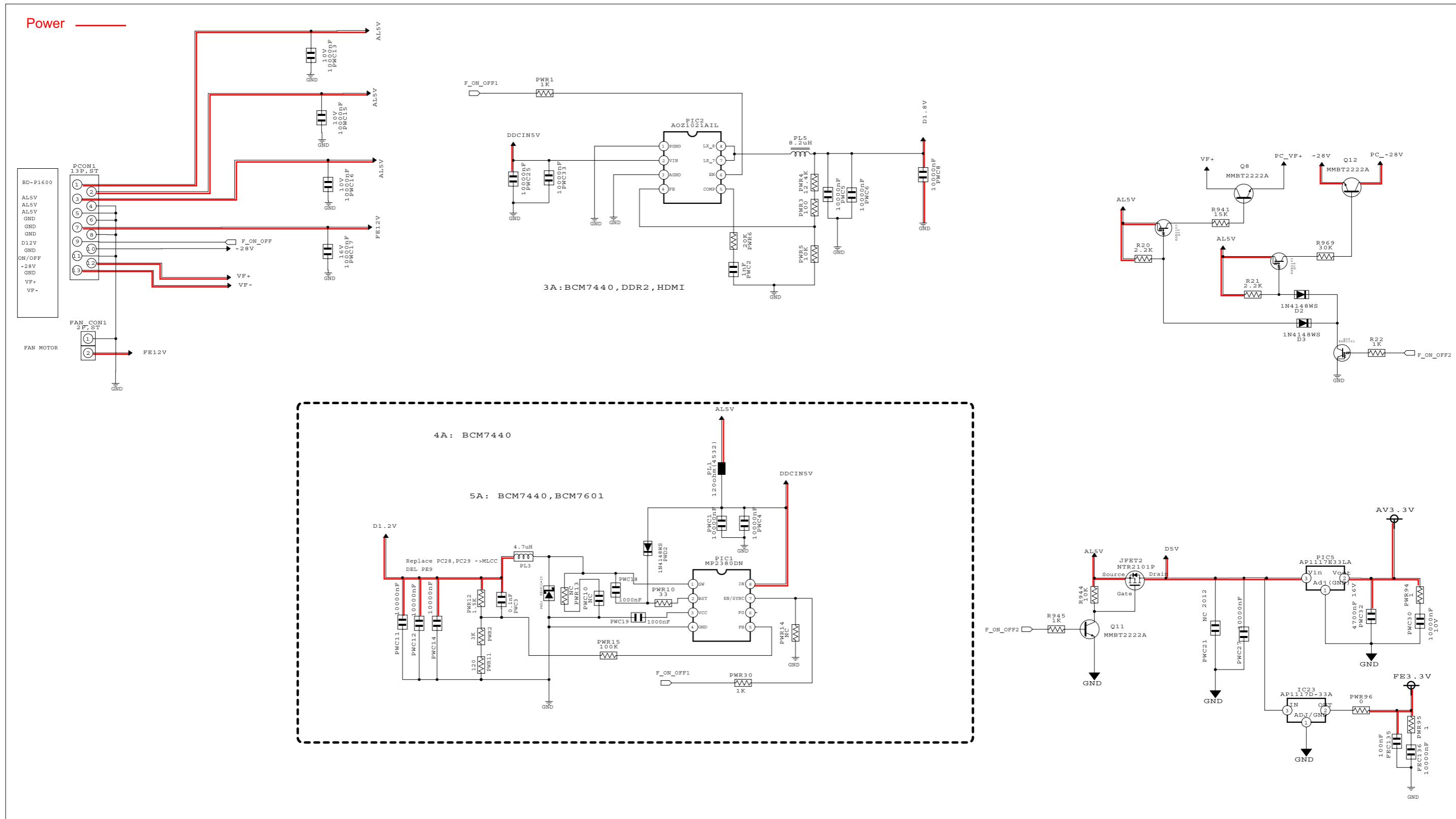
7-13 Ethernet, USB, S-ATA, P-ATA (Main PCB)



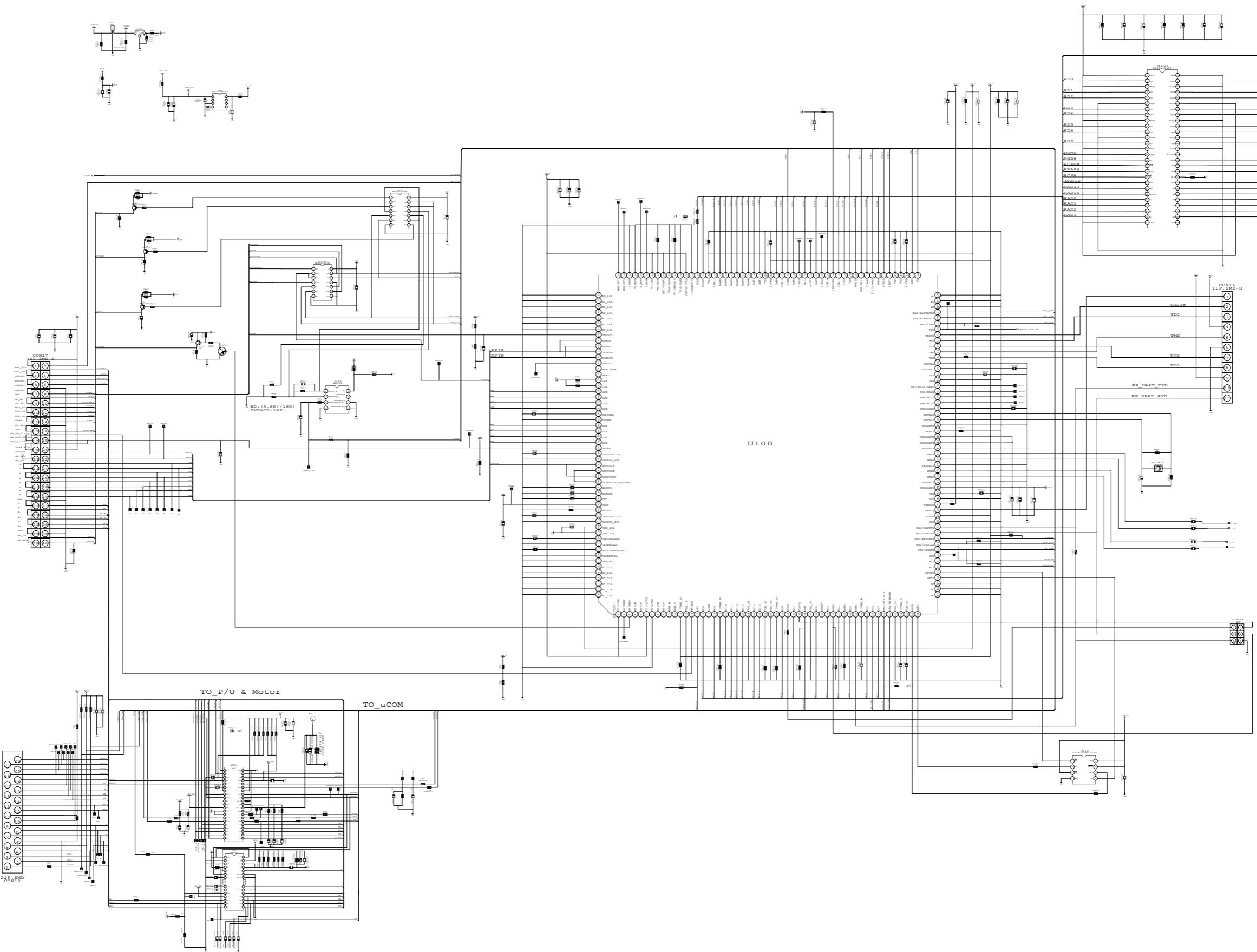
7-14 GPIO Block (Main PCB)

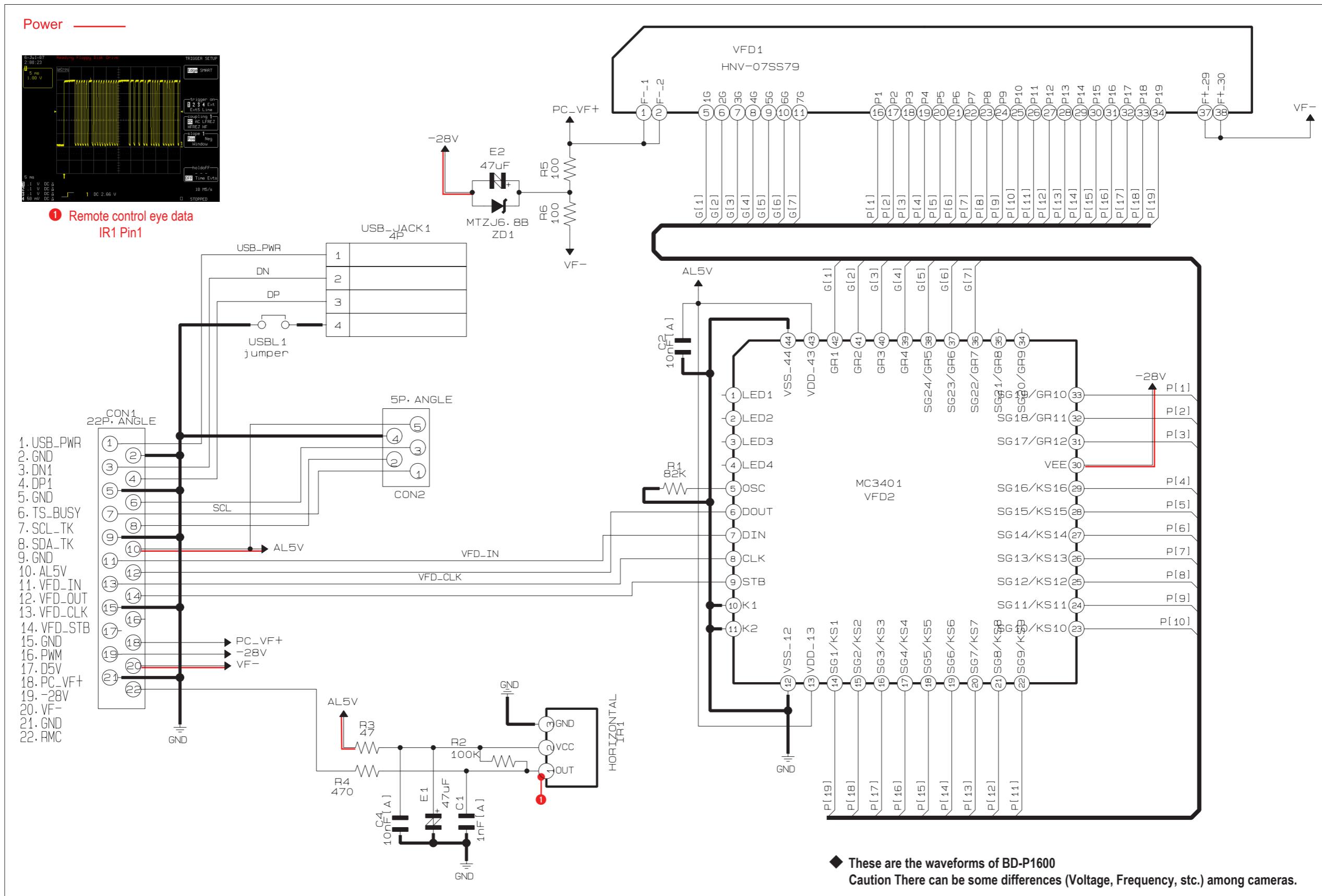


7-15 Main Power (Main PCB)

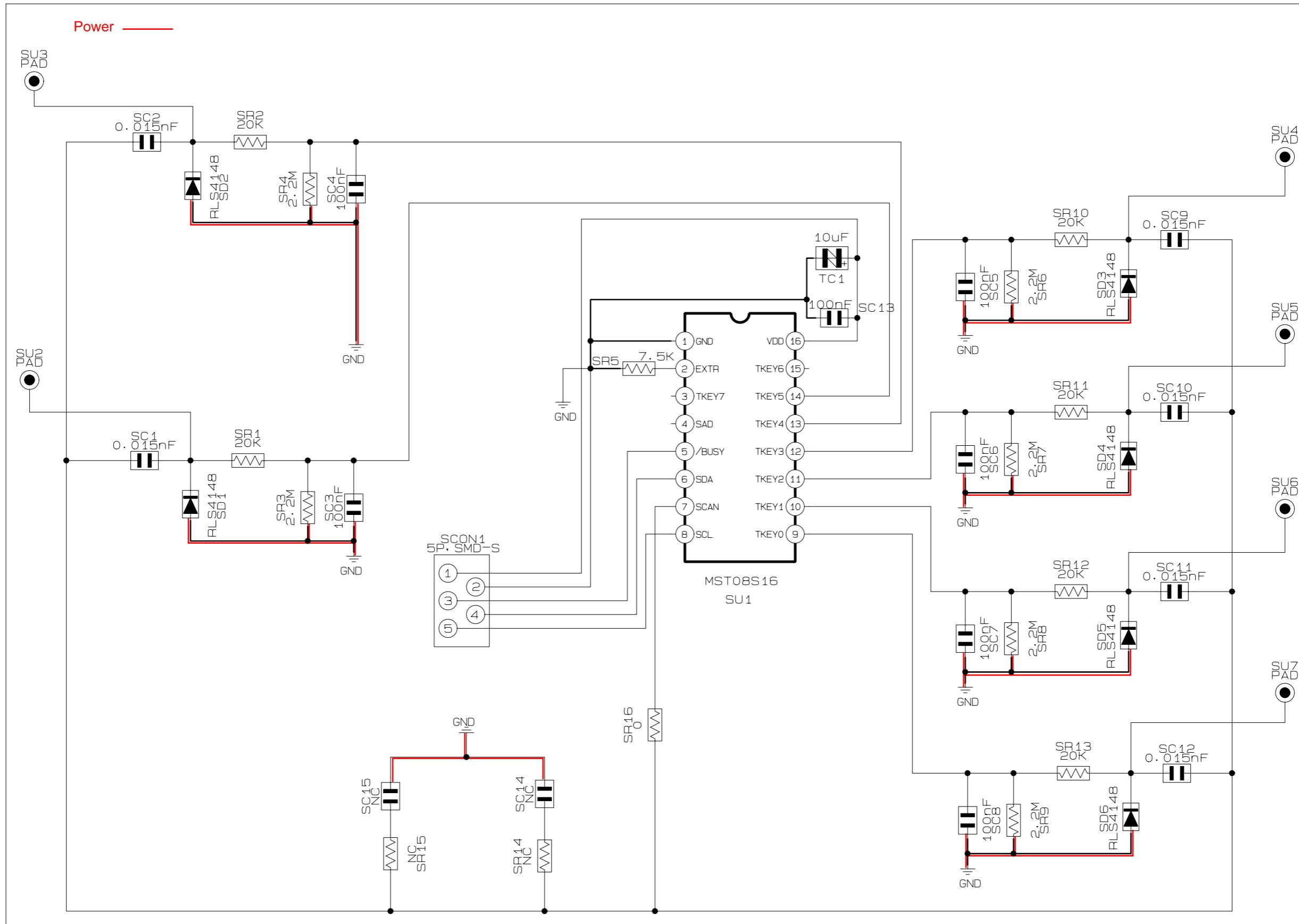


7-16 BCM7620 (F/E)(Main PCB)



7-17 Front Key (Front PCB)

7-18 Touch Key PCB (Touch PCB)



M E M O