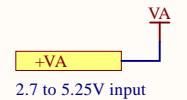


POWER PORTS

WARNING! +VA \geq +VBD (pg 51)



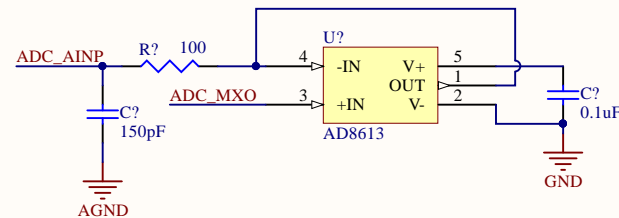
2V5 REF — 2V5

WARNING: cannot source current



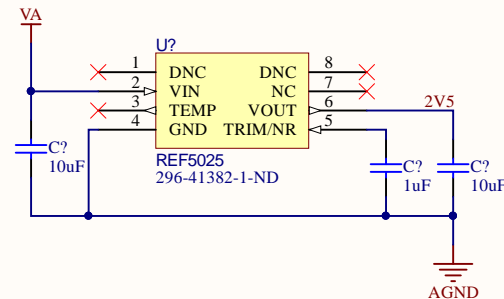
ADC INPUT BUFFER

See pg. 50 for discussion of unity buffer design procedure



2V5 REFERENCE

Output cap should have ESR from 1 - 1.5 ohm (see pg. 21)



ADC

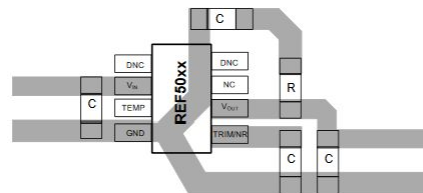
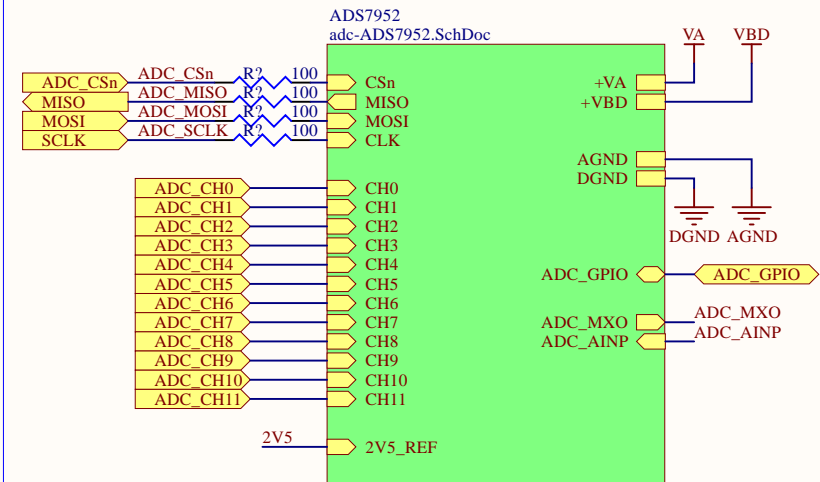


Figure 44. Layout Example

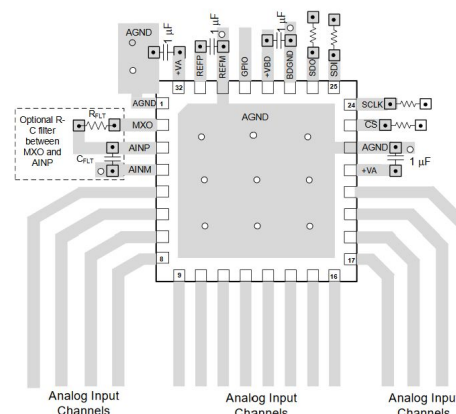


Figure 70. Recommended Layout for the VQFN Packaged Device

This schematic implements the ADS7952 analog-to-digital converter with a 2.5V reference and a unity-gain buffer on the output of the internal multiplexer.

- Recommended input impedance should be < XX ohm. Higher source impedances possible with slower sampling.
- Breaks out 2V5 for use as reference outside the circuit
- All necessary bypassing and pull-ups implemented in the ADS7952 schematic
- In most low-performance applications, +VA and +VBD can be tied together
- In the layout, the pins tied to AGND should be put on a local GND pour and then tied to the global ground plane with low-impedance.
- 100 ohm resistors on the SPI input help to isolate the ADC from digital noise

Title		AD57952 Circuit		UTAT SS	
Size	Number			Revision	
A4	*			1.0	
Date:	2018-09-26			Sheet *	of *
File:	C:\Users\...\adc-circuit-ADS7952.SchDoc			Drawn By: Dylan Vogel	