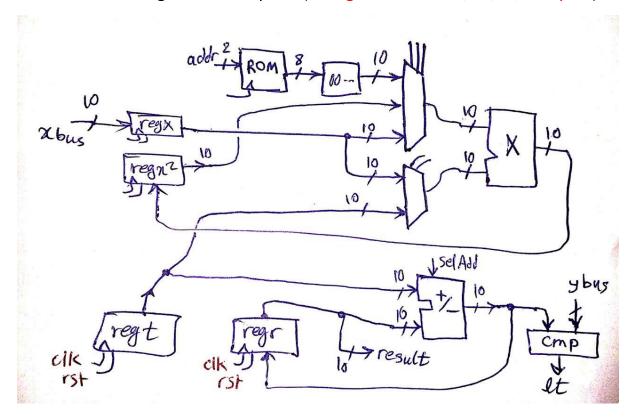
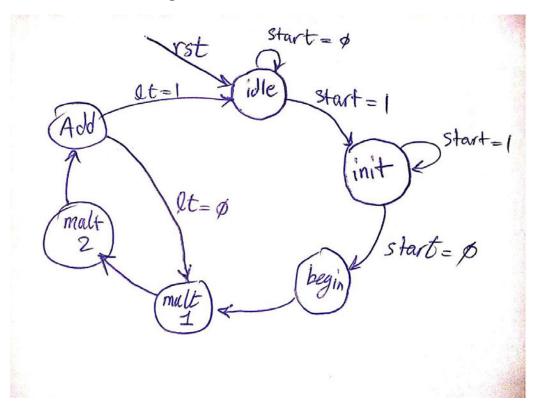
### CA#6 DLD Fall99 report

## Hesam Asadollahzadeh –

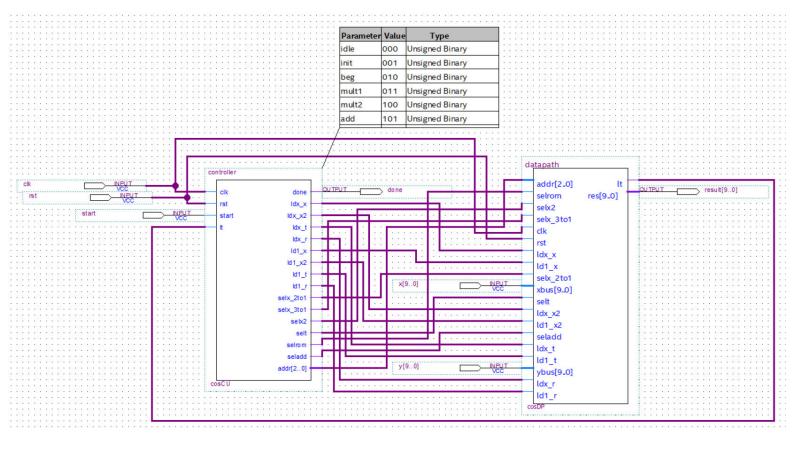
## 1&3.schematic diagram of data path: (all registers have ld1,ldx,clk,rst inputs)



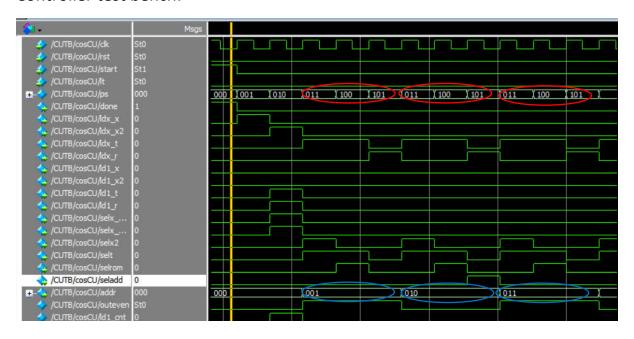
# 2.controller state diagram:



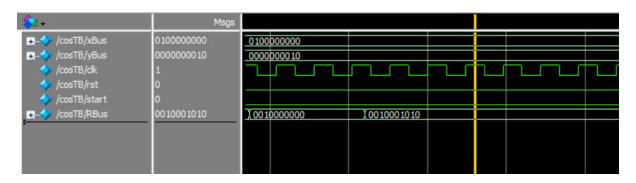
#### 6. cosx circuit:



### Controller test bench:

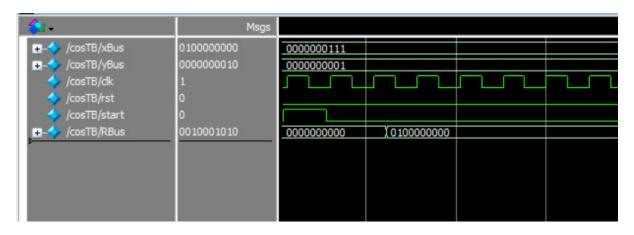


### Cosx Test bench:

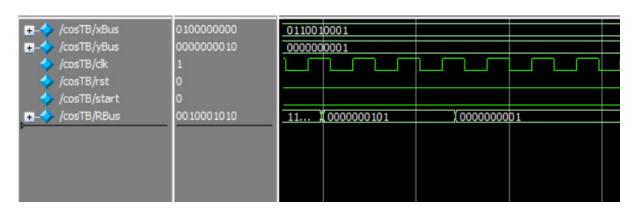


 $X = 01\ 0000\ 0000 = 1 \Rightarrow \cos(1) = 0.54$ 

R = 00 1000 1010 = 0.5390625 ©



 $X = 00\ 0000\ 0111 = 0.02734375 => \cos(0.02734375) = 0.99999988 = 1$  $R = 01\ 0000\ 0000 = 1$ 



 $X = 01\ 1001\ 0001 = 1.53515625 \Rightarrow cos(1.53515625) = 0.0356$ 

R = 00 0000 0001 = 0.0039625 ©