

Simulations of Single Cycle RISC-V

Assembly Code	MachineCode
addi x1, x0, 1	00000000000100000000000010010011
addi x2, x0, 2	000000000001000000000000100010011
jal x10, 2	00000000000100000000000010101101111
addi x3, x0, 3	00000000000110000000000000110010011
addi x4, x0, 4	000000000001000000000000001000010011
add x4, x3, x2	0000000000010000110000001000110011
add x3, x2, x2	0000000000010000100000000110110011
add x2, x2, x2	0000000000010000100000000100110011
sw x2, 0, x3	000000000001000011010000000100011
beq x0, x0, C	00000000000000000000000011001100011



