Fitter Summary

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Fitter Status	Successful - Wed Oct 26 01:49:06 2022
Quartus Prime Version	21.1.0 Build 842 10/21/2021 SJ Lite Edition
Revision Name	RSA_PIPELINE_CPU
Top-level Entity Name	top
Family	Cyclone V
Device	5CSEBA6U23I7
Timing Models	Final
Logic utilization (in ALMs)	1,909 / 41,910 (5 %)
Total registers	3613
Total pins	13 / 314 (4 %)
Total virtual pins	0
Total block memory bits	576 / 5,662,720 (< 1 %)
Total RAM Blocks	2 / 553 (< 1 %)
Total DSP Blocks	2 / 112 (2 %)
Total HSSI RX PCSs	0
Total HSSI PMA RX Deserializers	0
Total HSSI TX PCSs	0
Total HSSI PMA TX Serializers	0
Total PLLs	0 / 6 (0 %)
Total DLLs	0 / 4 (0 %)