Green sheet ISA Pipeline CPU

	System						
	ld			Null			
	Туре	Op	Null	Null			
	31:30 29:28 27:26		27:26	25:0			
NOP	00	00	00	000000000000000000000000000000000000000			
COM	00	01	00	000000000000000000000000000000000000000			
END	00	10	00	000000000000000000000000000000000000000			

		Data register-register						
	ld				Null			
	Type	Op	ı	RD	RA	RB	INUII	
	31:30	29:27	26	25:22	21:18	17:14	13:0	
ADD	01	000	0	XXXX	XXXX	XXXX	00000000000000	
SUB	01	001	0	XXXX	XXXX	XXXX	00000000000000	
AND	01	010	0	XXXX	XXXX	XXXX	00000000000000	
OR	01	011	0	XXXX	XXXX	XXXX	00000000000000	
MOV	01	100	0	XXXX	0000	XXXX	00000000000000	
MUL	01	101	0	XXXX	XXXX	XXXX	00000000000000	
CMP	01	110	0	0000	XXXX	XXXX	00000000000000	

		Data register-immediate						
		ld		Registers				
	Tipo Op I RD RA Immediate				Immediate			
	31:30	29:27	26	25:22	21:18	17:0		
ADDI	01	000	1	XXXX	XXXX	xxxxxxxxxxxxxxx		
SUBI	01	001	1	XXXX	XXXX	xxxxxxxxxxxxxxx		
ANDI	01	010	1	XXXX	XXXX	xxxxxxxxxxxxxxx		
ORI	01	011	1	XXXX	XXXX	xxxxxxxxxxxxxxx		
MOV	01	100	1	XXXX	0000	xxxxxxxxxxxxxxx		
MUL	01	101	1	XXXX	XXXX	xxxxxxxxxxxxxxx		
CMPI	01	110	1	0000	XXXX	xxxxxxxxxxxxxxx		

			Memoria							
			ld	Addressing						
		Туре	Op	Null	RD RA Immediate					
		31:30	29	28:26	26 25:22 21:18 17		17:0			
I	LDR	10	0	000	XXXX	XXXX	xxxxxxxxxxxxxxx			
	STR	10	1	000	XXXX	XXXX	xxxxxxxxxxxxxxx			

		Control						
	ld			Null	Instruction number			
	Туре	Op	Null	Null	Immediate			
	31:30	29:28	27:26	25:18	17:0			
JMP	11	00	00	00000000	xxxxxxxxxxxxxxx			
JEQ	11	01	00	0000000	xxxxxxxxxxxxxxx			
JLT	11	10	00	0000000	xxxxxxxxxxxxxxx			