## **Green sheet ISA Pipeline CPU**

		Stall						
	ld			Null				
	Туре	Op	Null	Ivuii				
	31:30	29	28:26	25:0				
NOP	00	0	000	000000000000000000000000000000000000000				
END	00	1	000	000000000000000000000000000000000000000				

		Data register-register					
	ld			Registers			Null
	Туре	Ор	ı	RD	RA	RB	Null
	31:30	29:27	26	25:22	21:18	17:14	13:0
ADD	01	000	0	XXXX	XXXX	XXXX	00000000000000
SUB	01	001	0	XXXX	XXXX	XXXX	00000000000000
AND	01	010	0	XXXX	XXXX	XXXX	00000000000000
OR	01	011	0	XXXX	XXXX	XXXX	00000000000000
MOV	01	100	0	XXXX	0000	XXXX	00000000000000
MOD	01	101	0	XXXX	XXXX	XXXX	00000000000000
CMP	01	110	0	0000	XXXX	XXXX	00000000000000

	Data register-immediate							
		ld		Registers				
	Tipo Op I RD RA Immed				Immediate			
	31:30	29:27	26	25:22	21:18	17:0		
ADDI	01	000	1	XXXX	XXXX	xxxxxxxxxxxxxxx		
SUBI	01	001	1	XXXX	XXXX	xxxxxxxxxxxxxxx		
ANDI	01	010	1	XXXX	XXXX	XXXXXXXXXXXXXXXXX		
ORI	01	011	1	XXXX	XXXX	XXXXXXXXXXXXXXXXX		
MOV	01	100	1	XXXX	0000	xxxxxxxxxxxxxxx		
MOD	01	101	1	XXXX	XXXX	xxxxxxxxxxxxxxx		
CMPI	01	110	1	0000	XXXX	XXXXXXXXXXXXXXXXX		

			Memoria						
		ld Addressing					ldressing		
		Type	Op	Null	RD	RA	Immediate		
		31:30	29	28:26	25:22	21:18	17:0		
ĺ	LDR	10	0	000	XXXX	xxxx	XXXXXXXXXXXXXXXX		
Ī	STR	10	1	000	XXXX	XXXX	xxxxxxxxxxxxxxxx		

		Control						
	ld			Null	Instruction number			
	Type	Op	Null	Null	Immediate			
	31:30	29	28:26	25:18	17:0			
JMP	11	0	000	0000000	xxxxxxxxxxxxxxx			
JEQ	11	1	000	0000000	XXXXXXXXXXXXXXXX			