Green sheet ISA Pipeline CPU

Stall
31:0
000000000000000000000000000000000000000

		Data register-register						
	ld			Registers			Null	
	Type	Ор		RD	RA	RB	Null	
	31:30	29:27	26	25:22	21:18	17:14	13:0	
ADD	01	000	0	XXXX	XXXX	XXXX	00000000000000	
SUB	01	001	0	XXXX	XXXX	XXXX	00000000000000	
AND	01	010	0	XXXX	XXXX	XXXX	00000000000000	
OR	01	011	0	XXXX	XXXX	XXXX	00000000000000	
MOV	01	100	0	XXXX	0000	XXXX	00000000000000	
MOD	01	101	0	XXXX	XXXX	XXXX	00000000000000	
EXP	01	110	0	XXXX	XXXX	XXXX	00000000000000	
CMP	01	111	0	0000	XXXX	XXXX	00000000000000	

	Data register-immediate						
		ld		Registers			
	Tipo Op I RD RA Immedi				Immediate		
	31:30	29:27	26	25:22	21:18	17:0	
ADDI	01	000	1	XXXX	XXXX	xxxxxxxxxxxxxx	
SUBI	01	001	1	XXXX	XXXX	xxxxxxxxxxxxxxx	
ANDI	01	010	1	XXXX	XXXX	xxxxxxxxxxxxxxx	
ORI	01	011	1	XXXX	XXXX	xxxxxxxxxxxxxxx	
MOV	01	100	1	xxxx	0000	xxxxxxxxxxxxxxx	
MOD	01	101	1	XXXX	XXXX	xxxxxxxxxxxxxx	
EXP	01	110	1	XXXX	XXXX	xxxxxxxxxxxxxxx	
CMPI	01	111	1	0000	XXXX	XXXXXXXXXXXXXXXX	

	Memoria						
ld					Addressing		
	Type	Op	Null	RD	RA	Immediate	
	31:30	29	28:26	25:22	21:18	17:0	
LDR	10	0	000	XXXX	XXXX	xxxxxxxxxxxxxxx	
STR	10	1	000	XXXX	XXXX	xxxxxxxxxxxxxxx	

				Control		
	ld			Null	Instruction number	
	Туре	Op	Null	Null	Immediate	
	31:30	29:28	27:26	25:18	17:0	
JMP	11	00	00	0000000	xxxxxxxxxxxxxxx	
JEQ	11	01	00	0000000	xxxxxxxxxxxxxxx	
JNE	11	10	00	0000000	XXXXXXXXXXXXXXXX	