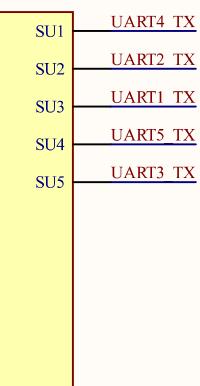
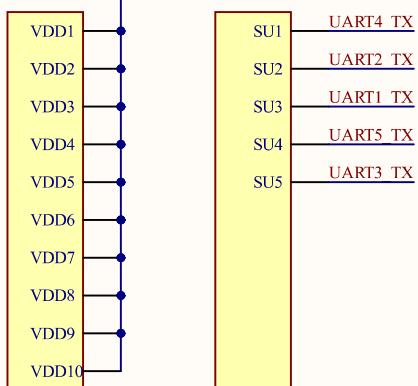
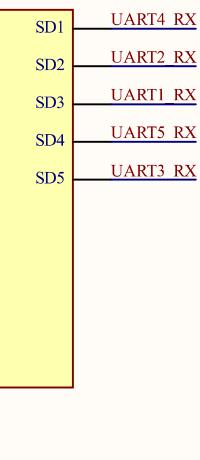
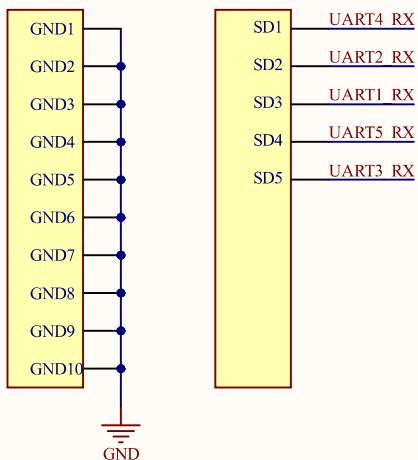


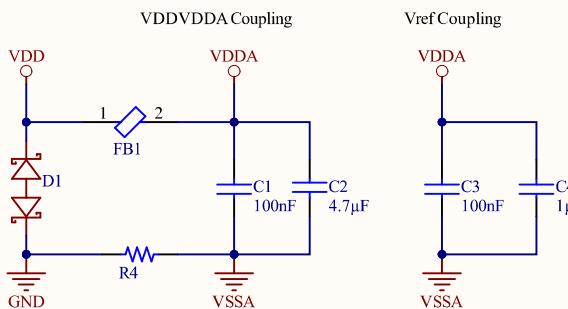
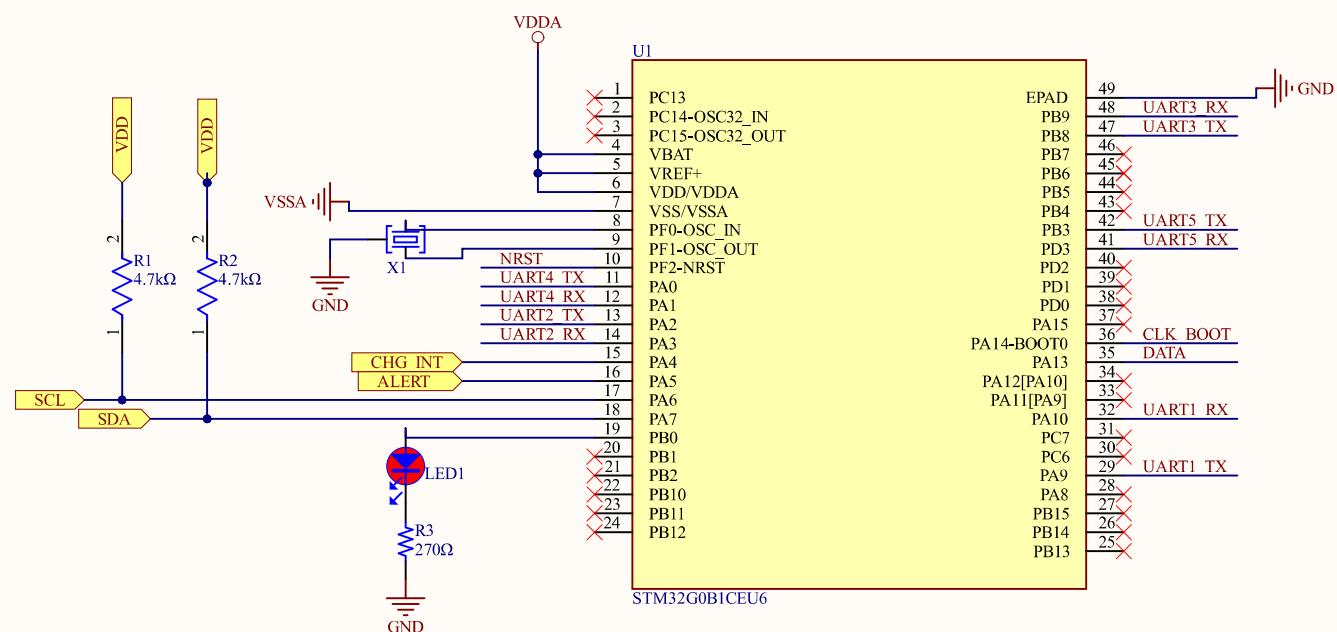
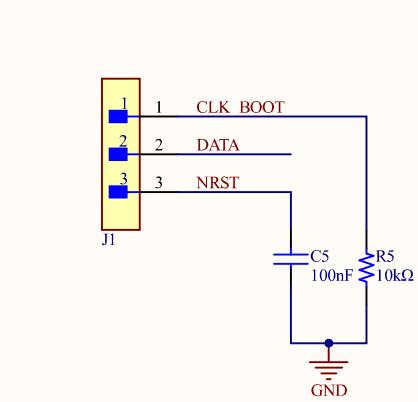
A



B



C



Module: H05R1

Title: H05R1_Backend.SchDoc

Description:
USB-C PD LiPo charger

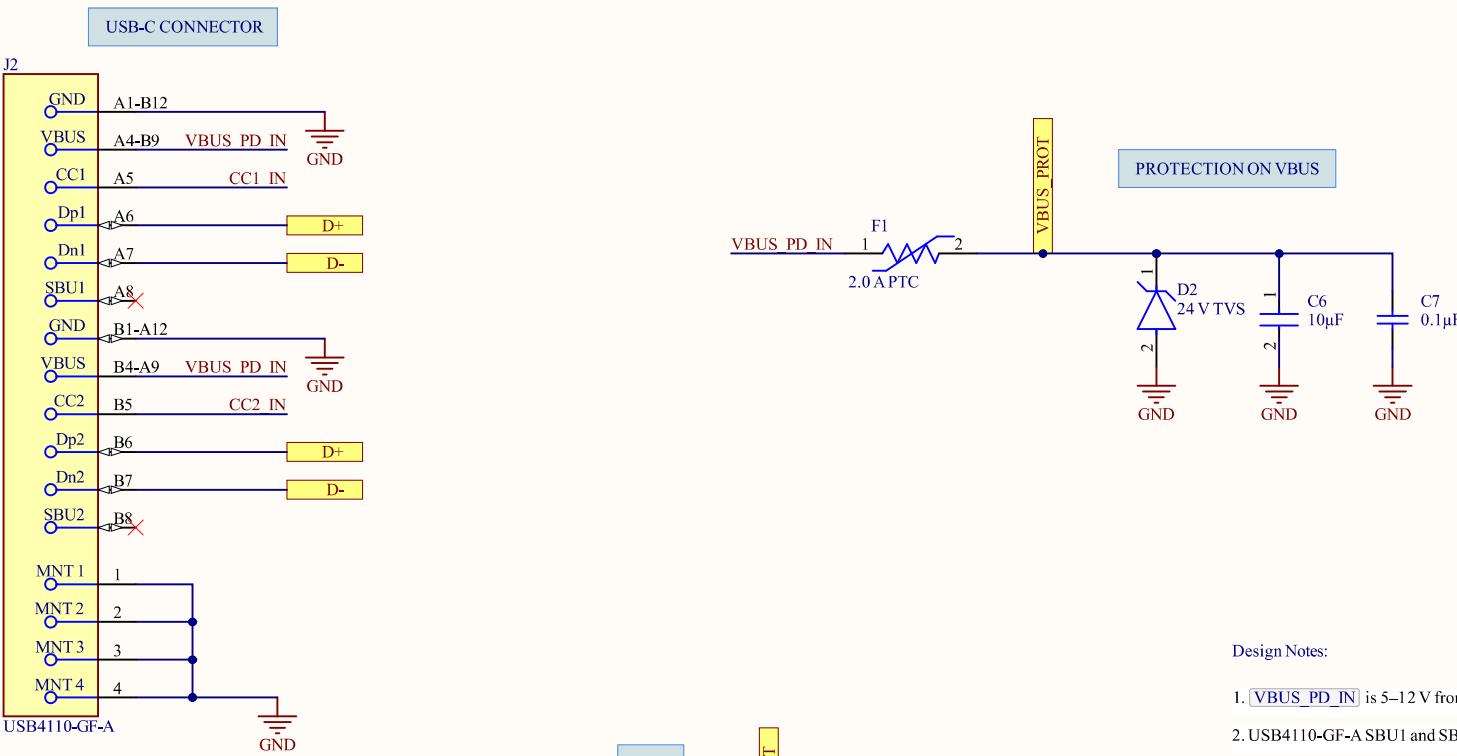
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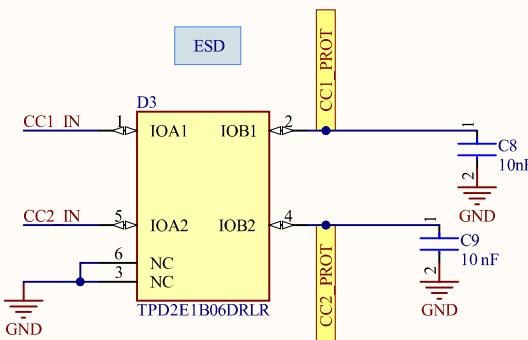
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Time: 11:03:51 AM

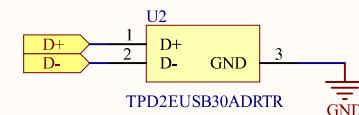
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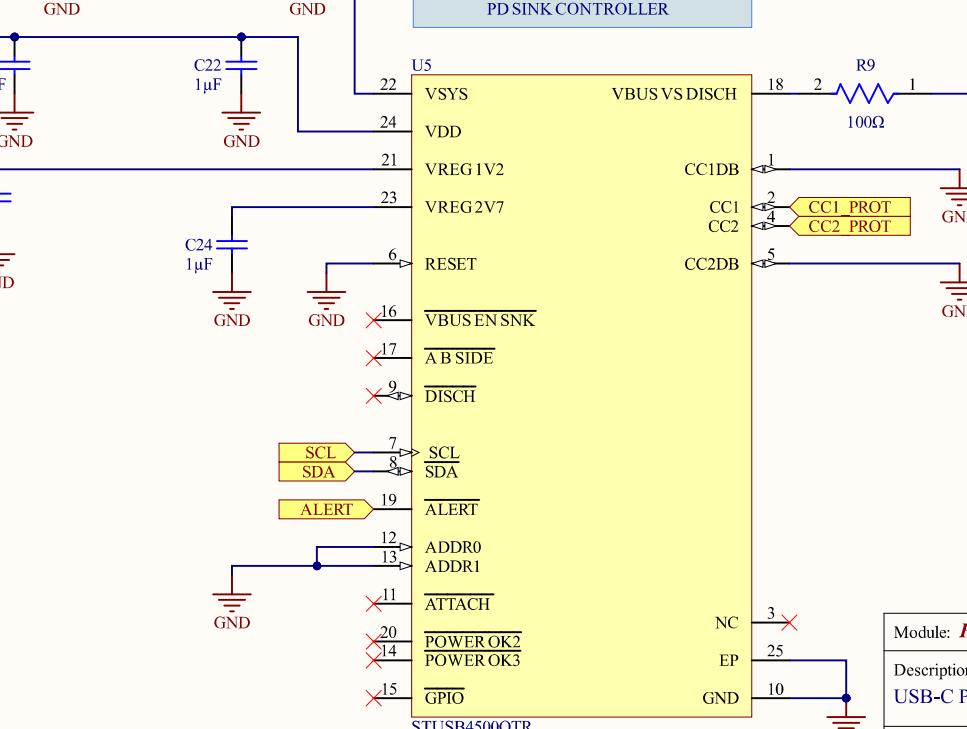
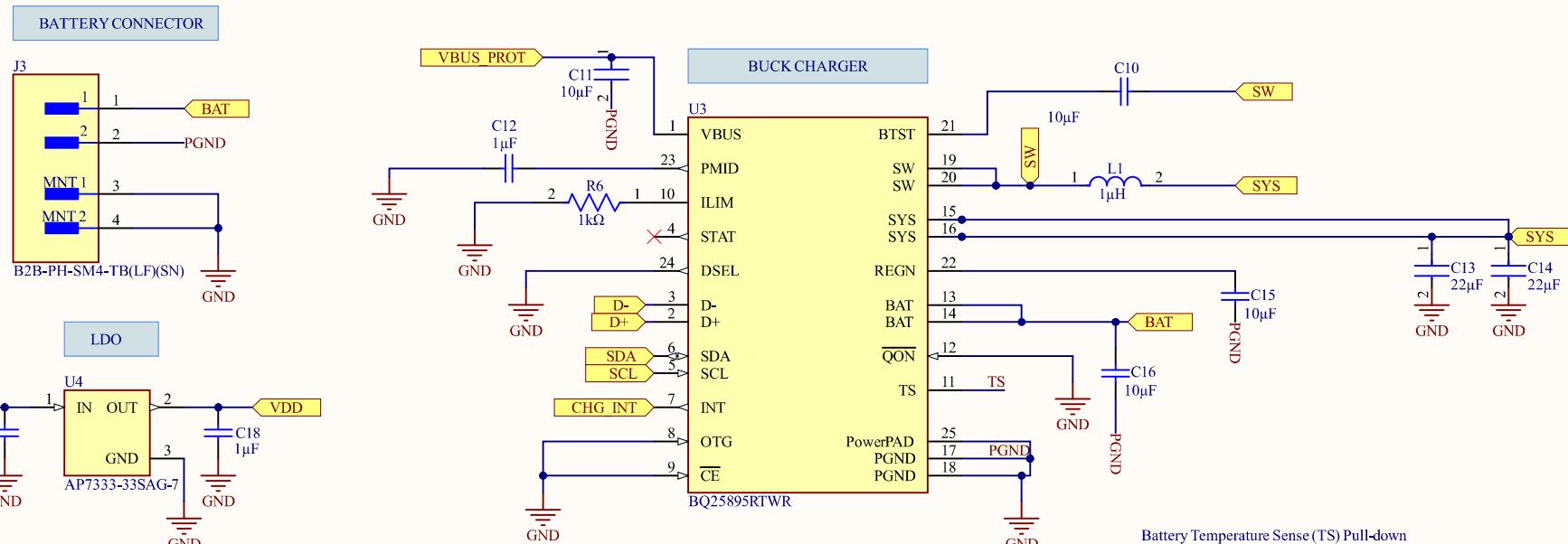
B



C



Module: H05R1	Title: H05R1_Frontend_1.SchDoc	Revision: 0
Description: USB-C PD LiPo charger	Size: Letter	
Date: 11/14/2025	Time: 11:03:52 AM	Sheet 2 of 3

**Design Notes**

1. VBUS_PROT from H05R1_Frontend_1 supplies both the PD controller U4 (VDD / VBUS_VS_DISCH) and the buck charger U3 (VBUS).
2. SYS output of buck charger U3 powers the LDO U6.
3. VDD (3.3 V) from LDO U6 powers the PD controller U4 and the MCU on the H05R1_Backend sheet.
4. CC1_PROT / CC2_PROT from H05R1_Frontend_1 connect directly to CC1/CC2 of U4 for USB-C role detection and PD negotiation.
5. SDA, SCL, ALERT (U4), and CHG_INT (U3) are routed to the MCU within H05R1_Backend to enable PD control and charger status monitoring.

Module: H05R1	Title: H05R1_Frontend_2.SchDoc	
Description: USB-C PD LiPo charger	Revision: 0	
	Size: Letter	
Date: 11/14/2025	Time: 11:03:52 AM	Sheet 3 of 3