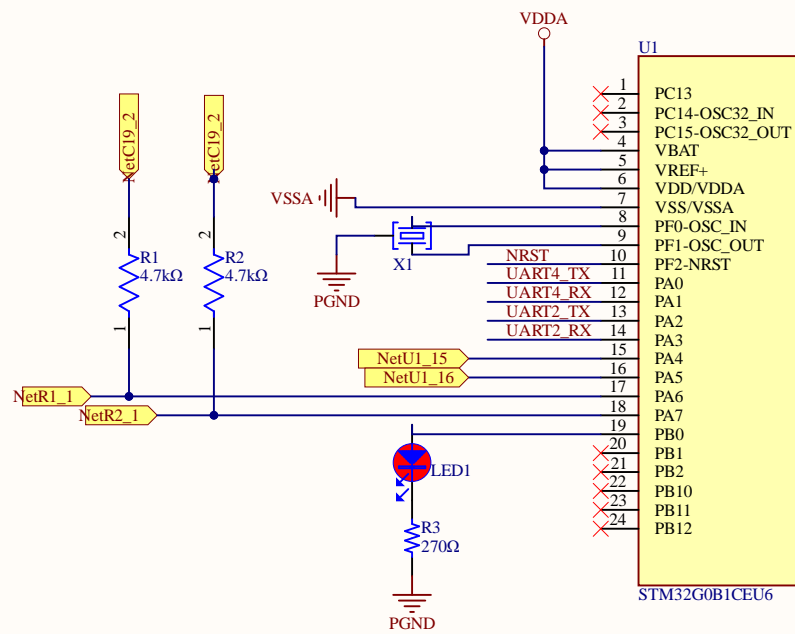
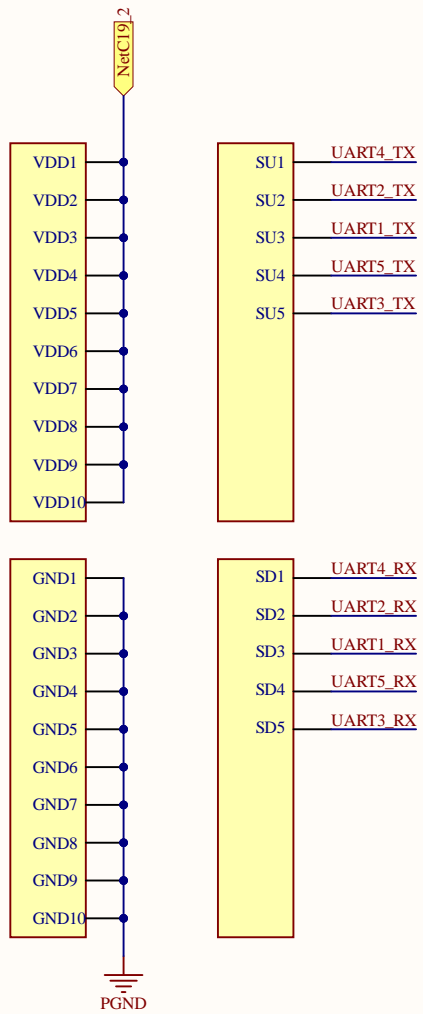


A

B

C

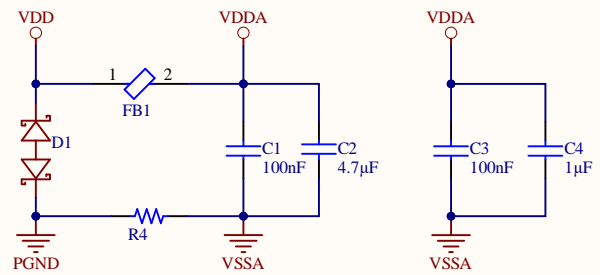


U1		
1	PC13	EPAD
2	PC14-OSC32_IN	PB
3	PC15-OSC32_OUT	PB
4	VBAT	PB
5	VREF+	PB
6	VDD/VDDA	PB
7	VSS/VSSA	PB
8	PF0-OSC_IN	PB
9	PF1-OSC_OUT	PD
10	PF2-NRST	PD
11	PA0	PD
12	PA1	PD
13	PA2	PA1
14	PA3	PA14-BOOT
15	PA4	PA1
16	PA5	PA12[PA10]
17	PA6	PA11[PA9]
18	PA7	PA1
19	PB0	PC
20	PB1	PC
21	PB2	PA
22	PB10	PA
23	PB11	PB1
24	PB12	PB1

STM32G0B1CEU6

VDD/VDDA Coupling

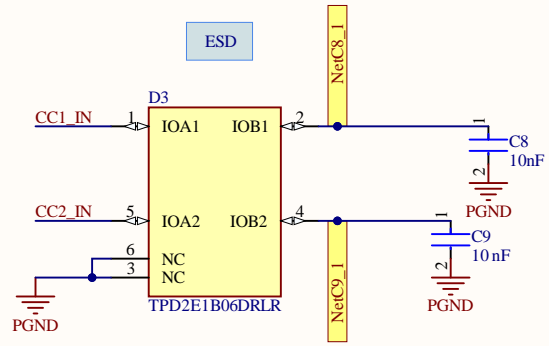
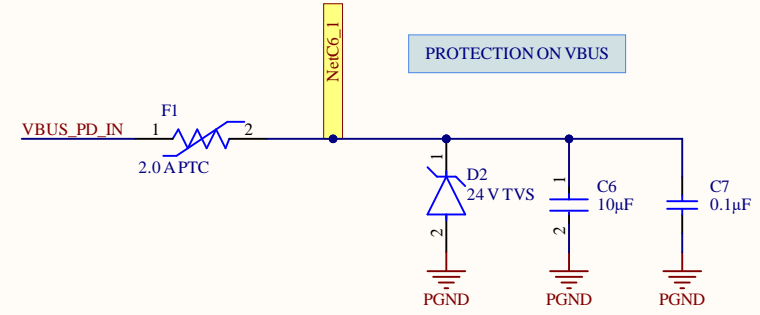
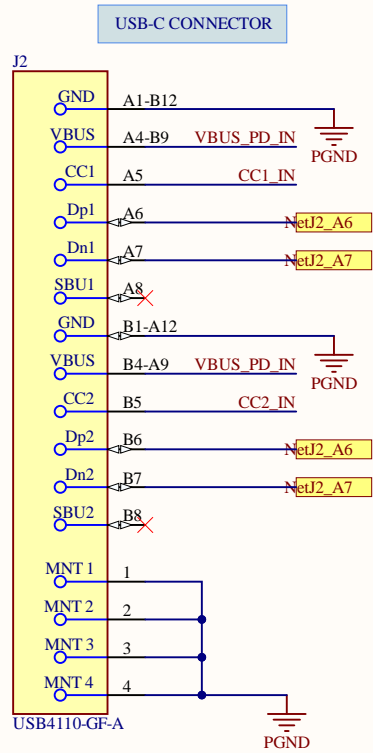
Vref Coupling



A

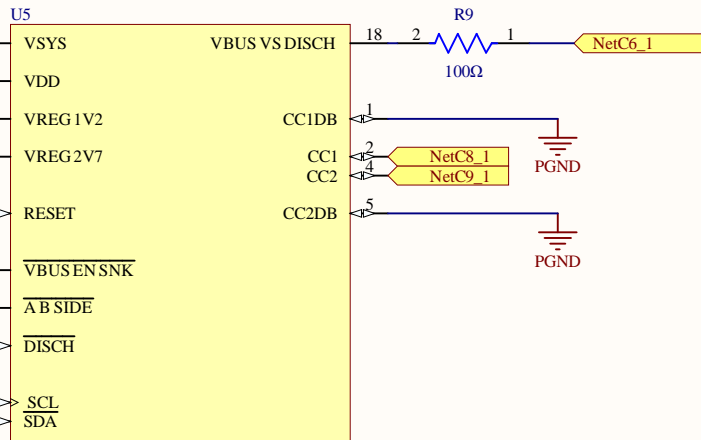
B

C

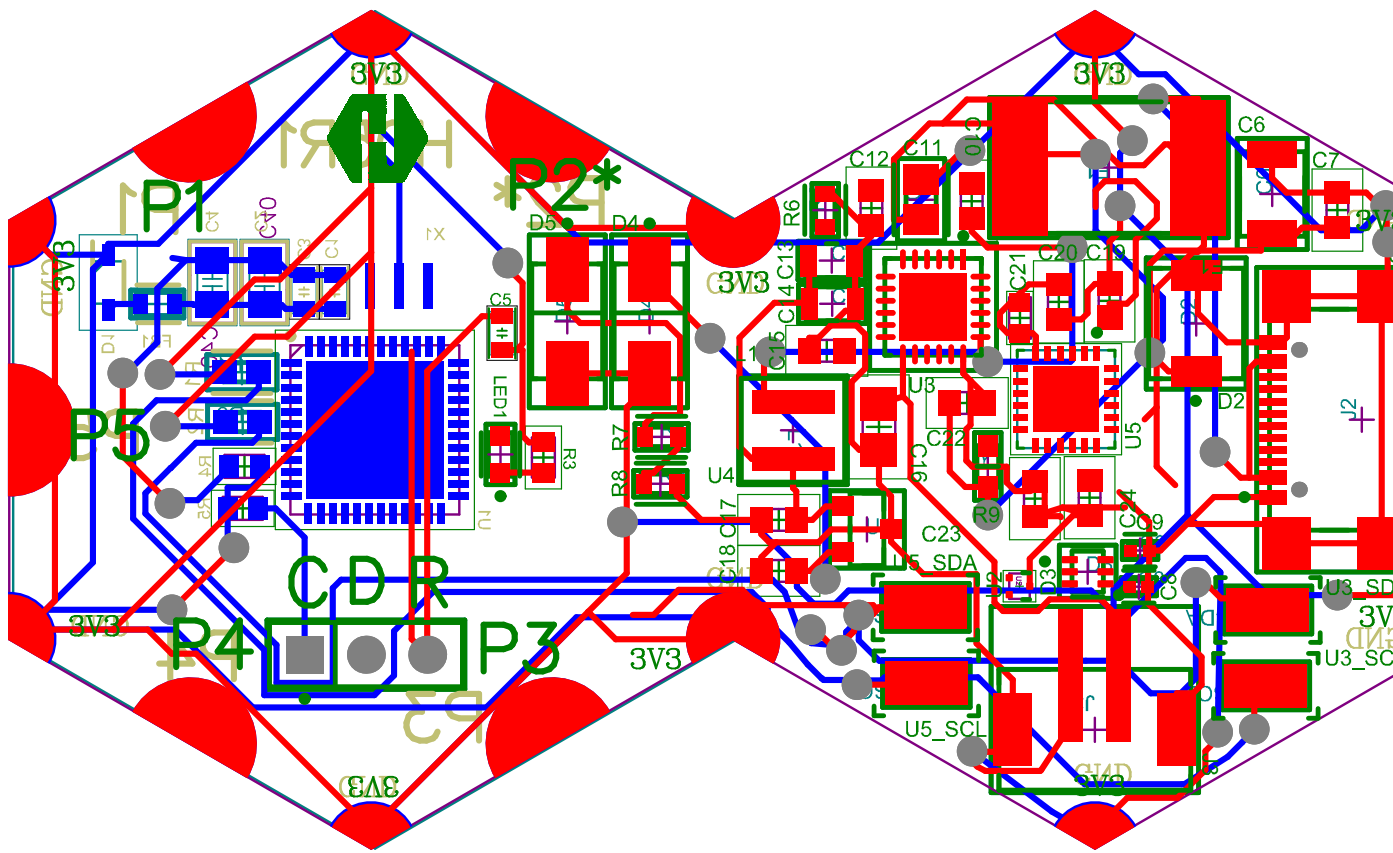


Design Notes:

1. **VBUS_PD_IN** is 5–12 V from U
2. USB4110-GF-A SBU1 and SBU2
3. For the TPD2E1B06, pins labeled to VCC per TI datasheet guidance. In a low-inductance return path and im
4. VBUS_PROT routed to USB-C P VIN/VSYS pin and to buck charger HO5R1_Frontend_2.
5. CC1_PROT routed to CC1 pin of on HO5R1_Frontend_2.
6. CC2_PROT routed to CC2 pin of on HO5R1_Frontend_2.
7. D+ routed to DP pin of buck charger for USB BC1.2 detection.
8. D- routed to DM pin of buck charger for USB BC1.2 detection.



5. SDA, SCL, ALERT



Board Stack Rep