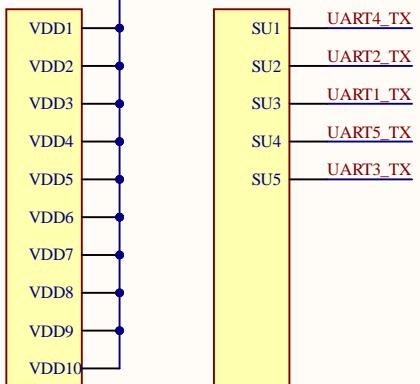


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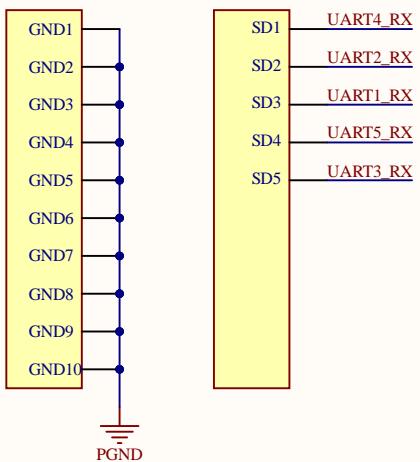
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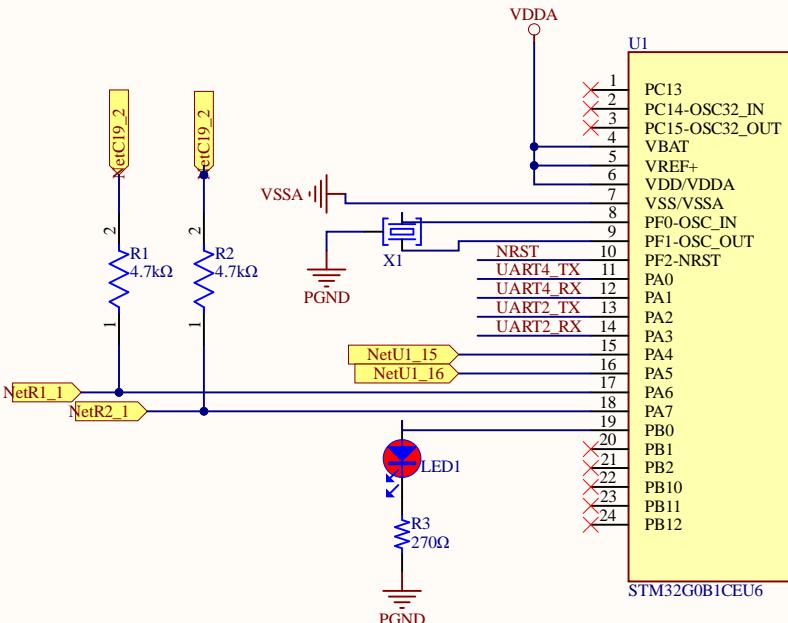
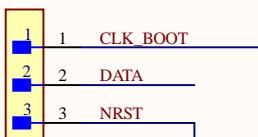
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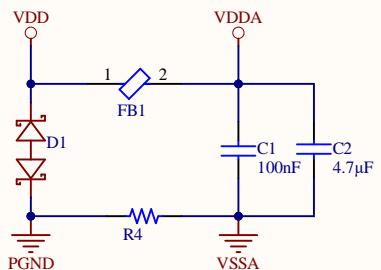
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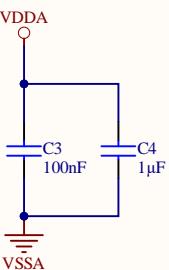
C



VDD/VDDA Coupling

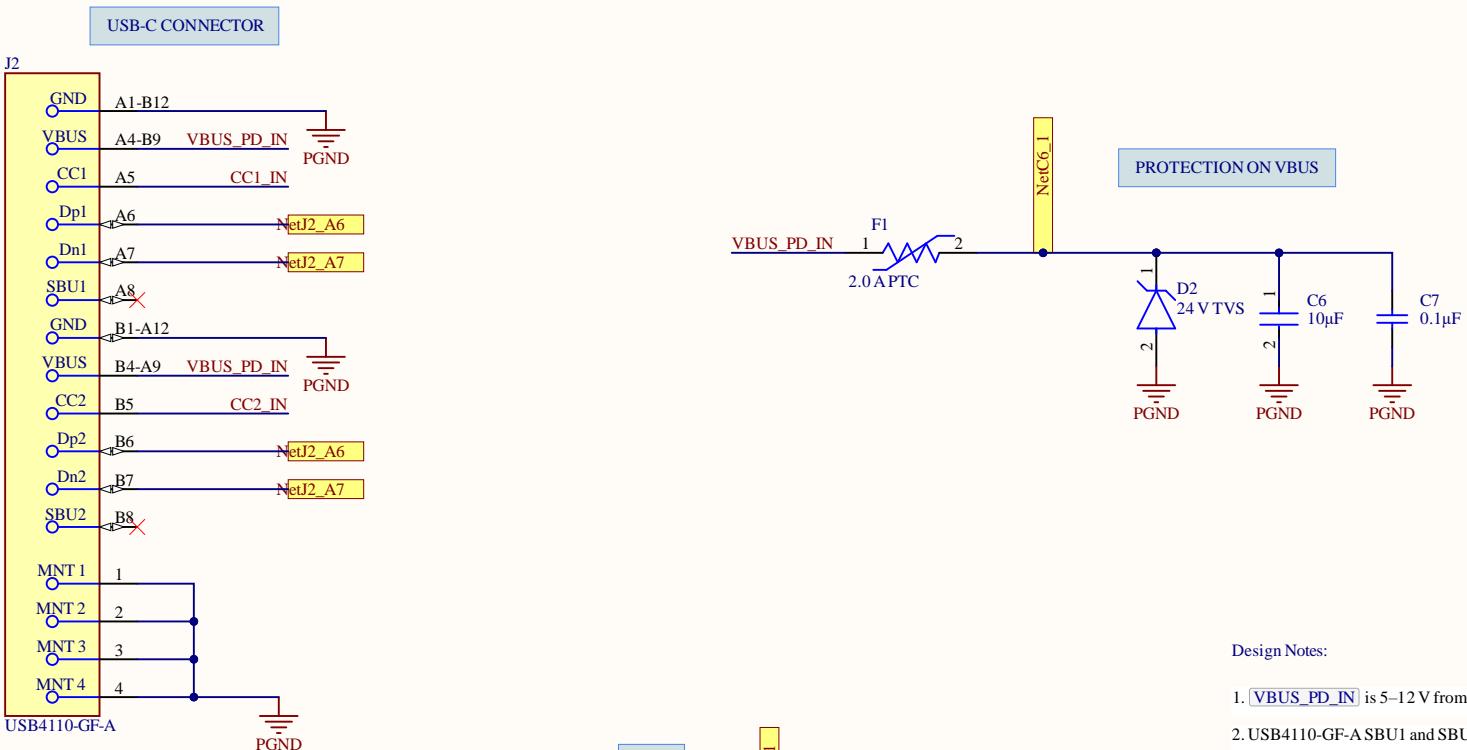


Vref Coupling

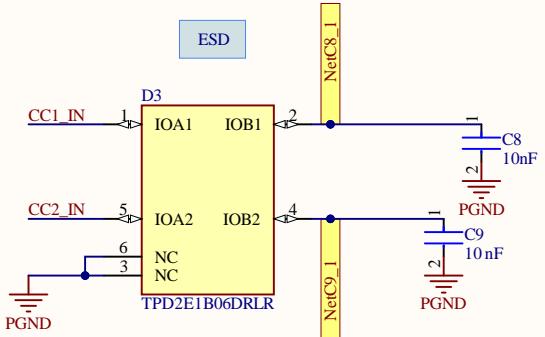


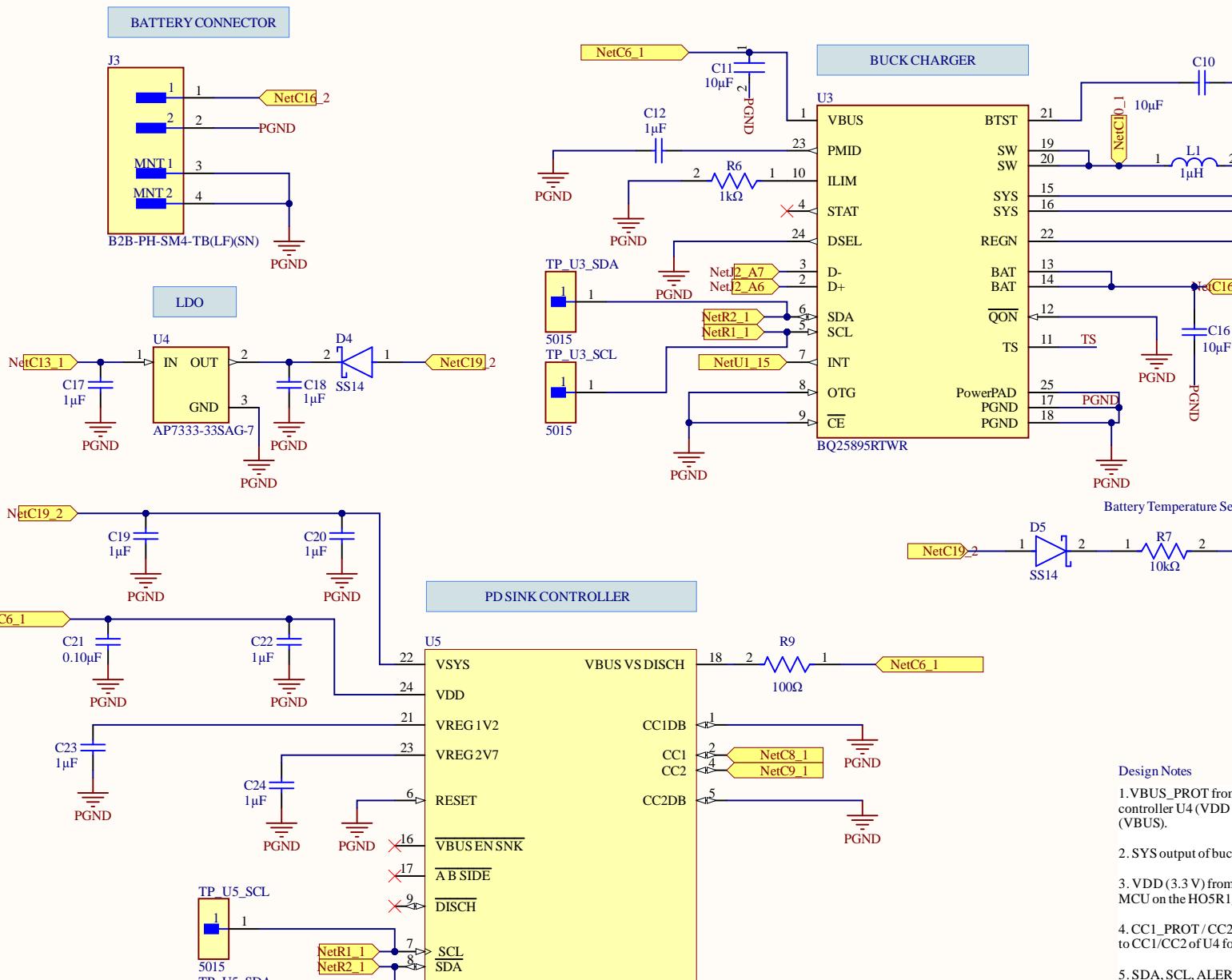
PA14-BOOT	
PA1	PB
PA12[PA10]	PB
PA11[PA9]	PB
PA1	PC
PA1	PC
PA1	PA
PA1	PA
PB10	PB1
PB11	PB1
PB12	PB1

A



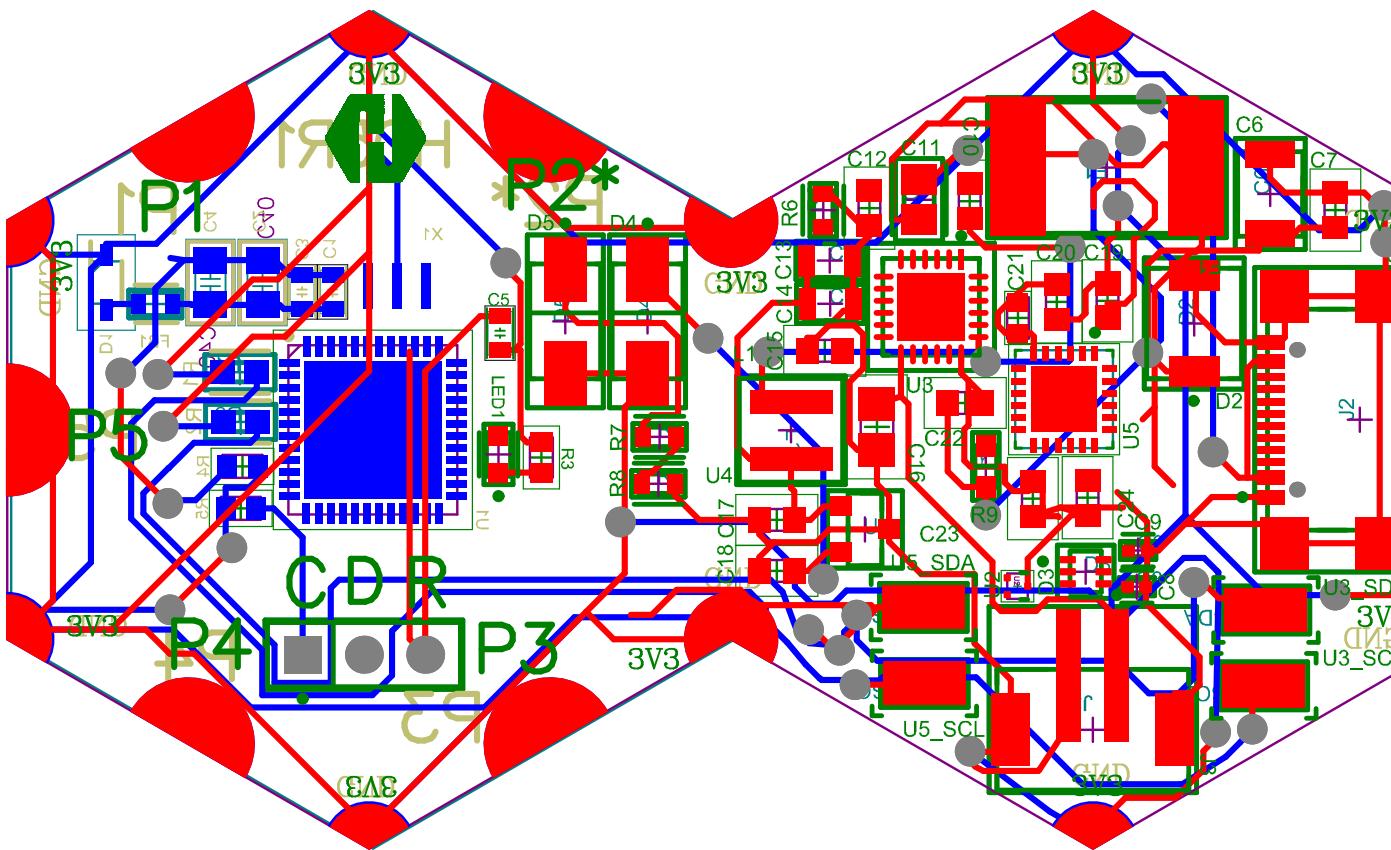
C





Design Notes

1. VBUS_PROT from controller U4 (VDD (VBUS)).
 2. SYS output of buc
 3. VDD (3.3 V) from MCU on the HO5R1
 4. CC1_PROT/CC2 to CC1/CC2 of U4 for
 5. SDA, SCL, ALERT



Board Stack Repo