

VDD=3.3 Volts

P1
HEXABIT__NO_P1_P5_P6_P7_P12



GND

USART2*_TX

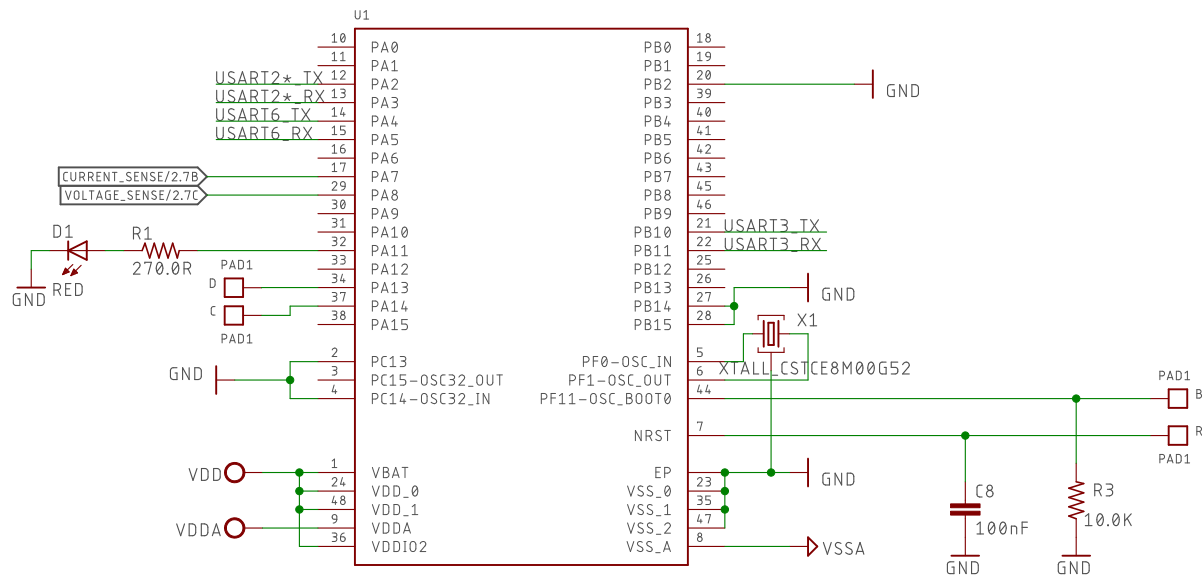
USART6_TX

USART3_TX

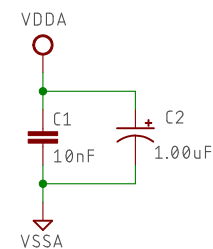
USART2*_RX

USART6_RX

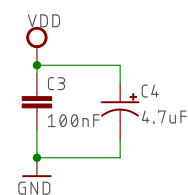
USART3_RX



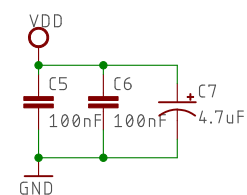
VDDA Coupling



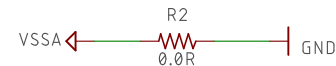
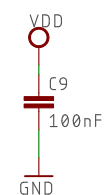
VDDIO2 Coupling



VDD Coupling

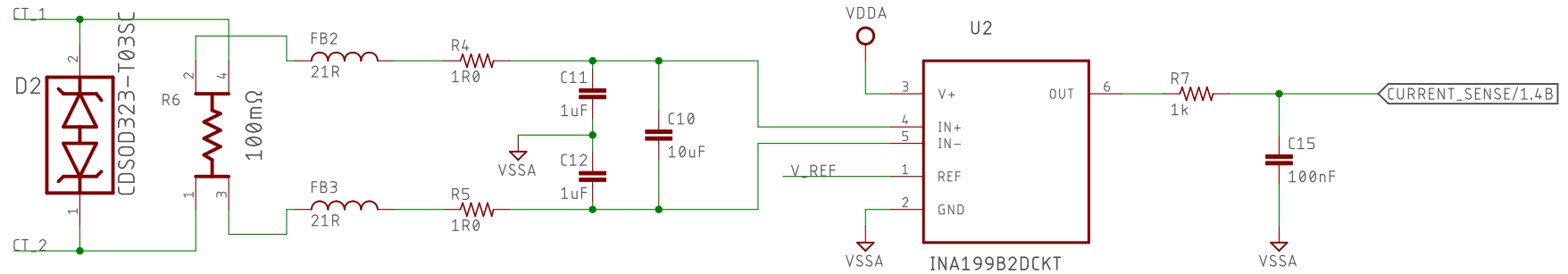


VBAT

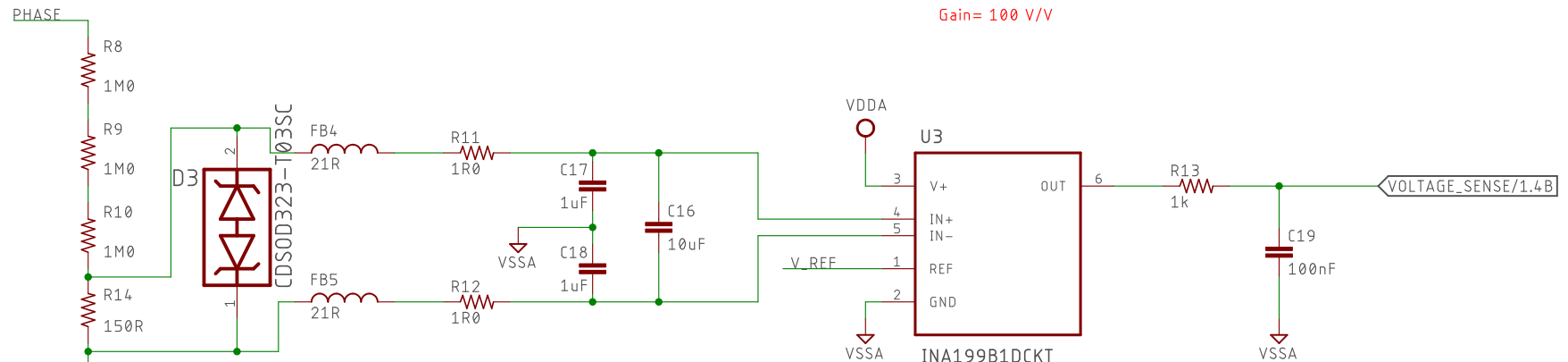


Module	H2AR3	Title	H2AR3_backend
Description	Current and Voltage Sense Module		Revision 0
Author	Amir_Tabbakh	Size	Letter
		Sheet	1 of 2

VDD=3.3 Volts

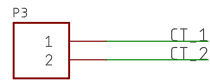


Gain= 100 V/V



Gain = 50 V/V

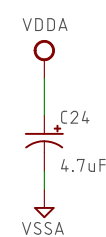
CT_connector



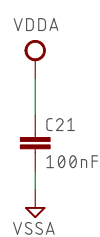
Voltage_connector



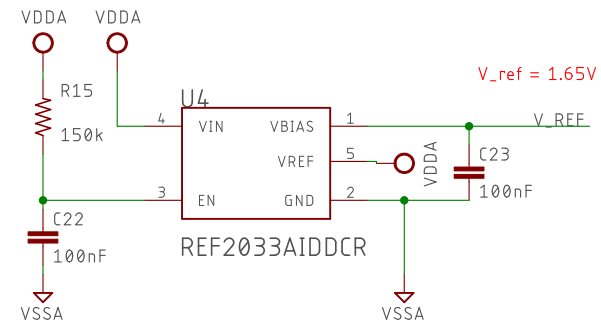
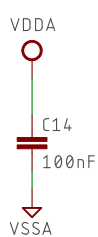
U4_filter



U3_filter



U2_filter



Module	H2AR3	Title	H2AR3_Frontend
Description	Current and Voltage Sense Module		Revision 0
Author	Amir_Tabakh	Size	Letter
		Sheet	2 of 2