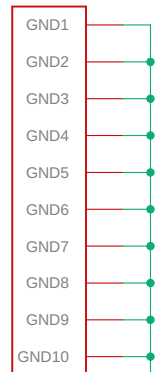
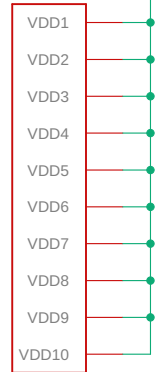


VDD=3.3 Volts

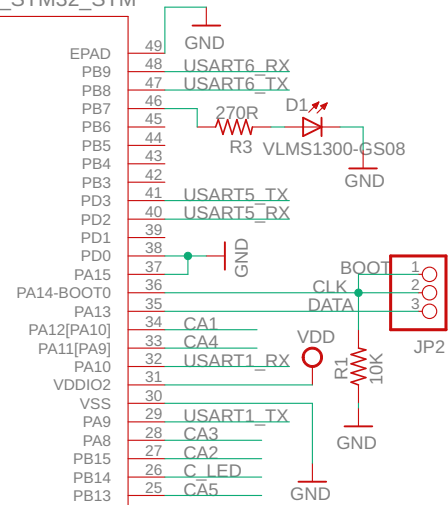
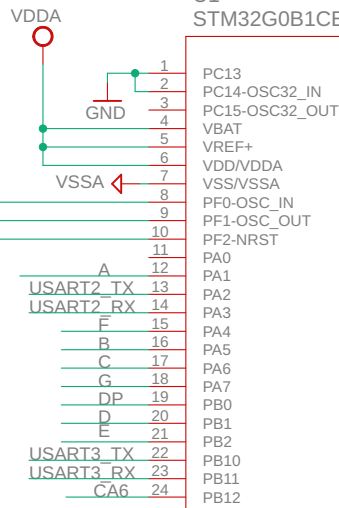
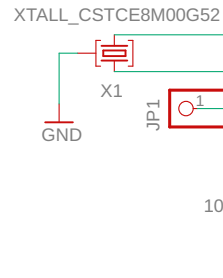
P2  
HEXABIT\_X2\_P1\_P5



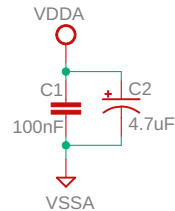
GND

SU1 USART6\_TX  
SU2 USART2\_TX  
SU3 USART3\_TX  
SU4 USART1\_TX  
SU5 USART5\_TX

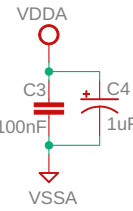
SD1 USART6\_RX  
SD2 USART2\_RX  
SD3 USART3\_RX  
SD4 USART1\_RX  
SD5 USART5\_RX



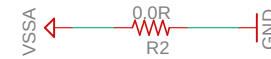
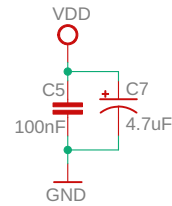
VDDVDDA Coupling



Vref Coupling



VDDIO2 Coupling



Module H3BR61	Title H3BR61_Frontend
Description 6-Digit 7-Segment Display	Revision 0 Size Letter
Author Ibrahim Zaeiter	Sheet 1 of 2



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