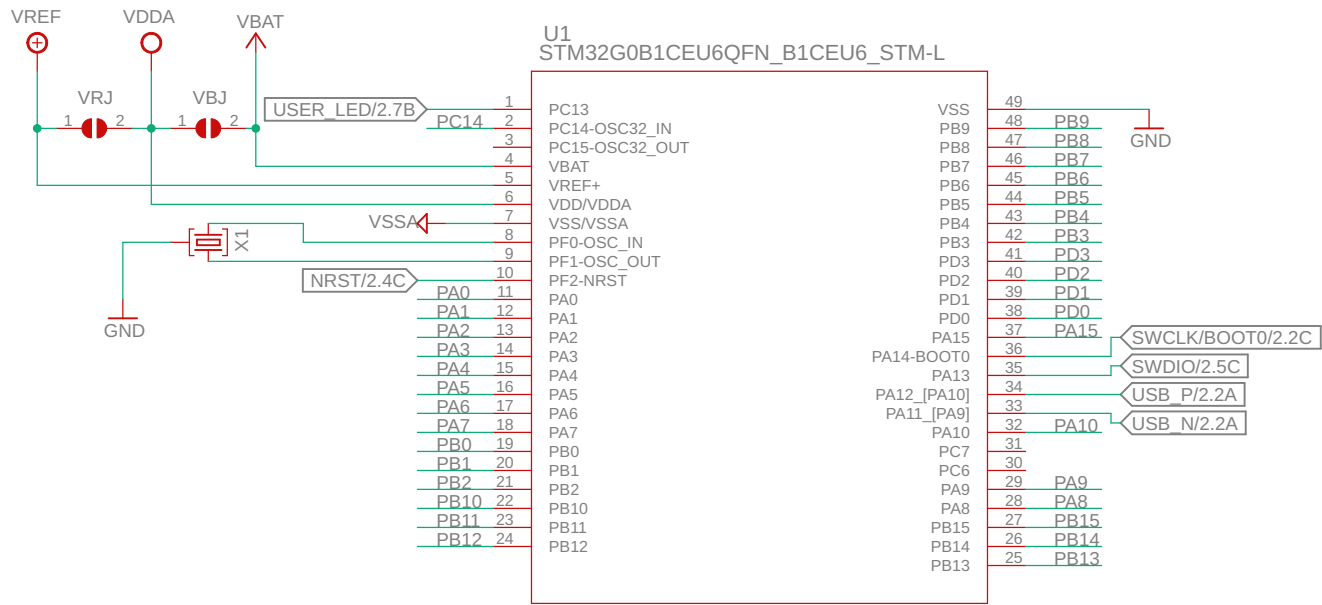


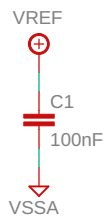
1	+5V
2	GND
3	VDD
4	VDDA
5	VREF
6	VBAT
7	VSSA
8	PC14
9	PB9
10	PB8
11	PB7
12	PB6
13	PB5
14	PB4
15	PB3
16	PD3
17	PD2
18	PD1
19	PD0
20	PA15

1	PA10
2	PA9
3	PA8
4	PB15
5	PB14
6	PB13
7	PB12
8	PB11
9	PB10
10	PB2
11	PB1
12	PB0
13	PA7
14	PA6
15	PA5
16	PA4
17	PA3
18	PA2
19	PA1
20	PA0

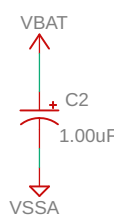
3V3 = VDD
AV = VDDA
AG = VSSA
VR = VREF
VB = VBAT



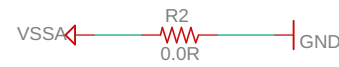
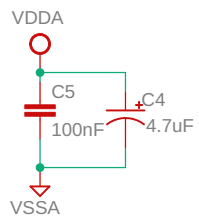
Vref Coupling



Vbat Coupling



VDDA Coupling



Title HG0B1CEU		Revision 1 Size Letter Sheet 1 of 2
Description STM32G0B1CEU6 DevKit		
Author		