

EL-213: Computer Organization & Assembly Language Lab

Lab 6: <i>Conditional Processing</i>	Session: Fall 2019
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Boolean Instructions

AND

Boolean AND operation between a source operand and destination operand.

Syntax:

```
AND reg, reg
AND reg, mem
AND reg, imm
AND mem, reg
AND mem, imm
```

OR

Boolean OR operation between a source operand and destination operand.

Syntax:

```
OR reg, reg
OR reg, mem
OR reg, imm
OR mem, reg
OR mem, imm
```

XOR

Boolean XOR operation between a source operand and destination operand.

Syntax:

```
XOR reg, reg
XOR reg, mem
XOR reg, imm
XOR mem, reg
XOR mem, imm
```

NOT

Boolean NOT operation on a destination operand.

Syntax:

```
NOT reg
NOT mem
```

TEST

Similar to AND operation, except that instead of affecting any operands it sets the FLAGS appropriately.

Syntax:

```
TEST reg, reg
TEST reg, mem
TEST reg, imm
TEST mem, reg
TEST mem, imm
```

Example 1:

.code

```
mov    al, 10101110b    ; Clear only bit 3
and    al, 11110110b    ; AL = 10100110

mov    al, 11100011b    ; set bit 2
or     al, 00000100b    ; AL = 11100111

mov    al, 10110101b    ; 5 bits means odd parity
xor    al, 0            ; PF = 0 (PO)
```

```

mov    al, 10100101b           ; 4 bits means even parity
xor    al, 0                    ; PF = 1 (PE)

mov    al, 11110000b
not    al                       ; AL = 00001111b

mov    al, 00100101b
test   al, 00001001b           ; ZF = 0

mov    al, 00100101b
test   al, 00001000b           ; ZF = 1

call   DumpRegs
exit

```

Set Operations (using Boolean instructions)

Set Complement

The complement of a set can be achieved through NOT instruction.

Set Intersection

The intersection of two sets can be achieved through AND instruction.

Set Union

The union of two sets can be achieved through OR instruction.

Example 2:

[illegible]

CMP instruction

CMP (compare) instruction performs an implied subtraction of a source operand from a destination operand for comparison.

For unsigned operands:

- Destination < source ZF = 0 CF = 1
- Destination > source ZF = 0 CF = 0
- Destination = source ZF = 1 CF = 0

For signed operands:

- Destination < source SF != OF
- Destination > source SF = OF
- Destination = source ZF = 1

Example 3:

.code

```
mov    ax, 5
cmp    ax, 10      ; ZF = 0      and    CF = 1
mov    ax, 1000
cmp    ax, 1000    ; ZF = 1      and    CF = 0
mov    si, 106
cmp    si, 0       ; ZF = 0      and    CF = 0
exit
```

Conditional Jumps

Jumps based on Flag values

Mnemonic	Description	Flags / Registers
JZ	Jump if zero	ZF = 1
JNZ	Jump if not zero	ZF = 0
JC	Jump if carry	CF = 1
JNC	Jump if not carry	CF = 0
JO	Jump if overflow	OF = 1
JNO	Jump if not overflow	OF = 0
JS	Jump if signed	SF = 1
JNS	Jump if not signed	SF = 0
JP	Jump if parity (even)	PF = 1
JNP	Jump if not parity (odd)	PF = 0

Jumps based on Equality

Mnemonic	Description
JE	Jump if equal (<i>leftOp</i> = <i>rightOp</i>)
JNE	Jump if not equal (<i>leftOp</i> ≠ <i>rightOp</i>)
JCXZ	Jump if CX = 0
JECXZ	Jump if ECX = 0

Jumps based on Unsigned Comparisons

Mnemonic	Description
JA	Jump if above (if $leftOp > rightOp$)
JNBE	Jump if not below or equal (same as JA)
JAЕ	Jump if above or equal (if $leftOp \geq rightOp$)
JNB	Jump if not below (same as JAE)
JB	Jump if below (if $leftOp < rightOp$)
JNAE	Jump if not above or equal (same as JB)
JBE	Jump if below or equal (if $leftOp \leq rightOp$)
JNA	Jump if not above (same as JBE)

Jumps based on Signed Comparisons

Mnemonic	Description
JG	Jump if greater (if $leftOp > rightOp$)
JNLE	Jump if not less than or equal (same as JG)
JGE	Jump if greater than or equal (if $leftOp \geq rightOp$)
JNL	Jump if not less (same as JGE)
JL	Jump if less (if $leftOp < rightOp$)
JNGE	Jump if not greater than or equal (same as JL)
JLE	Jump if less than or equal (if $leftOp \leq rightOp$)
JNG	Jump if not greater (same as JLE)

Example 4:

; This program compares and finds larger of the two integers

```
.data
    var1 DWORD 250
    var2 DWORD 125
    larger DWORD ?

.code
    mov     eax, var1
    mov     larger, eax
    mov     ebx, var2
    cmp     eax, ebx
    jae     L1
    mov     larger, ebx
L1:
    exit
```

Example 5:

; This program compares and finds smallest of the three integers

```
.data
    var1 DWORD 50
    var2 DWORD 25
    var3 DWORD 103
    msg BYTE "The smallest integer is: ", 0

.code
    mov     eax, var1
    cmp     eax, var2
    jbe     L1
    mov     eax, var2
L1:
    cmp     eax, var3
```

```

jbe    L2
mov    eax, var3
L2:
mov    edx, OFFSET msg
call   WriteString
call   WriteDec
exit

```

Example 6:

; The following program continues a loop until an alphanumeric key is pressed

```

.data
    char BYTE ?

.code
L1:
mov    eax, 10                ; create 10ms delay
call   Delay
call   ReadKey                ; reads a key input
jz     L1                     ; repeat if no key is pressed
mov    char, al               ; saves the character
exit

```

MUL Instructions:

The **MUL** instruction is for unsigned multiplication. Operands are treated as unsigned numbers.

Multiplicand	Multiplier	Product
AL	reg/mem8	AX
AX	reg/mem16	DX:AX
EAX	reg/mem32	EDX:EAX

Syntax: *MUL source*

EXAMPLE:

```

mov    eax, 12345h
mov    ebx, 1000h
mul    ebx                    ; EDX:EAX = 0000000012345000h, CF = 0

```

DIV Instructions:

The **DIV** (unsigned divide) instruction performs 8-bit, 16-bit, and 32-bit unsigned integer division. The single register or memory operand is the divisor.

Dividend	Divisor	Quotient	Remainder
AX	reg/mem8	AL	AH
DX:AX	reg/mem16	AX	DX
EDX:EAX	reg/mem32	EAX	EDX

Syntax: *DIV source*

```

mov    dx, 0                  ; clear dividend, high
mov    ax, 8003h              ; dividend, low
mov    cx, 100h               ; divisor
div    cx                     ; AX = 0080h, DX = 0003h

```

Activity:

Use cmp and jumps to find the first non-zero value in the given array:

```
intArr  WORD    0, 0, 0, 0, 1, 20, 35, -12, 66, 4, 0
```

Write a program that takes four input integers from the user. Then compare and display a message whether these integers are equal or not.

Write a program for sequential search. Take an input from the user and find if it occurs in the following array:

```
arr      WORD 10, 4, 7, 14, 299, 156, 3, 19, 29, 300, 20
```

Translate the following pseudo-code to Assembly Language:

1. Swap_Count = 0
for all elements of list
 if list[i] > list[i+1]
 swap(list[i], list[i+1])
 Swap_Count = Swap_Count + 1
 end if
end for
Print Swap_Count
2. var = 5
if (var < ecx) AND (ecx >= edx) then
 x = 0
else
 x = 1
3. var = 0
while(var <= 10)
 if (var % 2 == 0)
 Print "Hello"
 else
 Print "World"
var = var + 1
end while