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Version Control		
Version	Date	Modifications
V0.1	01/2025	

Design Engineer: Milan Pavlovic		
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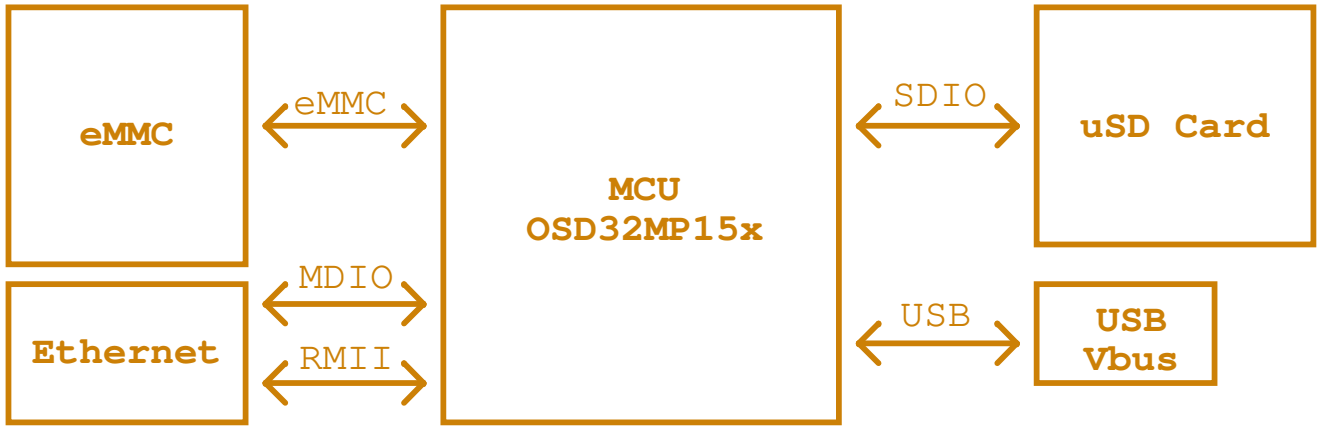
Block Diagram



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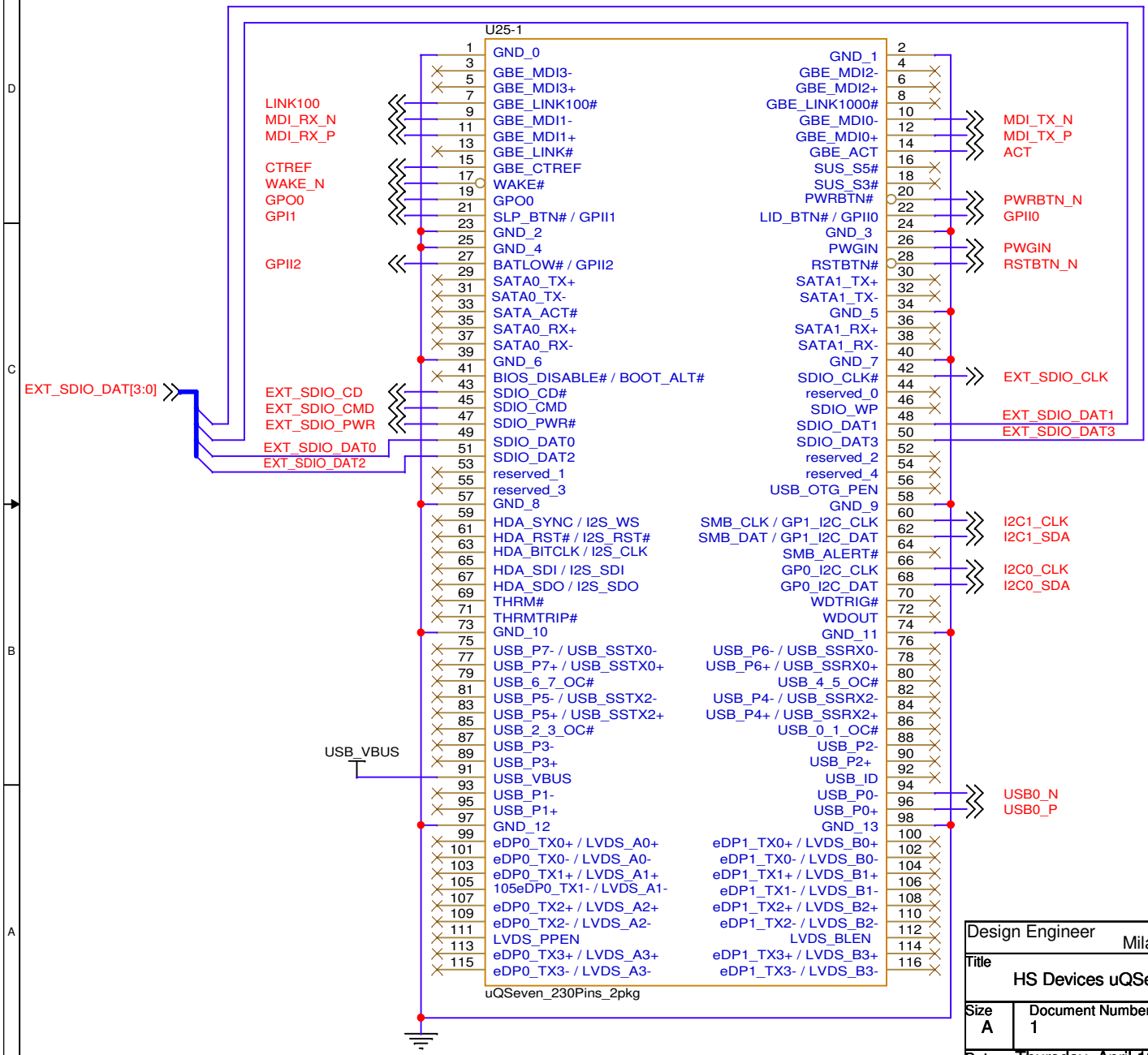
Connectors 1



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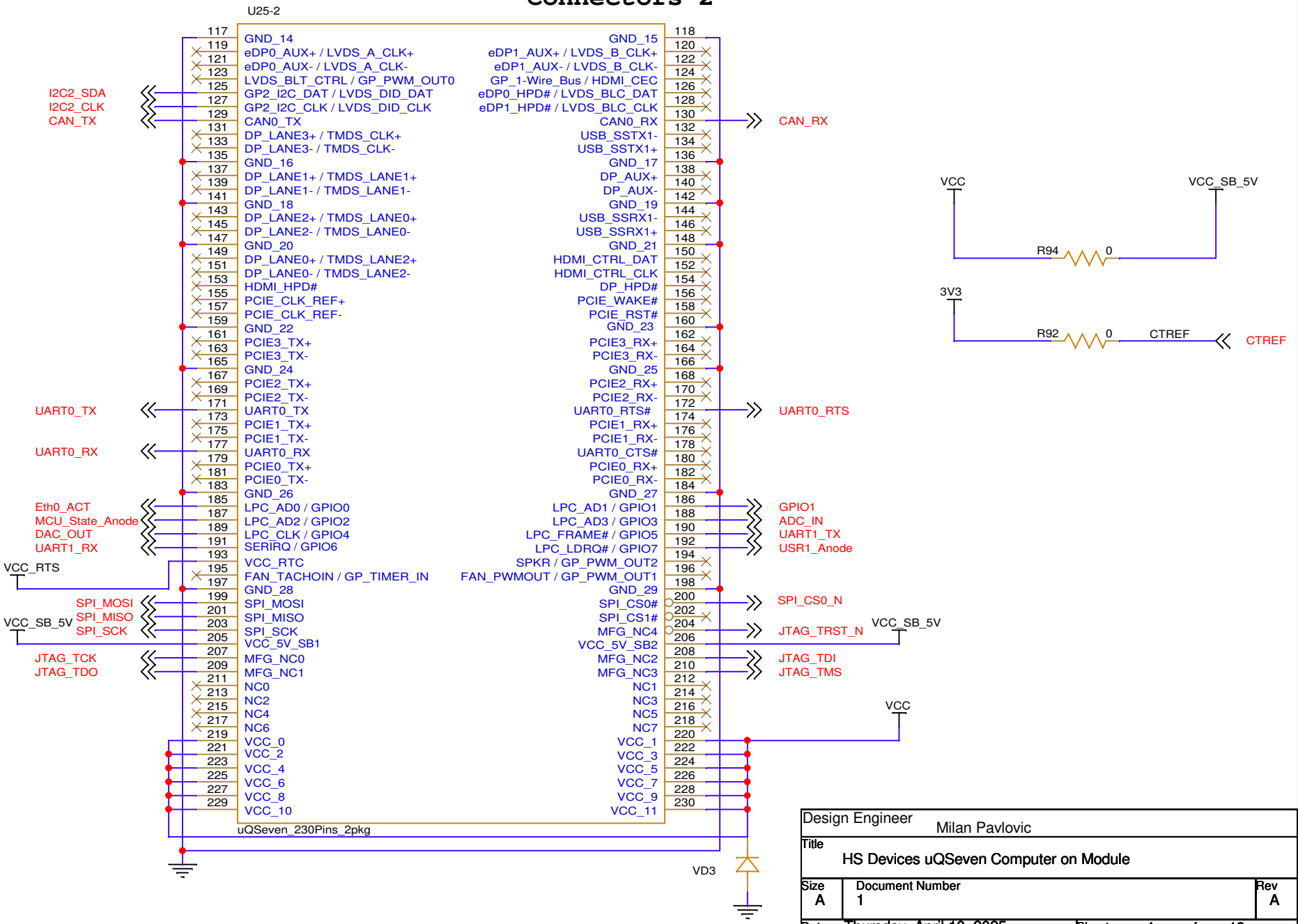
Connectors 2



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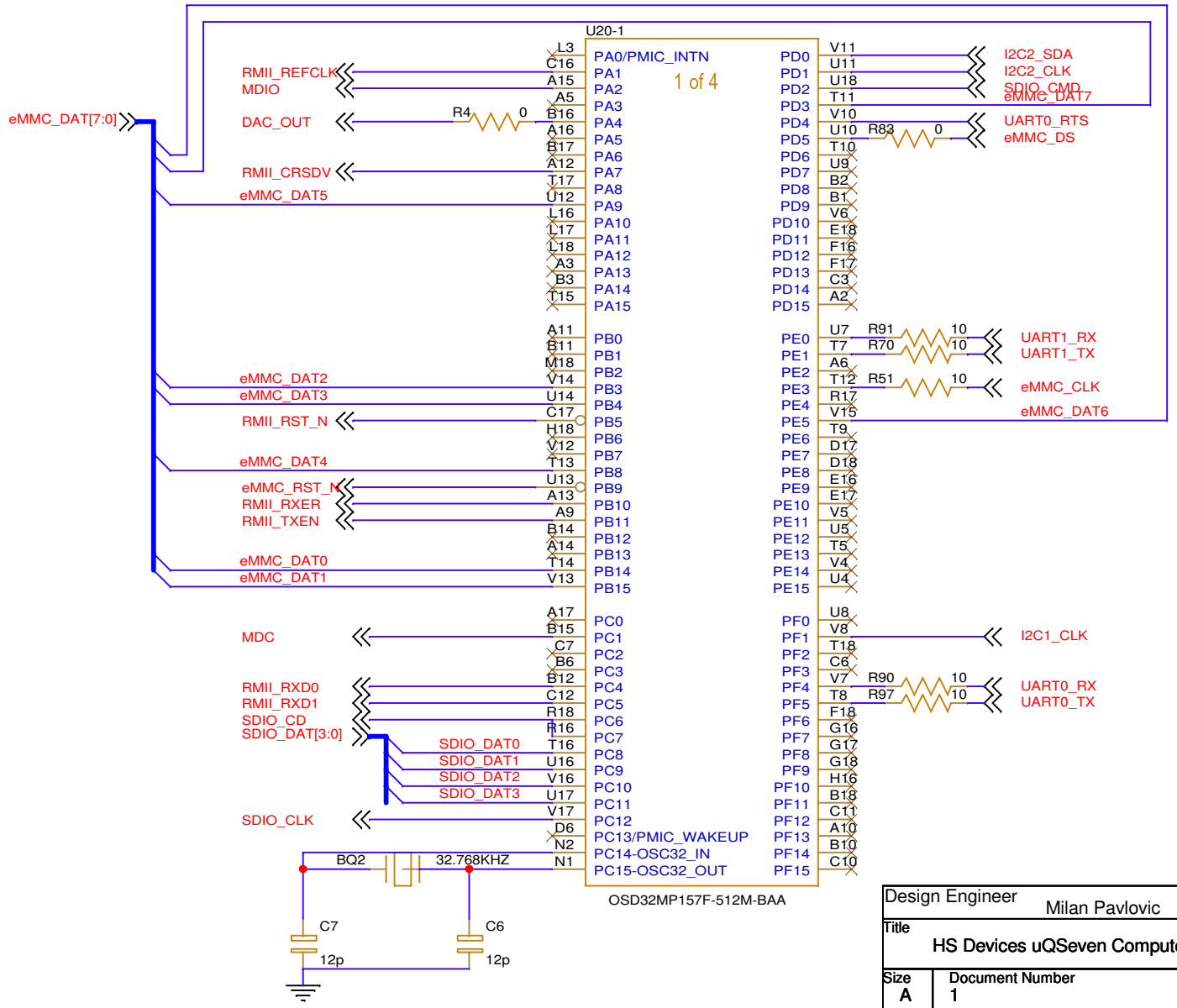
CPU 1



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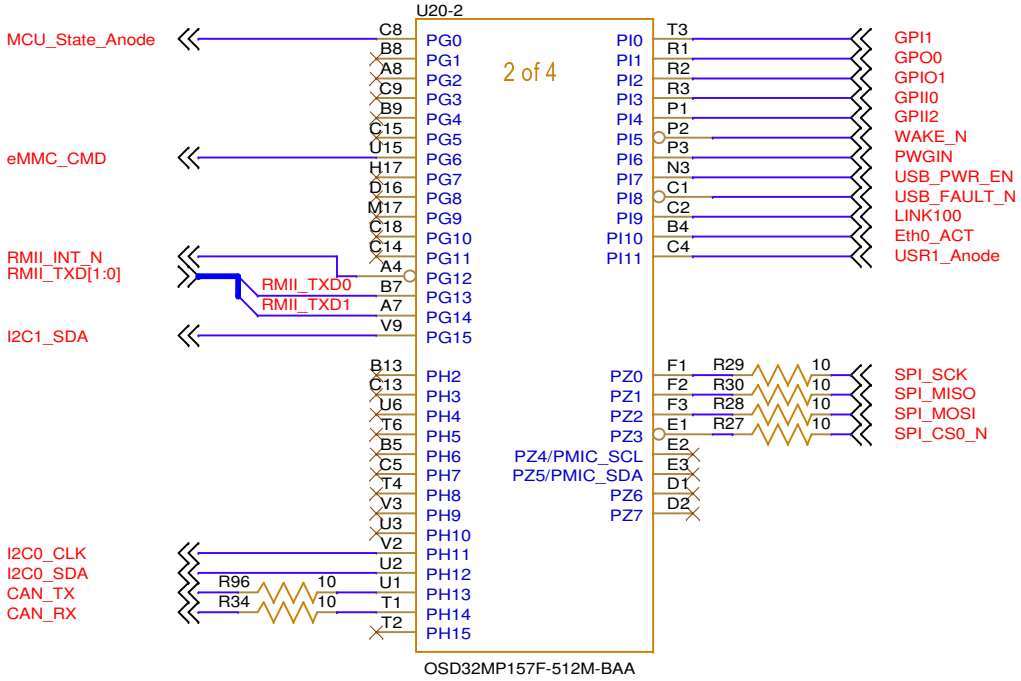
CPU 2



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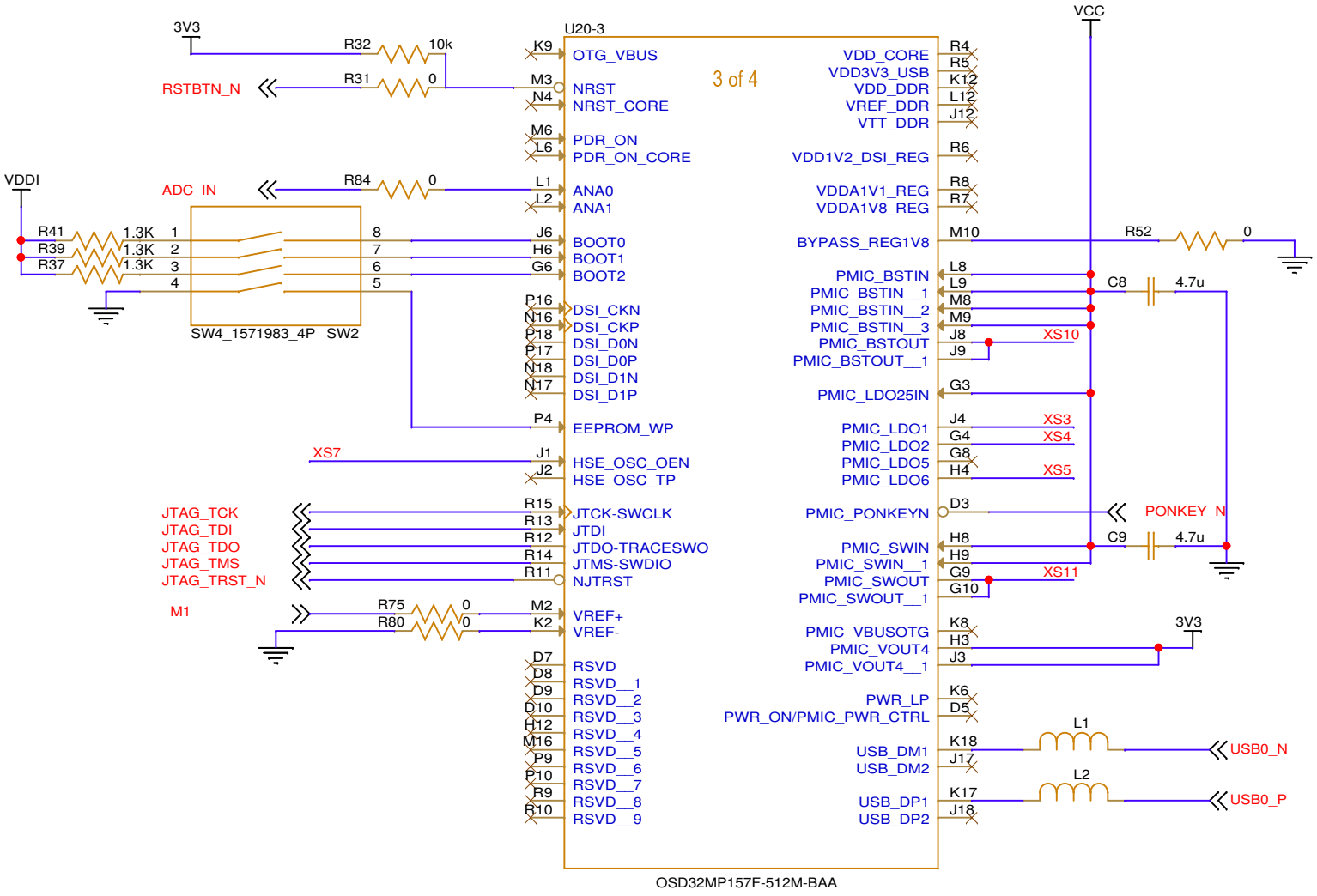
CPU 3



OSD32MP157F-512M-BAA

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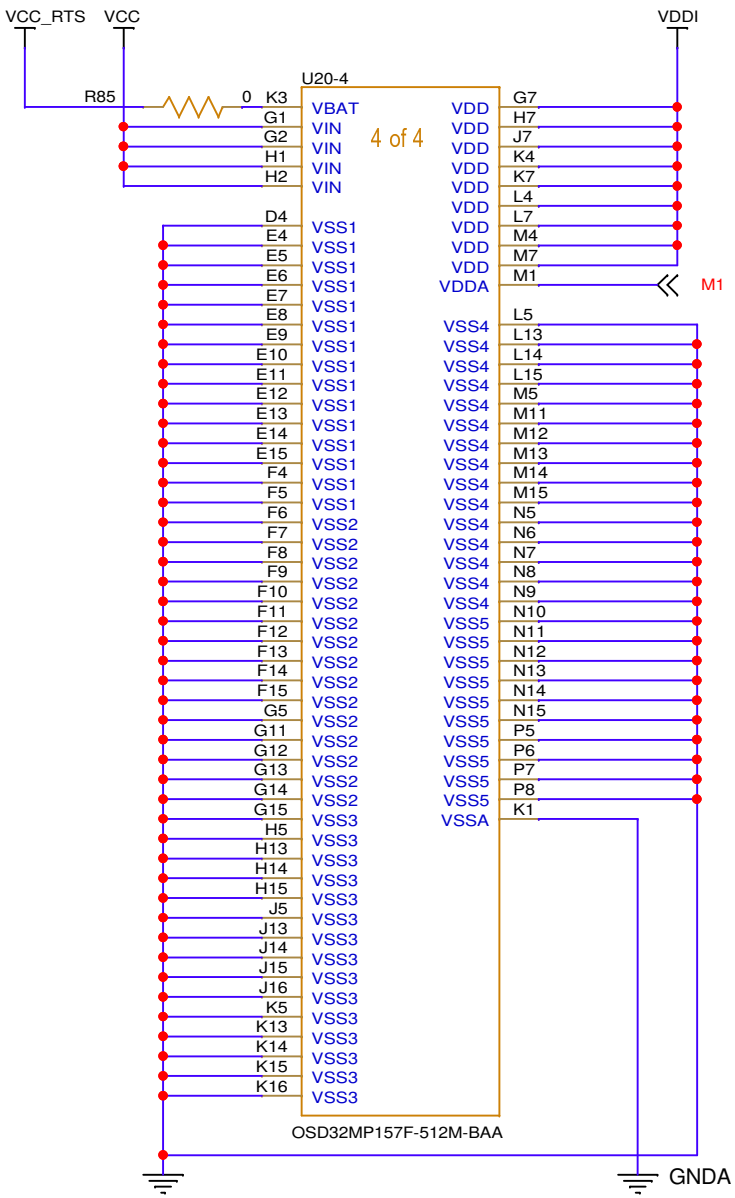
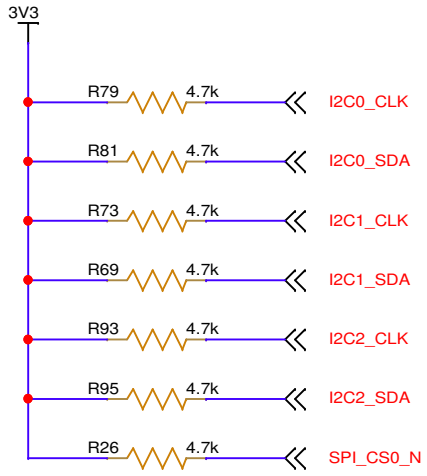
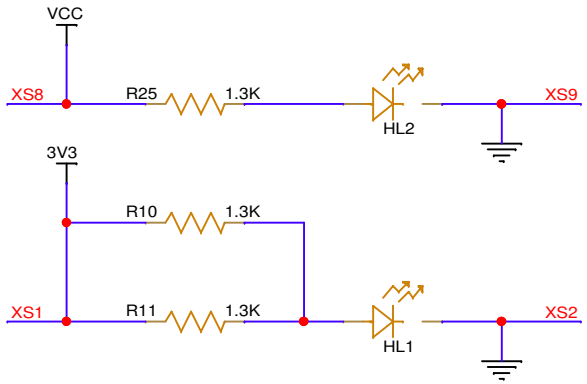
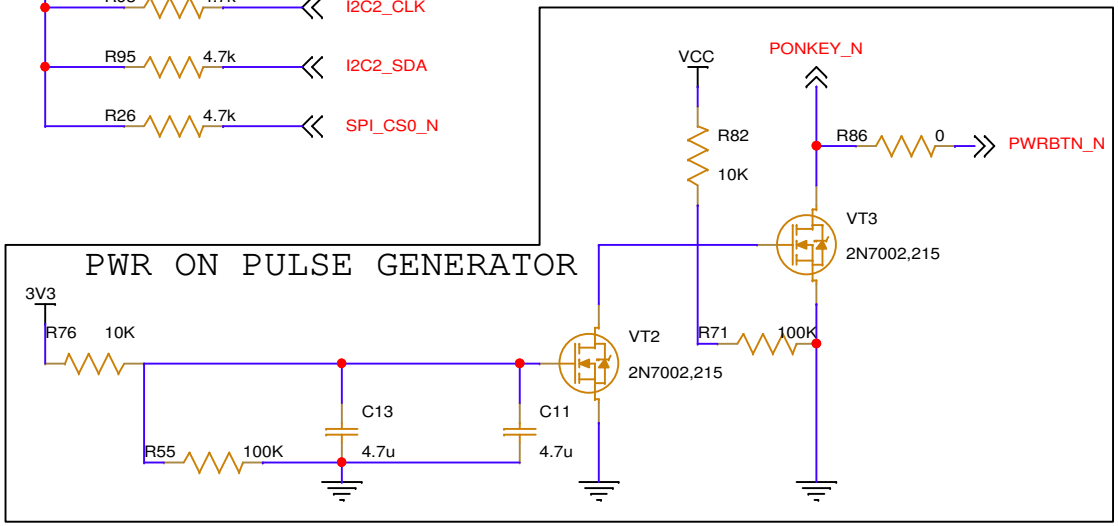
CPU 4
Ground and Power









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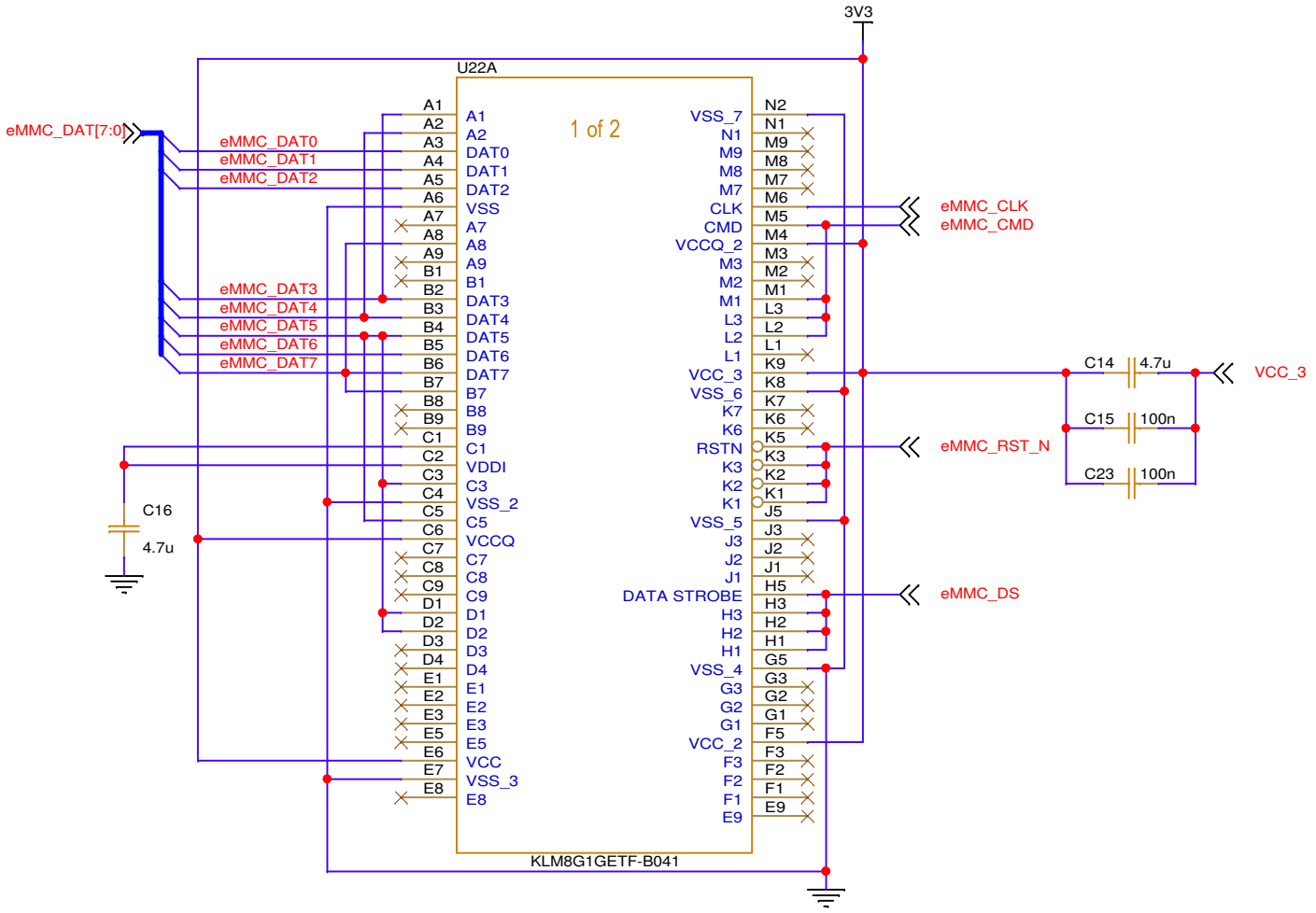
eMMC 1



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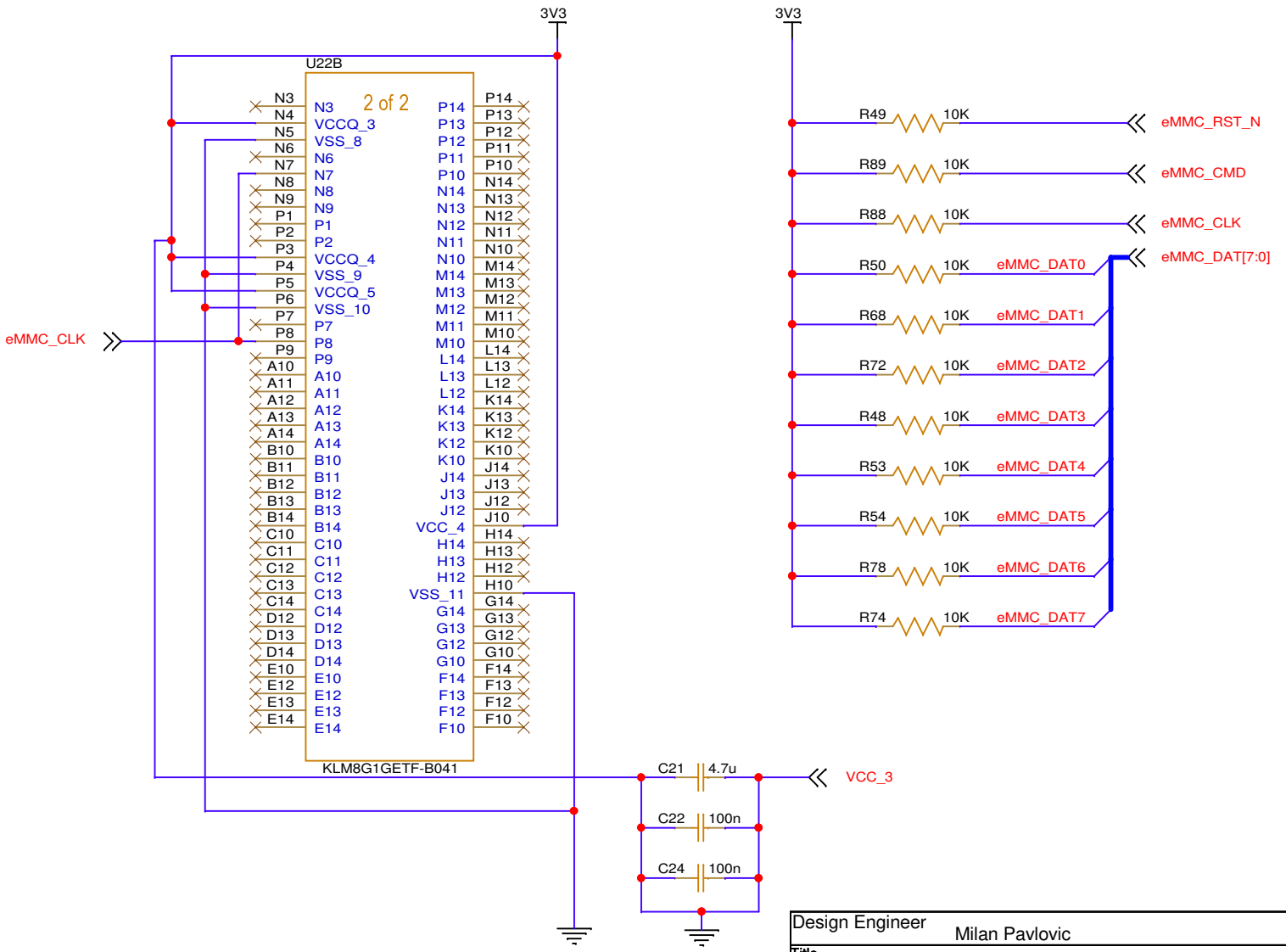
eMMC 2



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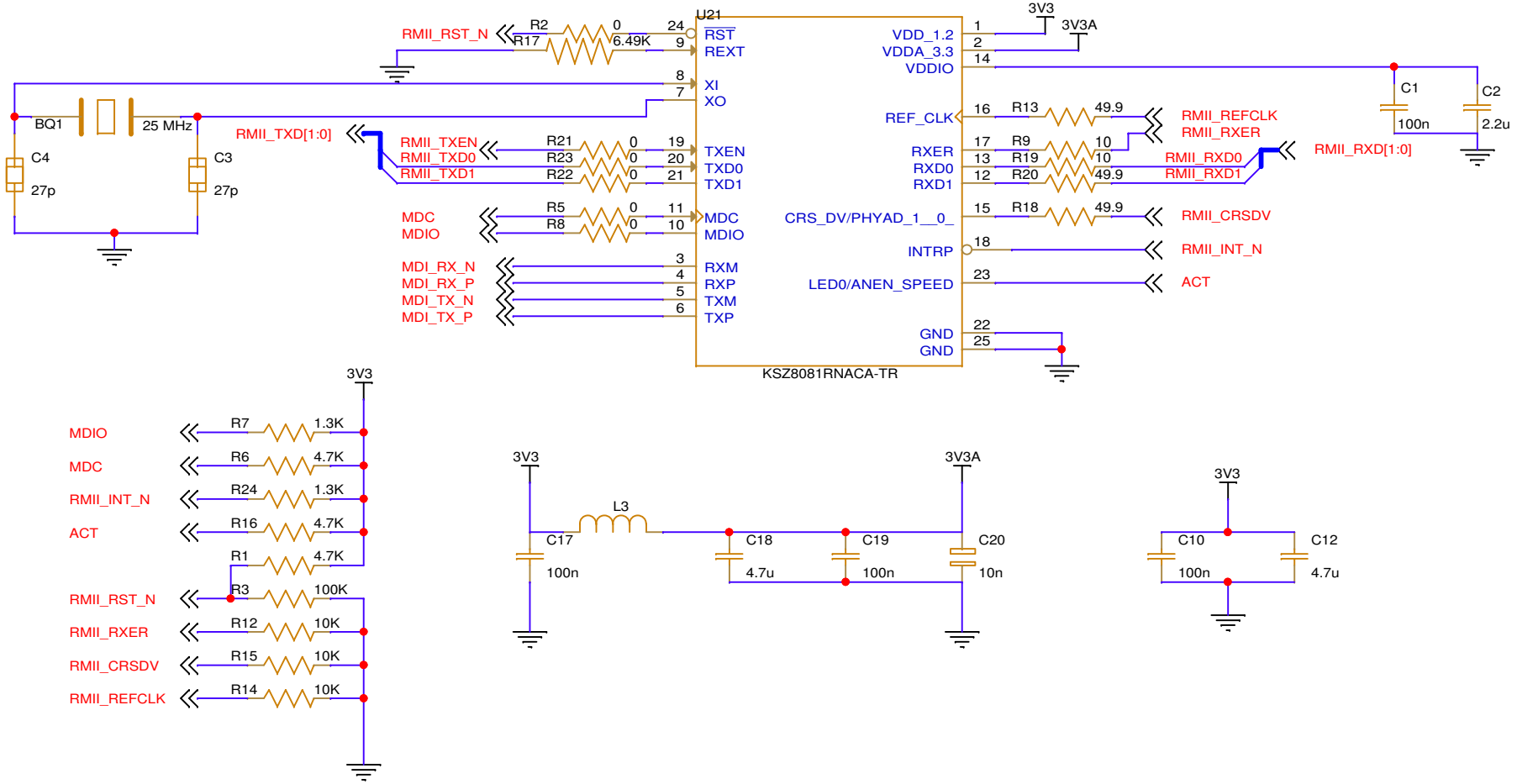
Ethernet



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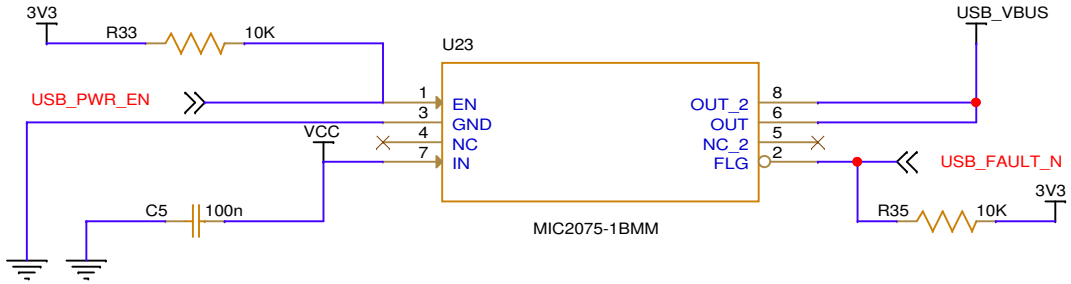
USB VBUS



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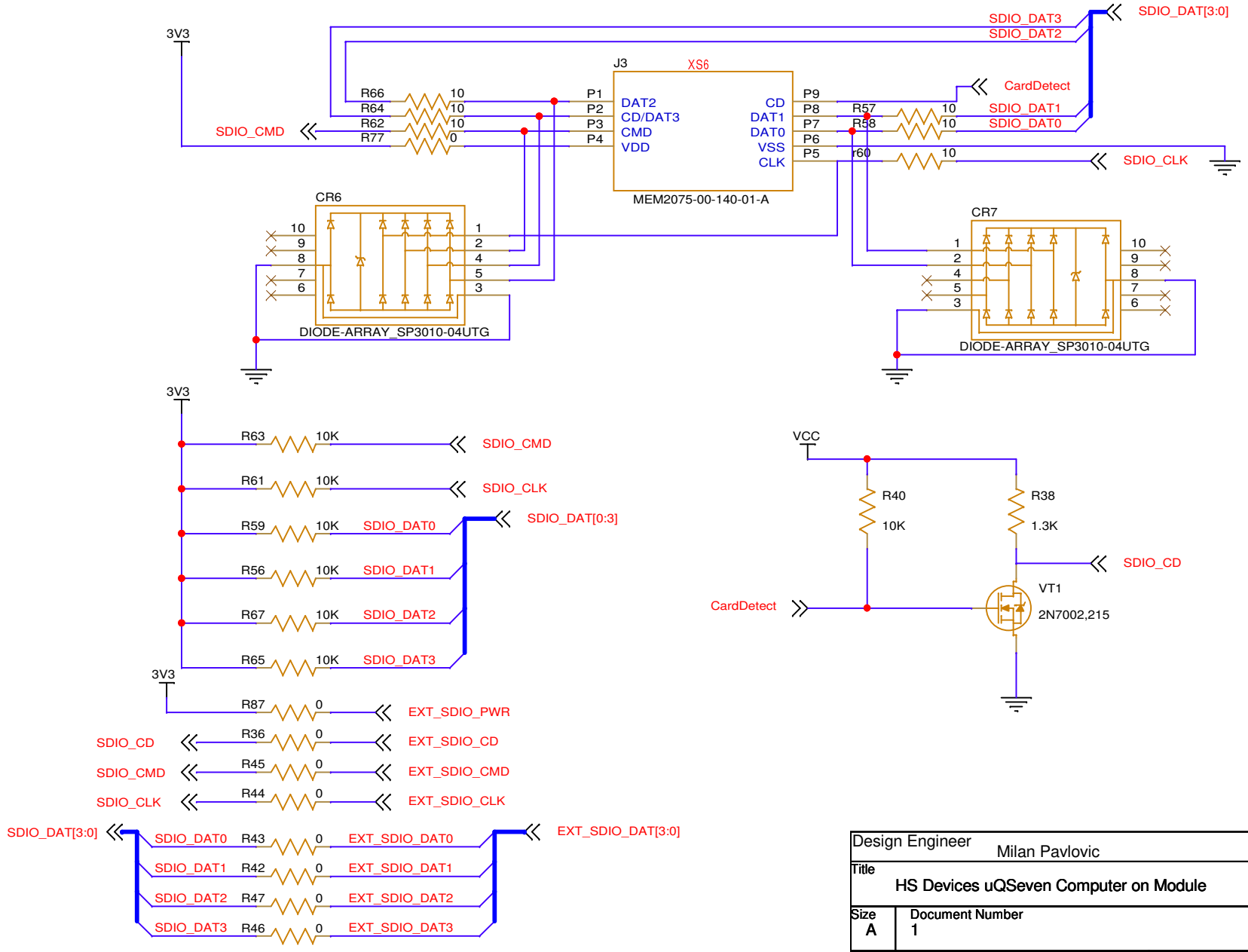
uSD Card



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