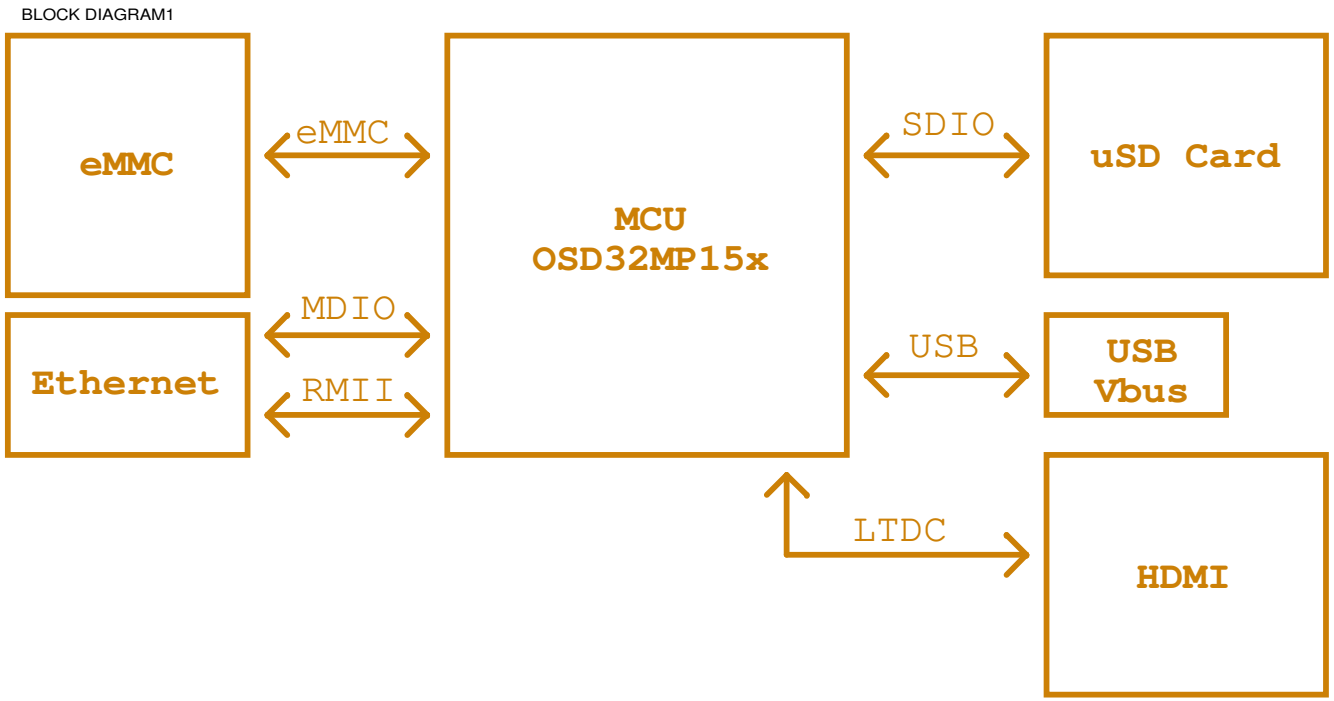
Page Description	
1	Schematic Page Listing
2	Block Diagram
3	MXM Connectors 1
4	MXM Connectors 2
5	CPU 1
6	CPU 2
7	CPU 3
8	CPU 4
9	eMMC 1
10	eMMC 2
11	Ethernet
12	USB Vbus
13	uSD Card
14	HDMI

Version Control		
Version	Date	Modifications
V0.12.1	05/2025	

All information is subject to change without notice.
No warranty, expressed or applied, is made as to the
accuracy of the information contained herein. This schematic is provided for reference purposes only.

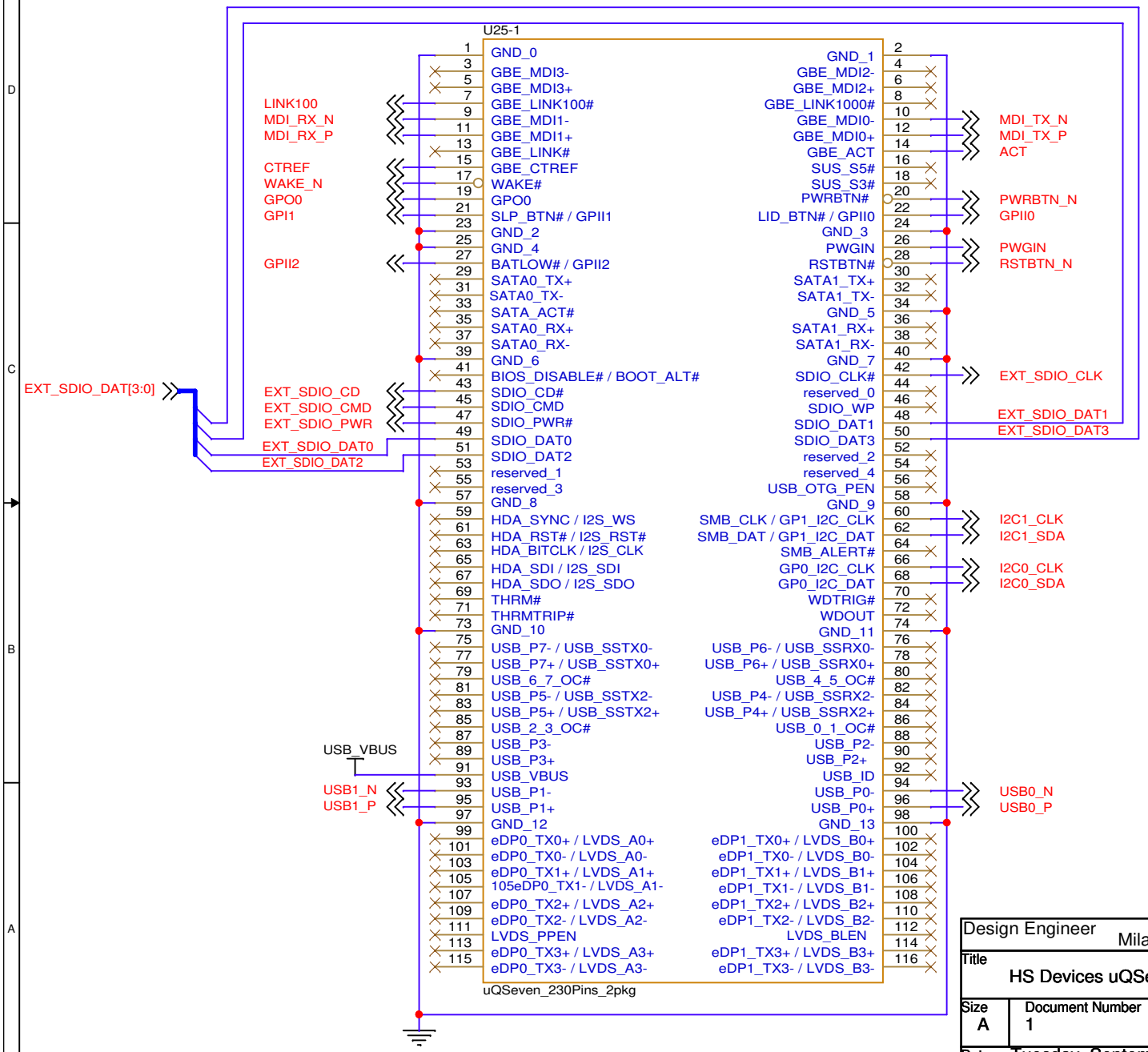
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 1 of 14	

Block Diagram



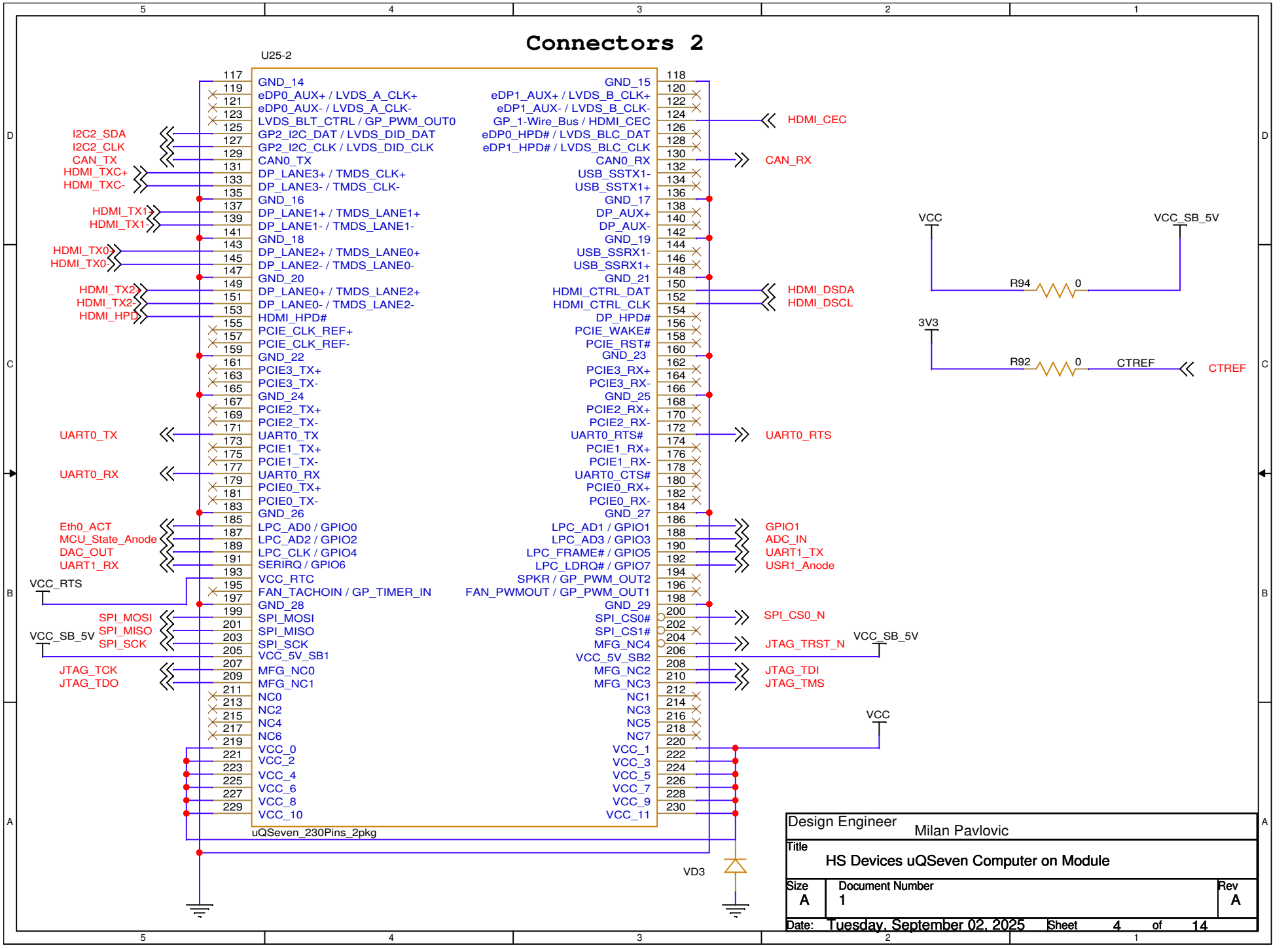
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 2 of 14	

Connectors 1



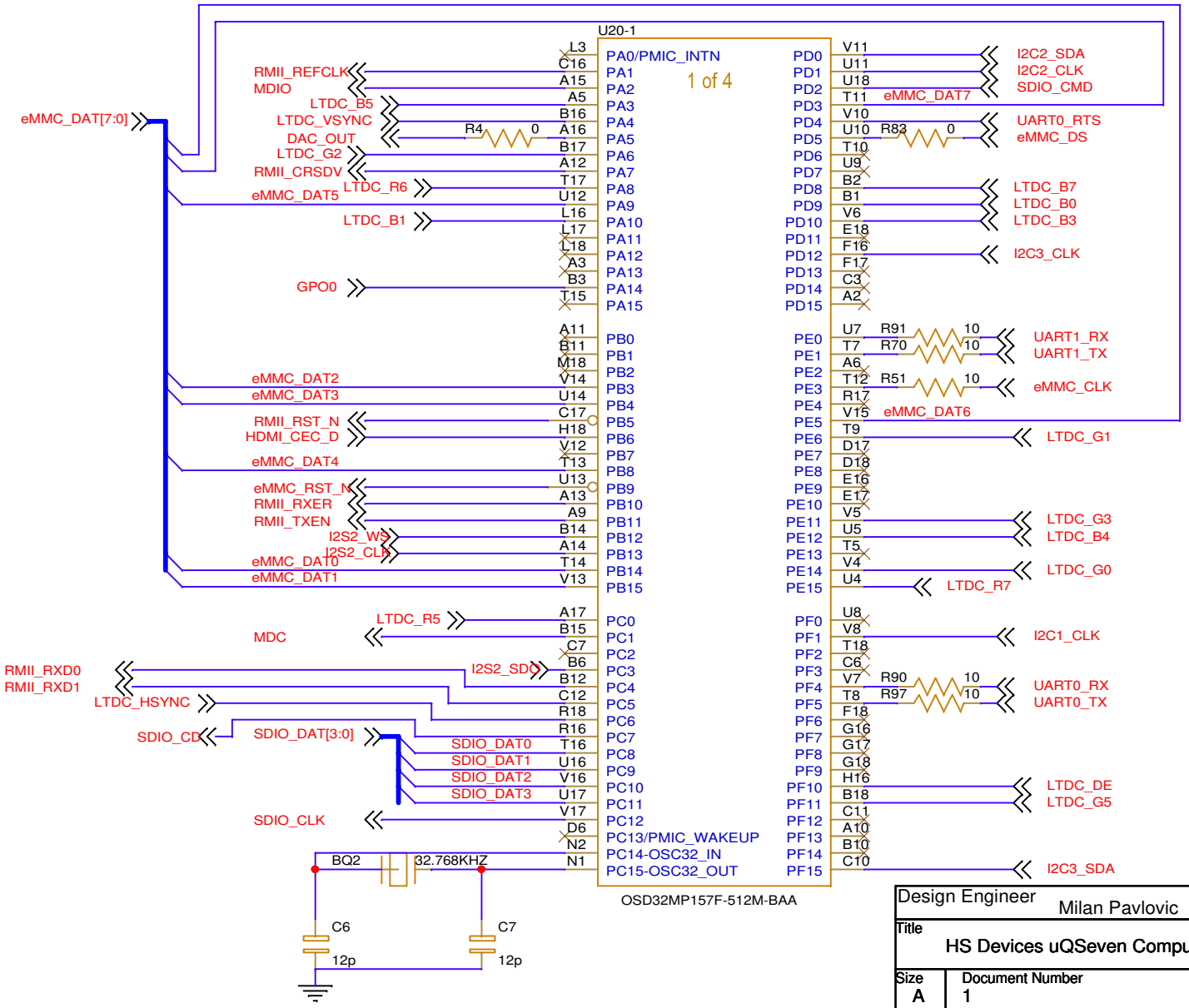
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 3 of 14	

Connectors 2



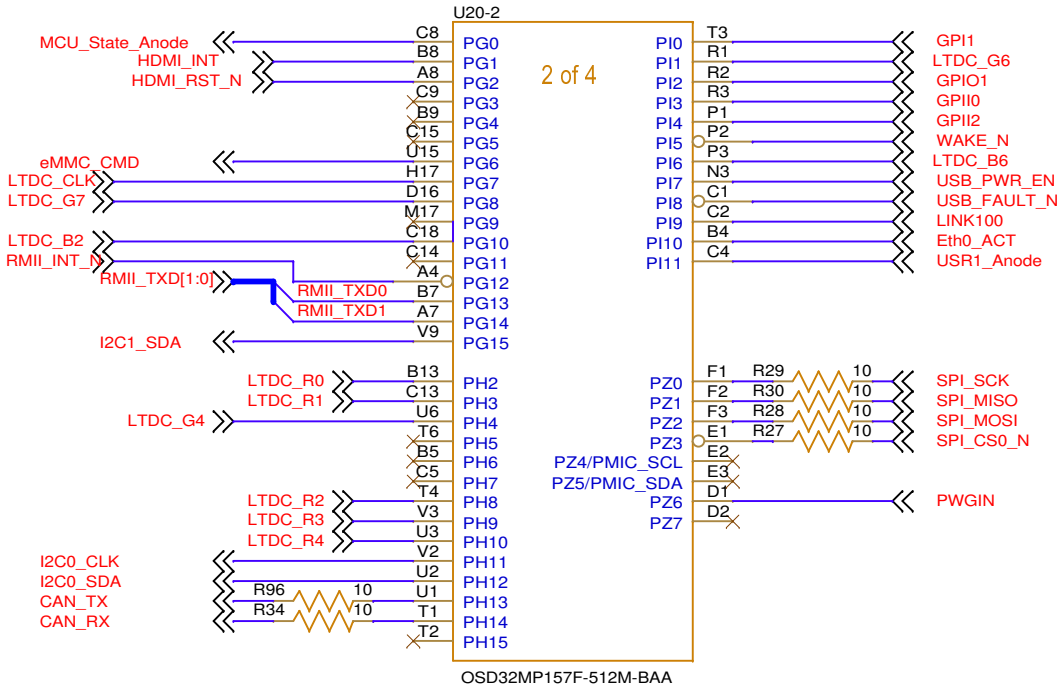
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 4 of 14	

CPU 1



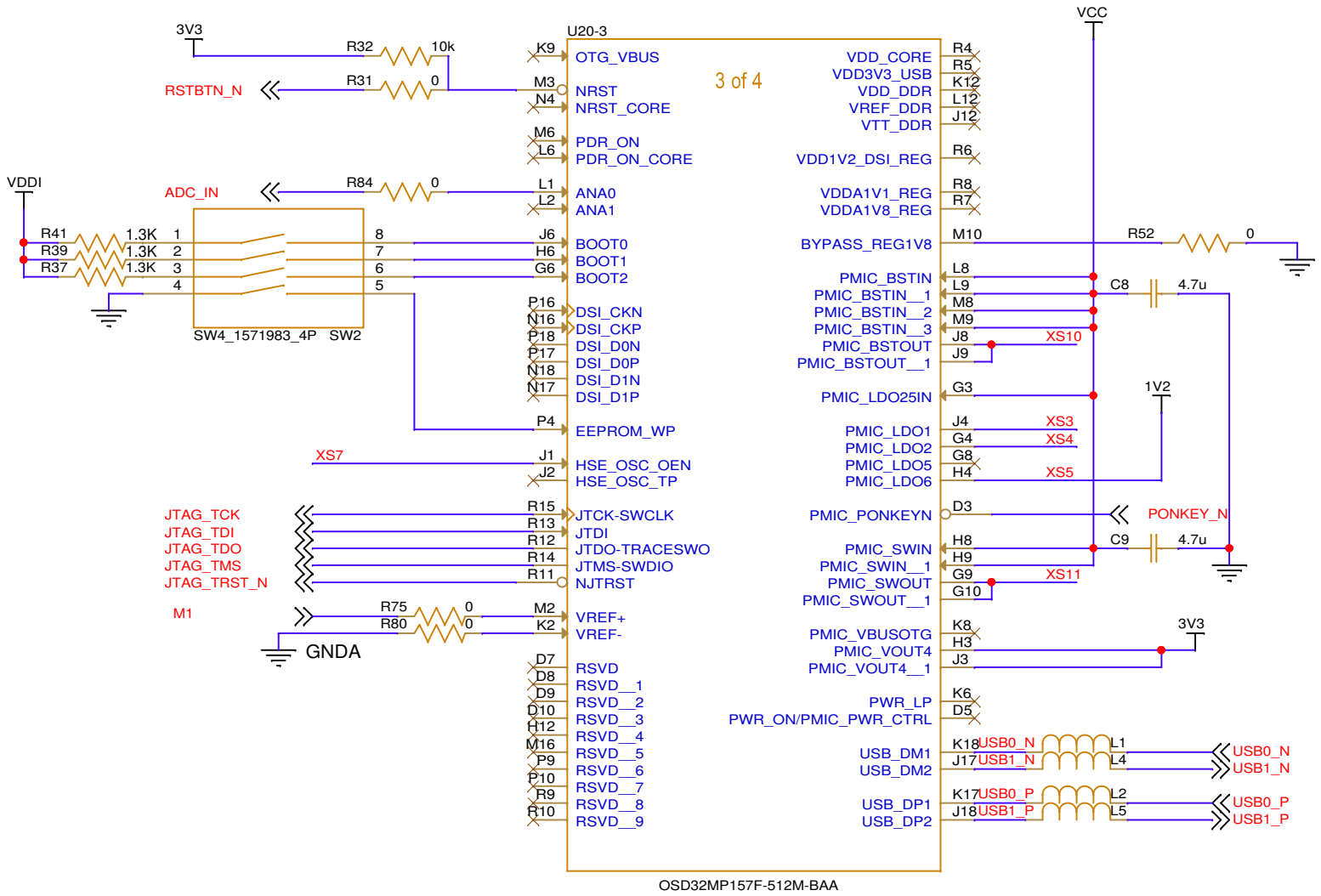
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 5 of 14	

CPU 2



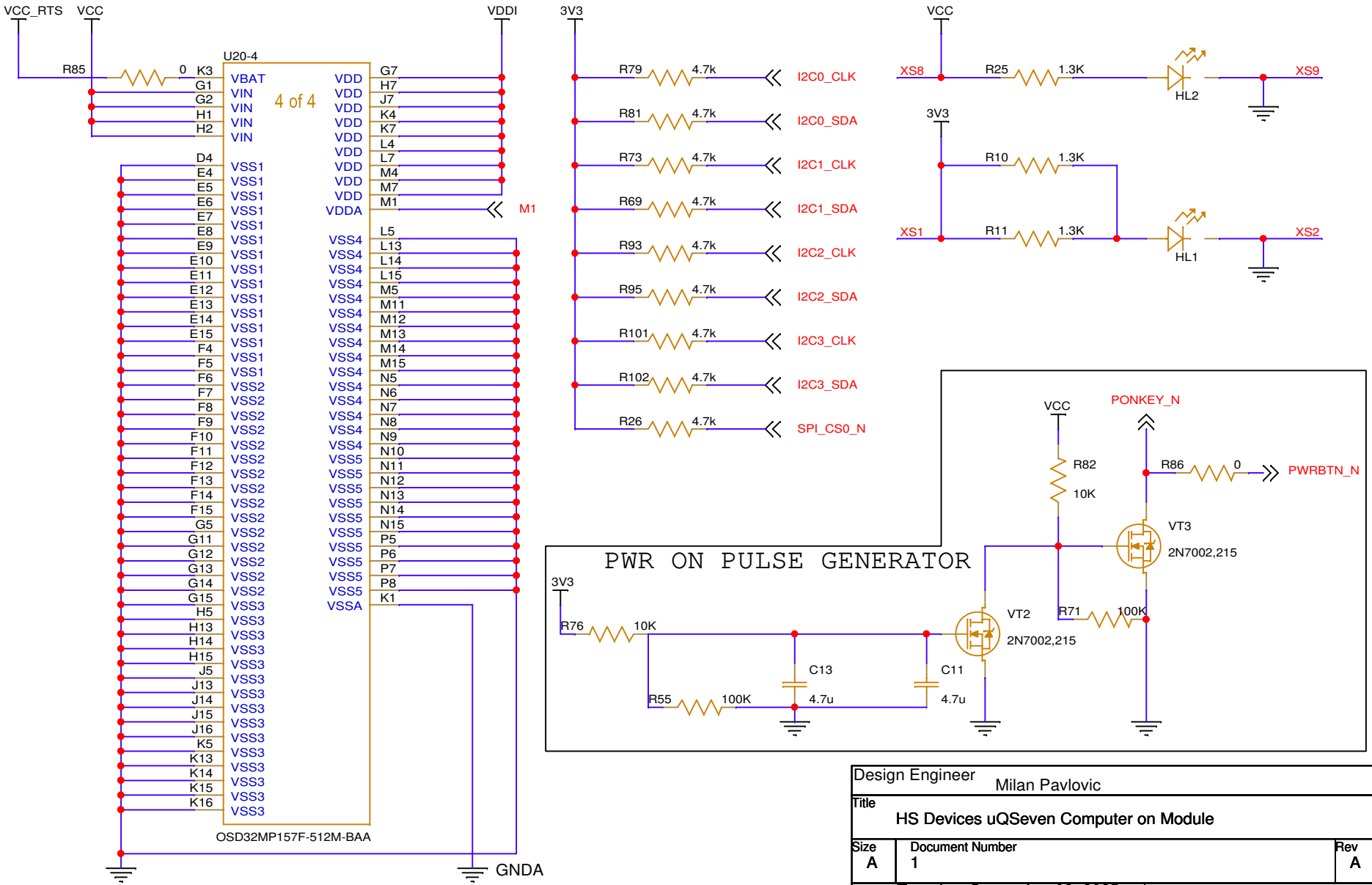
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 6 of 14	

CPU 3



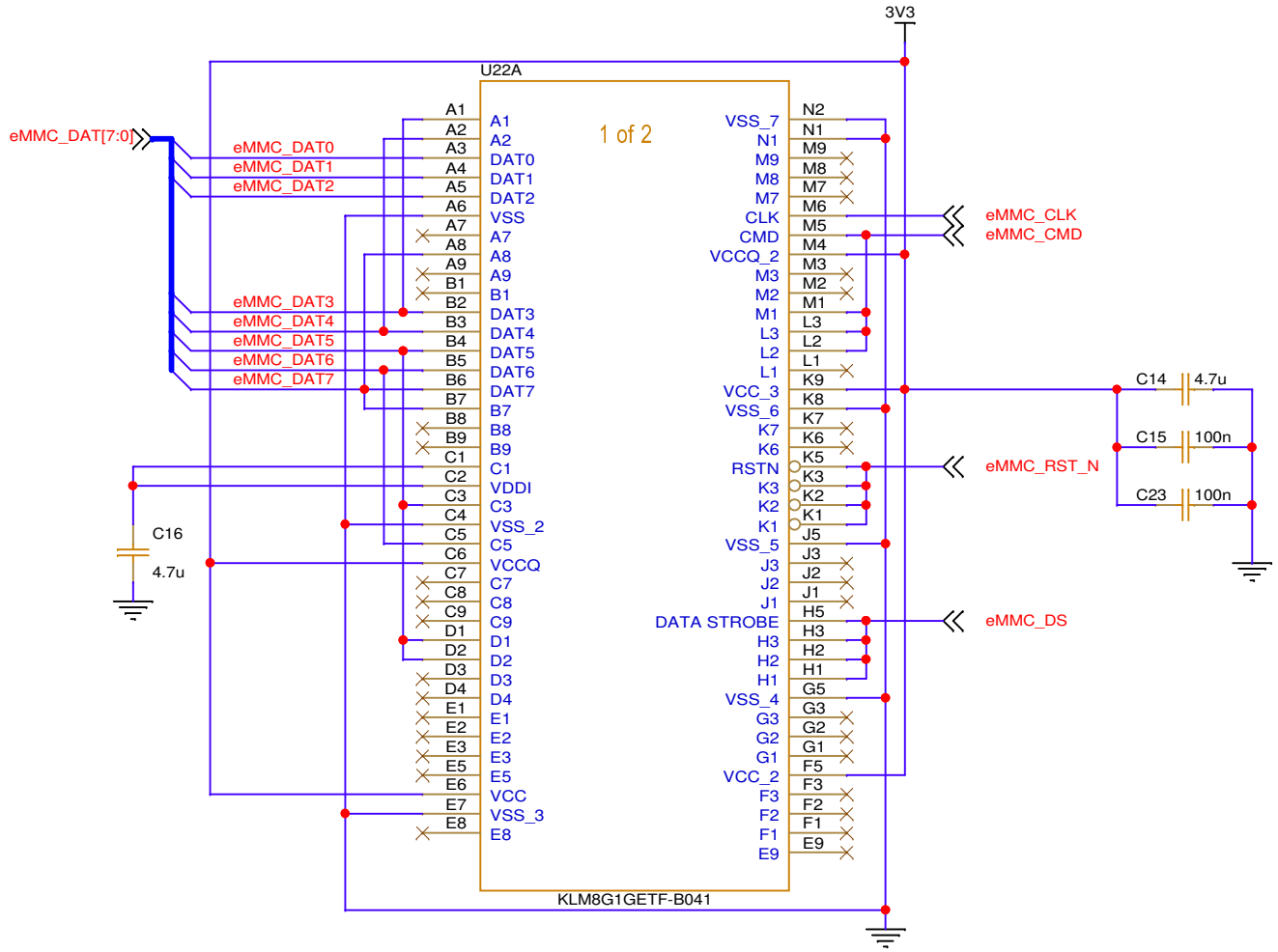
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 7 of 14	

CPU 4
Ground and Power



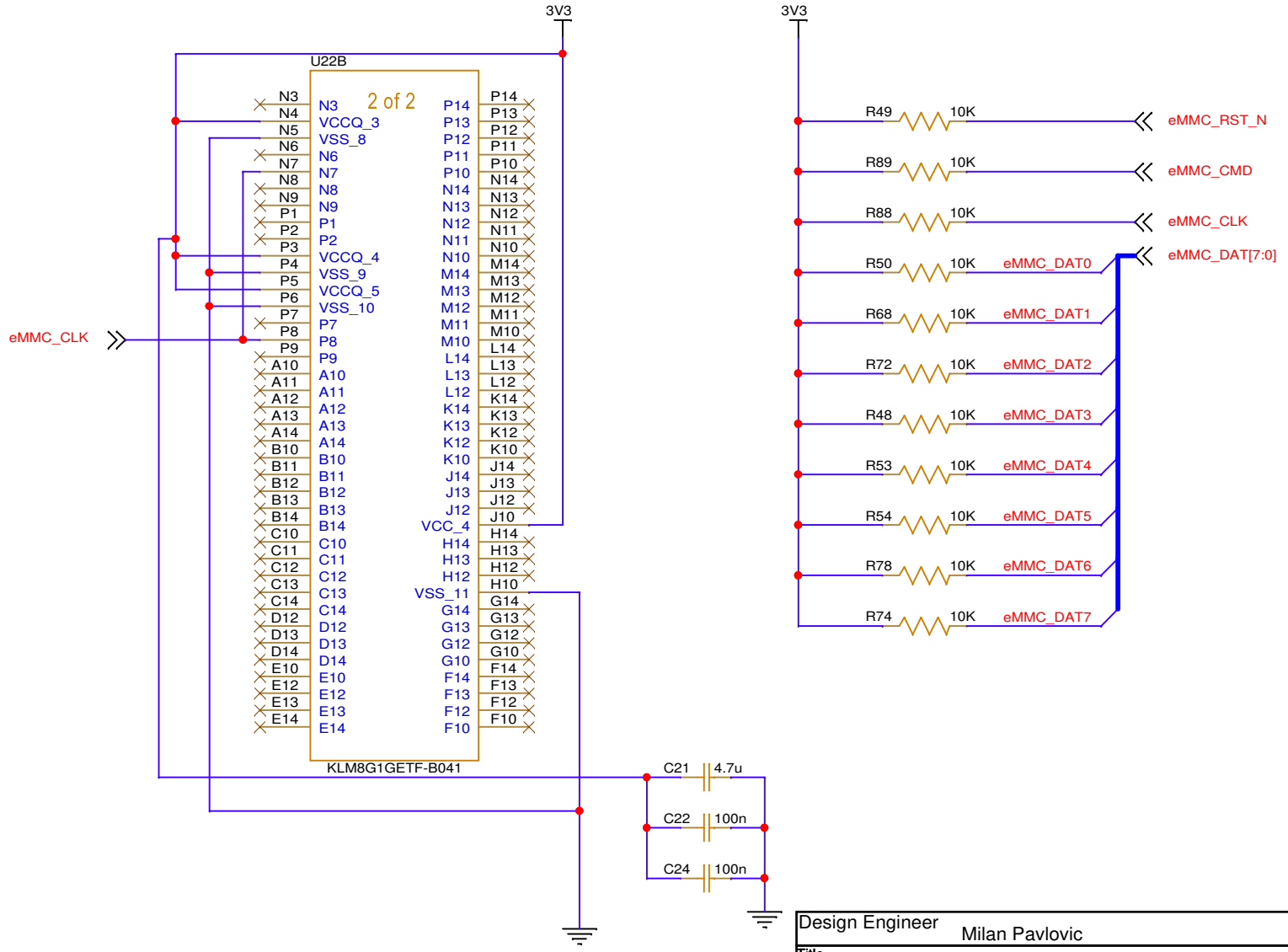
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 8 of 14	

eMMC 1



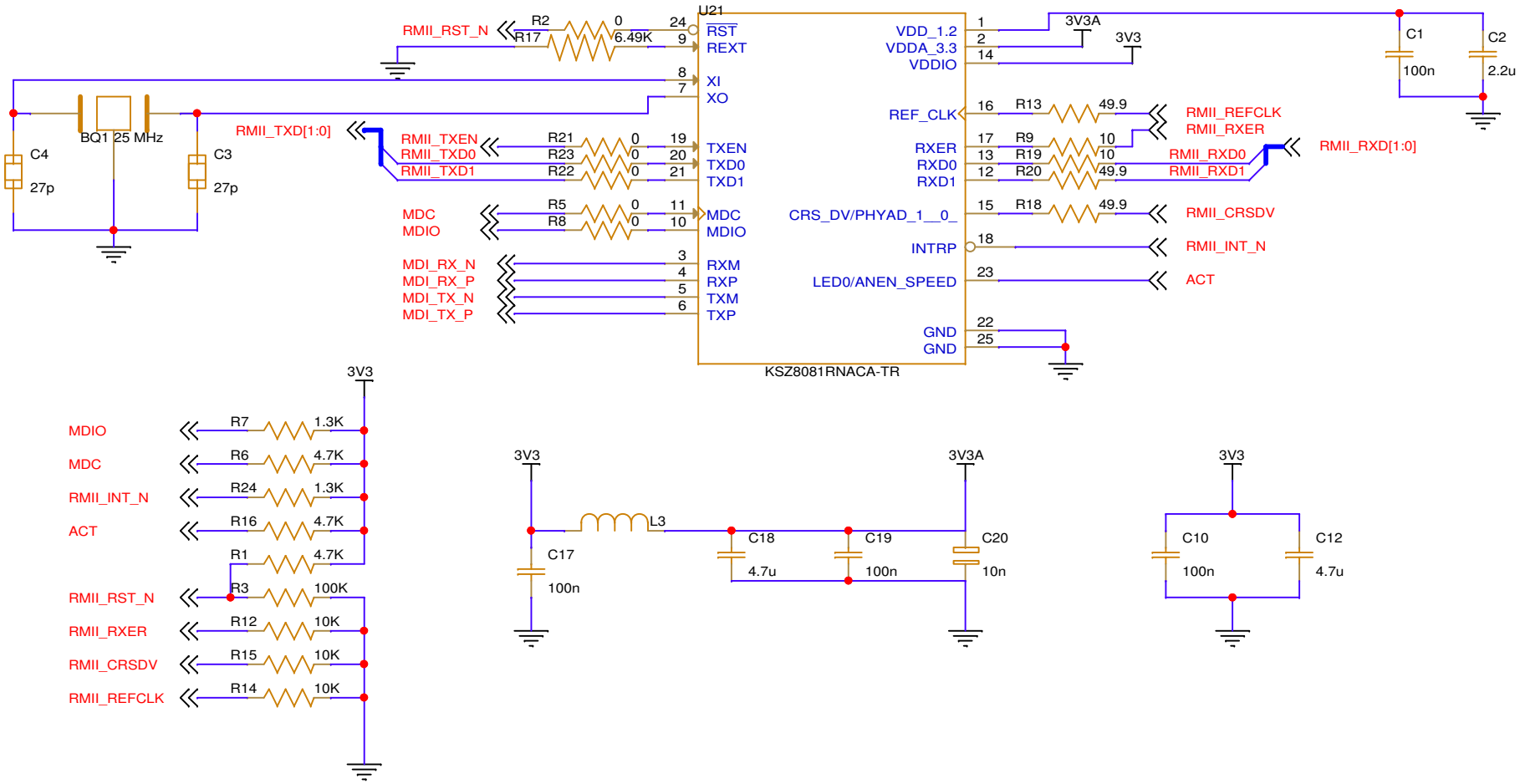
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 9 of 14	

eMMC 2



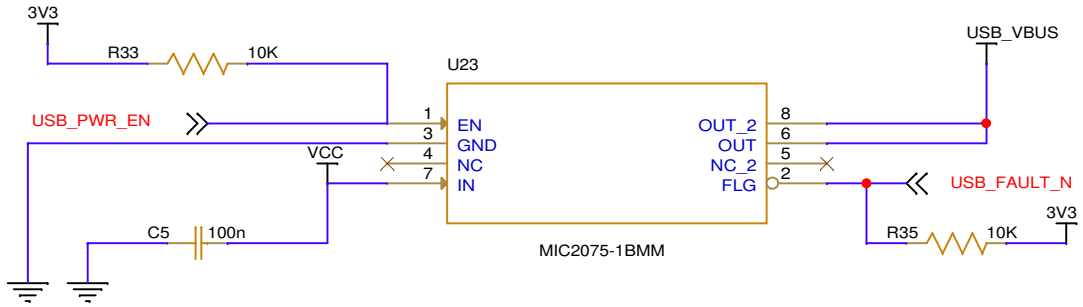
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 10 of 14	

Ethernet



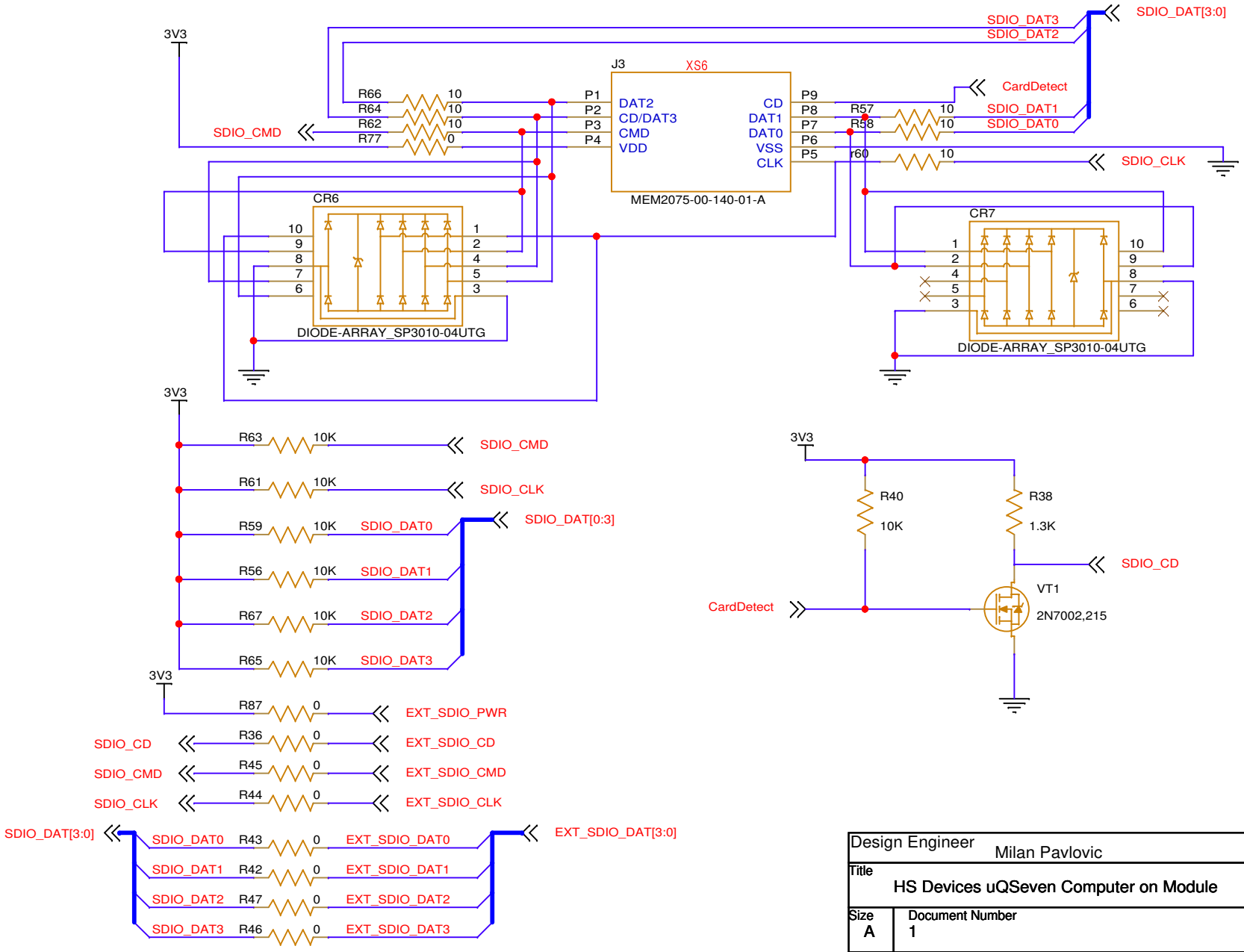
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 11 of 14	

USB VBUS



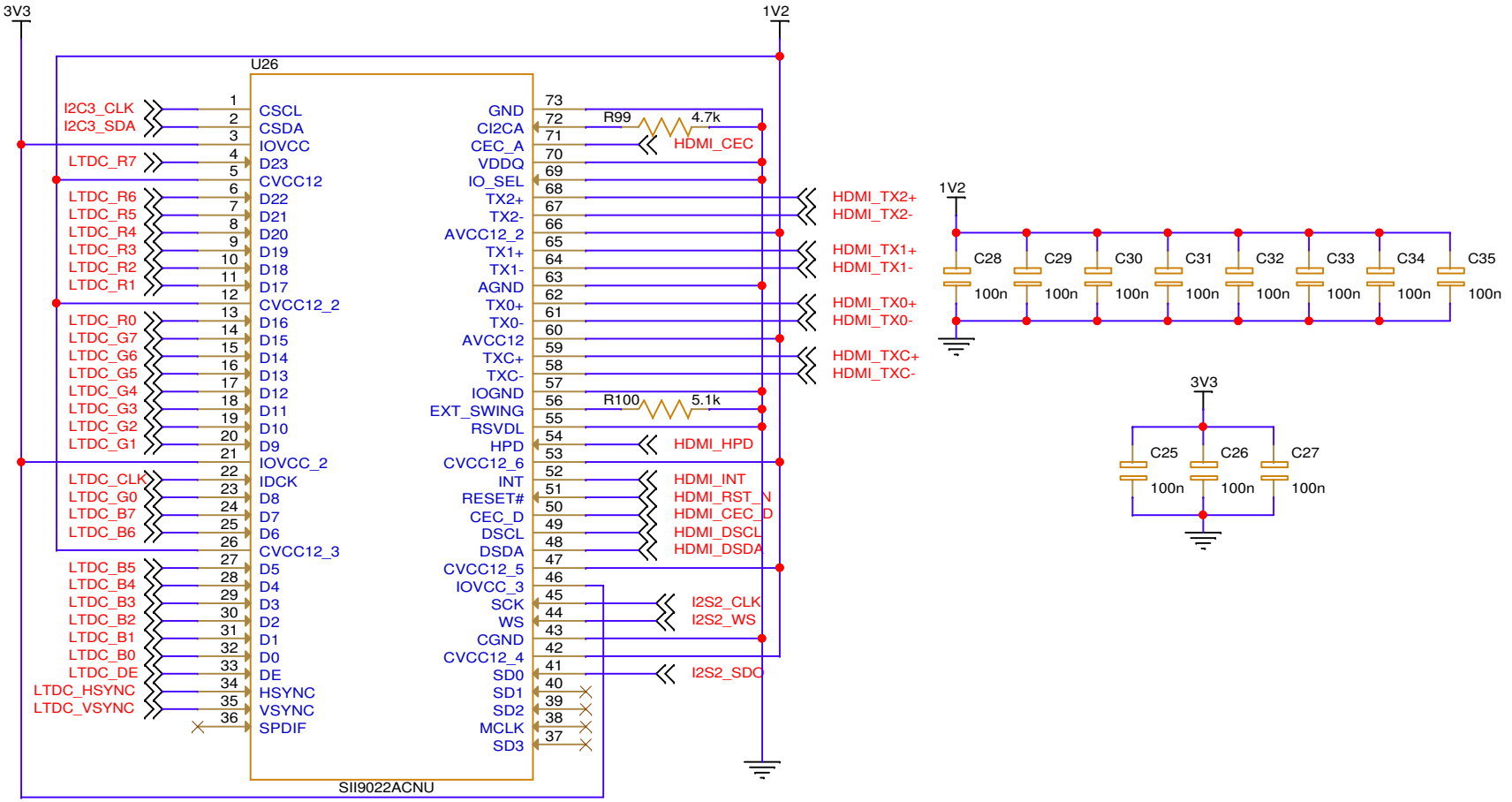
Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 12 of 14	

uSD Card



Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Monday, September 15, 2025	Sheet 13 of 14	

HDMI



Design Engineer	Milan Pavlovic	
Title	HS Devices uQSeven Computer on Module	
Size: A	Document Number: 1	Rev: A
Date: Tuesday, September 02, 2025	Sheet 14 of 14	