**Skills:**

C, Computer Networking, Operating System, Databases, Linux

**Education**

Uttar Pradesh Technical University 70%

Master of Technology, VLSI Design

Uttar Pradesh Technical University 72%

Bachelor of Technology, Electronics and Communication Engineering

**Academic projects**

* Huffman Coding using MATLAB

Coded Huffman algorithm using MATLAB and implemented for compressing the data using huffman coding. This compression provides security to the text giving coded output.

* Modeling of Diodes, MOS transistors, Bipolar Transistors etc using SPICE (studied DC characteristics of the diodes to describe the behavior in forward and reverse biased conditions).
* Studied I-V Curves of NMOS and PMOS Transistors (operation of MOS).
* Simulated DC Characteristics of CMOS Inverters (VTC, Noise Margin).
* Analyzed Dynamic Characteristics of CMOS Inverters (Propagation Delay, Power Dissipation).
* Designed and Simulated Single Stage Amplifiers (Common Source, Source Follower, Common Gate Amplifier).
* Designed and Simulated Single Stage Amplifiers (Cascode Amplifier, Folded Cascode Amplifier)
* Designed and Simulated a Differential Amplifier (with Resistive Load, Current Source Biasing).
* Designed and Simulated of Basic Current Mirror, Cascode Current Mirror.

**Certificates**

Basic Electronics and Digital Image Processing.

Embedded System

Wireless Sensor Networks Using Netsim.

**Publication**

Review of Low Power Johnson Counter In STM Journals

Implemented Low power Johnson counter and studied its results. A 2-input flip-flop is used instead of 3-input flip-flop plays a remarkable role in power loss minimization. There is reduction in power in comparison to conventional design but clock-gating logic being the additional circuitry has its own power dissipation.