Name: Stubban Sahay Roll. No.		
Department of Electrical Engineering,		
Indian Institute of Technology, Kanpur		
ESC201	<u>Mini-Quiz VI</u>	01/04/2025
Total Marks: 5	Instructions	Time: 10 minutes
 Please write your name and roll number first. Read the question carefully and answer it in the question paper itself. 		
1) We need to design a digital hardware which classifies the decimal numbers between 0 to 15 as prime numbers i.e. the hardware outputs a logic "1" if the decimal number is prime and logic "0" otherwise.		
(a) Write the	truth table for this hardware design.	(1 mark)
	at the truth table realized in part (a), would it be orm to represent this Boolean function? Why?	optimal to use the SOP form (1 mark)
(c) Using K-Map, find the minimized expression for the Boolean function to be implemented using this hardware. (3 marks)		
FYI: 0 and 1 are:	not considered prime numbers. Out :: Total no. of ::	" emnes are lower, it is better to use
0-,0000	o for the output	, it is better to use
1 10 001	50P form: minim	$\mathbf{c}_{\mathbf{A}}$
2,0010	1 AB CD 00 DI 1	1 10 - Correct
3-0011	1 00 0 0	K-MAP with grey
4-0100	0	Code and
570 101	1 01 0	entries-si
6-0 110	0 11 0 1	O → Poppropriate
7-0 111		grouping
8-1 000	0 10 0 0	1 0 -0
9-11 001		
10-11 0 (0	OUT = FCD+	ABC + ABD
11-21 0 11		
12-1 100	D + BC	
13-1 1 0 1		solution can be differen
19-11-10	tor different gr	rouping styles
15-1111	0 - if 4 minterns - 1	if more than 4-5 (0.5)