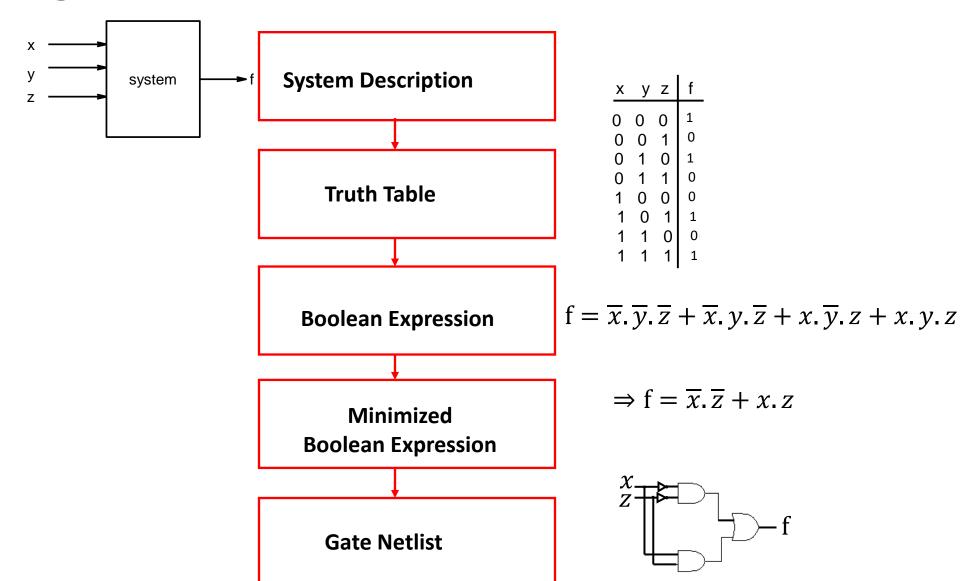


ESC201: Introduction to Electronics Module 6: Digital Circuits



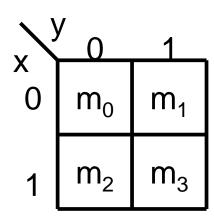
Dr. Shubham Sahay,
Associate Professor,
Department of Electrical Engineering,
IIT Kanpur

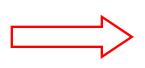
Design Flow



K-map Representation of Truth Table

X	У	min term
0	0	<u>x</u> . y m0
0	1	\overline{x} . y m1
1	0	x . y m2
1	1	$I_{x.ym3}$

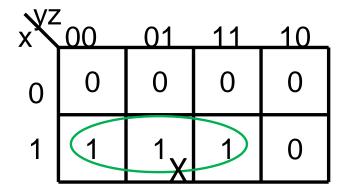


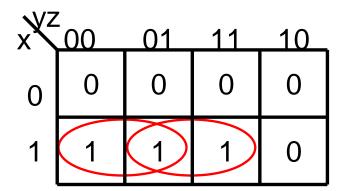


	0	1
0	0	1
1	1	0

Can We Group 3 Min Terms?

Can we do this?





Note that each encirclement should represent a single product term.

In this case it does not represent a single product term.

$$f = x.\overline{y}.\overline{z} + x.\overline{y}.z + x.y.z$$

$$= x.\overline{y} + x.z$$

We do not get a single product term.

Grouping in 3 terms will not help in minimization of terms in function.

Reordering of Numbering not Beneficial for Simplification

Can we use K-map with the following ordering of variables?

XXZ	00	01	10	11
0	0	0	0	0
1	0	(1)	(1)	0
'				

NOT A GOOD IDEA!

Can we combine these two terms into a single term?

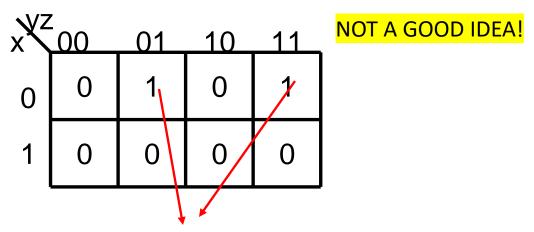
$$f = x.\overline{y}.z + x.y.\overline{z}$$

$$= x.(\overline{y}.z + y.\overline{z})$$

Note that no simplification is possible here.

K-map requires variable to change one bit between adjacent cells

- Continued -



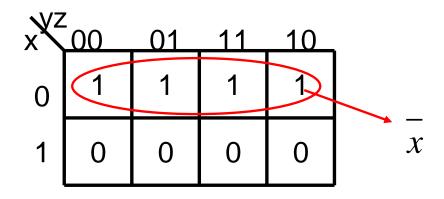
These two terms can be combined into a single term but it is not easy to show that on the diagram.

$$f = x.y.z + x.y.z$$

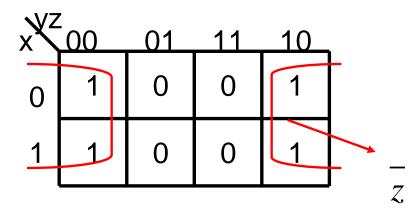
= $x.(y+y).z = x.z$

Kmap requires information to be represented in such a way that it is easy to apply the principle x + x = 1

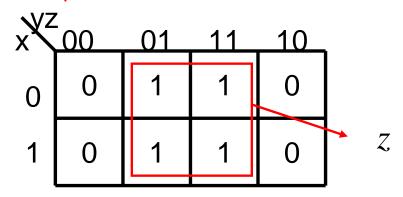
Example

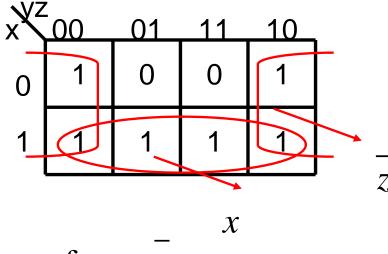


Example



Example

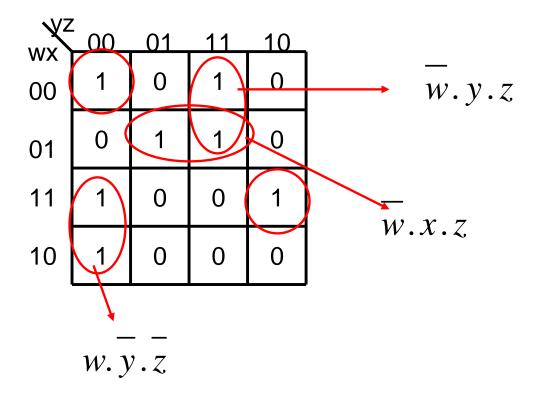




$$f = x + \overline{z}$$

4-variable Minimisation

Example

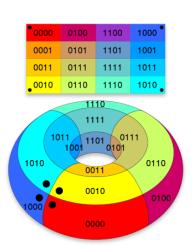


$$f = w. y. z + w. x. z + w. y. z + w. x. y. z + w. x. y. z$$

But is this the simplest expression?

The K-Map Folds on Itself

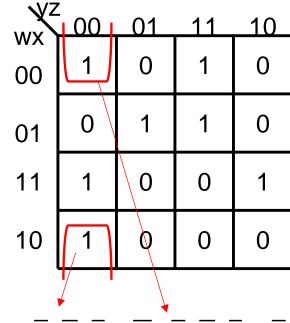
K-map drawn on a torus, and in a plane. The dot-marked cells are adjacent.
- source: Wikipedia



Example (continued)

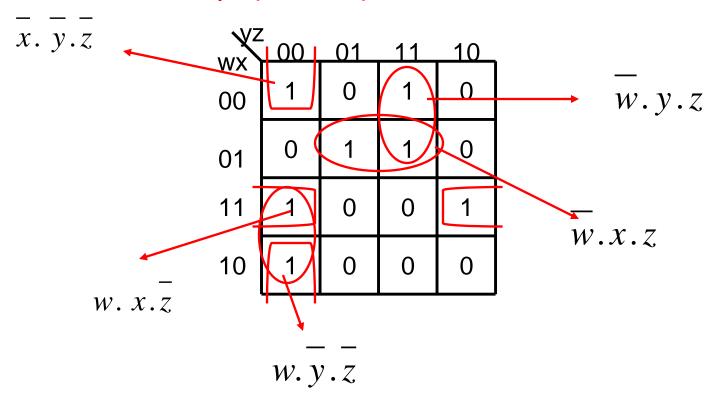
WX VZ	00	01	11	10		
00	1	0	1	0		
01	0	1	1	0		
11	1	0	0	1_	_	
10	1	0	0	0		
			_			
w. x. y. z + w. x. y. z = w. x. z						

Example (continued)



$$w. \overline{x}. \overline{y}. \overline{z} + \overline{w}. \overline{x}. \overline{y}. \overline{z} = \overline{x}. \overline{y}. \overline{z}$$

Example (continued)

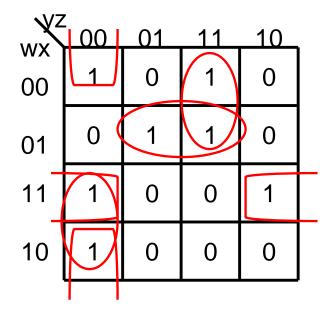


$$f = w. y. z + w. x. z + w. y. z + w. x. z + x. y. z$$

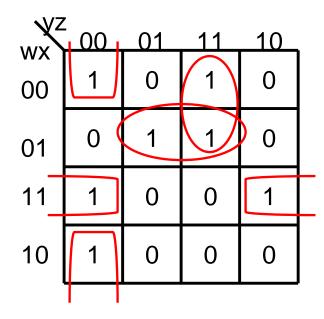
Is this the best that we can do?

Cover the 1's with Minimum Number of Groupings

Example (continued)

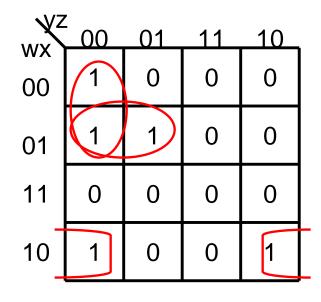


$$f = \overline{w}. y. z + \overline{w}. x. z +$$



$$f = \overline{w}. y. z + \overline{w}. x. z + w. x. z + w. x. z + x. y. z$$

Equivalent Solutions



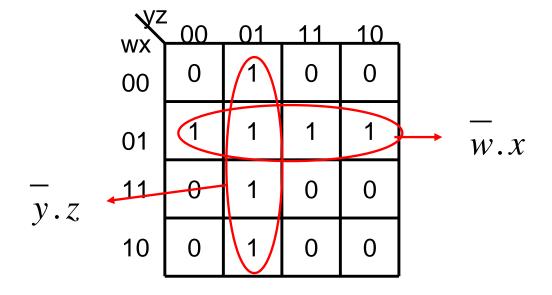
WX VZ	00	01	11	10	
00	1	0	0	0	
01	(T)	1	0	0	
11	0	0	0	0	
10	1	0	0	1	
		•			

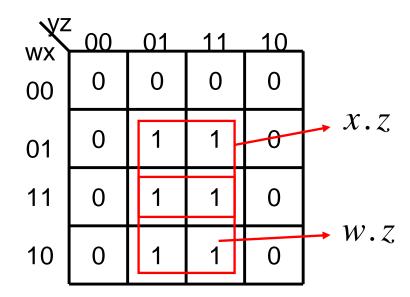
$$f = \overline{w}.x.\overline{y} + w.\overline{x}.\overline{z} + \overline{w}.\overline{y}.\overline{z}$$

$$f = \overline{w} \cdot x \cdot \overline{y} + w \cdot \overline{x} \cdot \overline{z} + \overline{x} \cdot \overline{y} \cdot \overline{z}$$

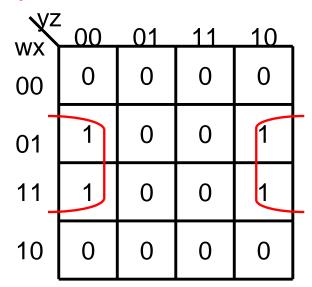
Groupings of Four

Example





Example



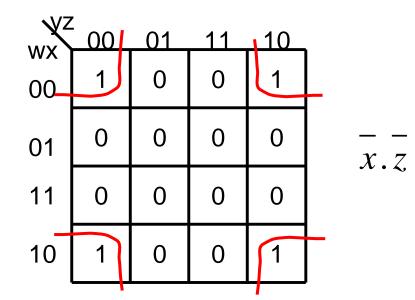
x.z

Example

00	01	11	10	ı
0	1	_1	0	
0	0	0	0	
0	0	0	0	
0	1	1	0	
	0 0	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	00 01 11 0 1 1 0 0 0 0 0	00 01 11 10 0 1 1 0 0 0 0 0 0 0 0 0

x.z

Example

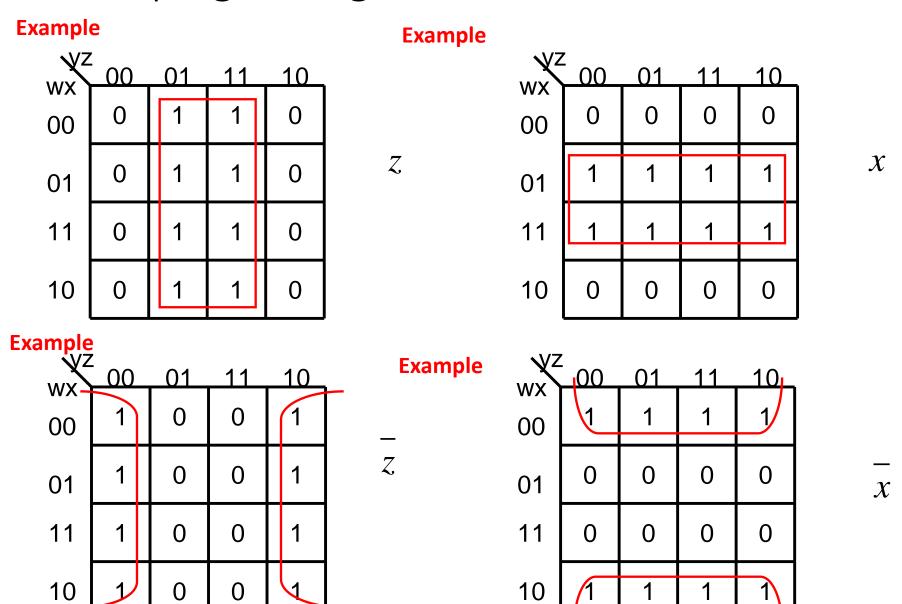


Example

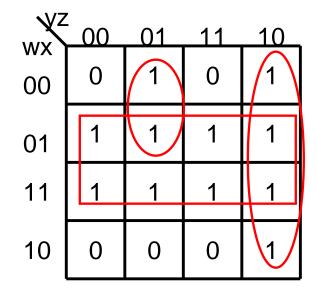
WX VZ	00	01	11	_10_
00	1	0	1	0
01	0	0	0	0
11	0	0	0	0
10	1	0	1	0

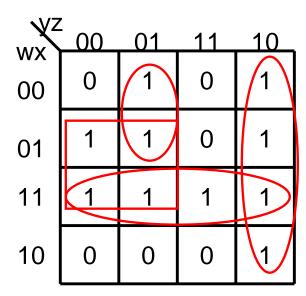
??

Groupings of Eight

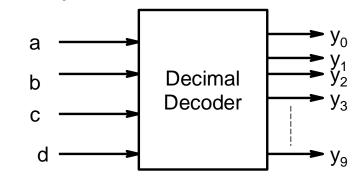


Example





Don't Care Terms

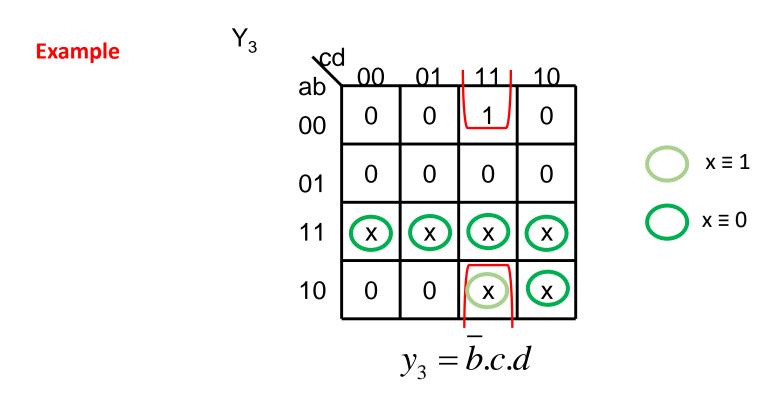


Y ₃	CO	00	01	11	10
	ab 00	0	0	1	0
	01	0	0	0	0
	11	X	X	X	X
	10	0	0	х	Х
		y_3	$= \overline{a}$.	_ b.c.d	!

	a	D	С	a	<i>y</i> ₀ <i>y</i> ₁ <i>y</i> ₂ <i>y</i> ₃ <i>y</i> ₄ <i>y</i> ₅ <i>y</i> ₆ <i>y</i> ₇ <i>y</i> ₈ <i>y</i> ₉
	0	0	0	0	1000000000
	0	0	0	1	0100000000
	0	0	1	0	0010000000
<	0	0	1	1	0001000000
	0	1	0	0	0000100000
	0	1	0	1	0000010000
	0	1	1	0	0000001000
	0	1	1	1	0000000100
	1	0	0	0	0000000010
	1	0	0	1	0000000001
	1	0	1	0	xxxxxxxxx
	1	0	1	1	xxxxxxxxx
	1	1	0	0	XXXXXXXXX
	1	1	0	1	XXXXXXXXX
	1	1	1	0	XXXXXXXXX
	1	1	1	1	xxxxxxxxx

Choice of Don't Care Terms

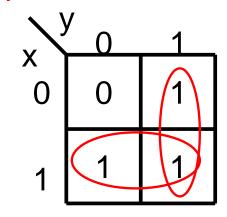
Don't care terms can be chosen as 0 or 1. Depending on the problem, we can choose the don't care term as 1 and use it to obtain a simpler Boolean expression



Don't care terms should only be included in encirclements if it helps in obtaining a larger grouping or smaller number of groups.

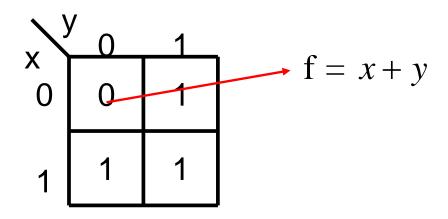
Minimisation of PoS Terms using K-map

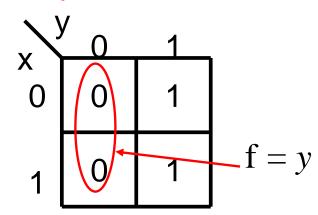
Example



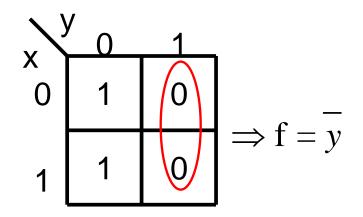
$$f = x + \overline{x} \cdot y + x \cdot y$$
$$= x + (\overline{x} + x) \cdot y$$
$$= x + y$$

Example

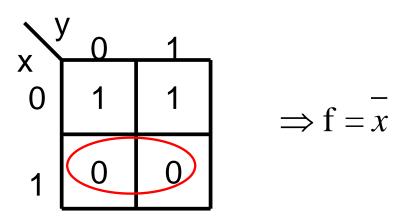


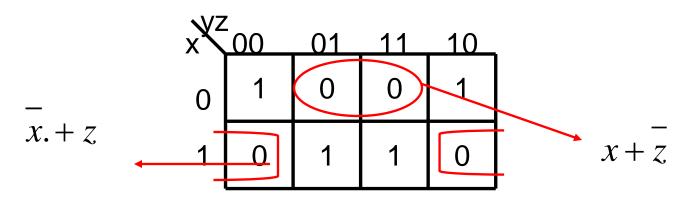


Example



Example





$$f = (x + z) \cdot (x + z)$$
 $\Rightarrow f = x \cdot z + x \cdot z$

$$x + y + z$$
 $wx = 00$ 01 11 10 $x + y + z$ $wx = 00$ 01 11

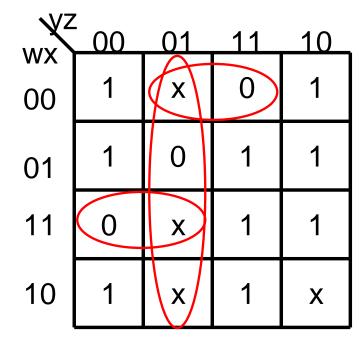
$$f = (x + y + z).(x + y + z).(w + y + z).(w + x)$$

Choosing x to be 0 to optimise PoS

Obtain the minimized PoS by suitably making don't care terms to be zero.

Rest of don't care(s) are to be chosen as one.

Example

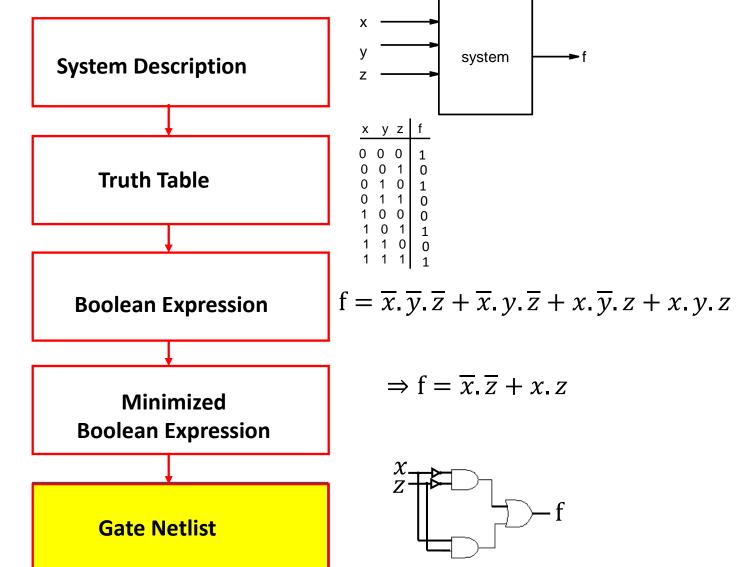


For grouping you assume:

- all selected x^s are '0'
- not selected x is '1'

$$f = (x + w + \overline{z}).(x + \overline{w} + y).(y + \overline{z})$$

Design Flow



Boolean Functions

So far we have been looking at SoP or PoS forms

For example:

$$f = \overline{x} \cdot \overline{z} + x \cdot z$$
 or $f = (\overline{x} + z) (x + \overline{z})$

This requires AND, OR and NOT operations.

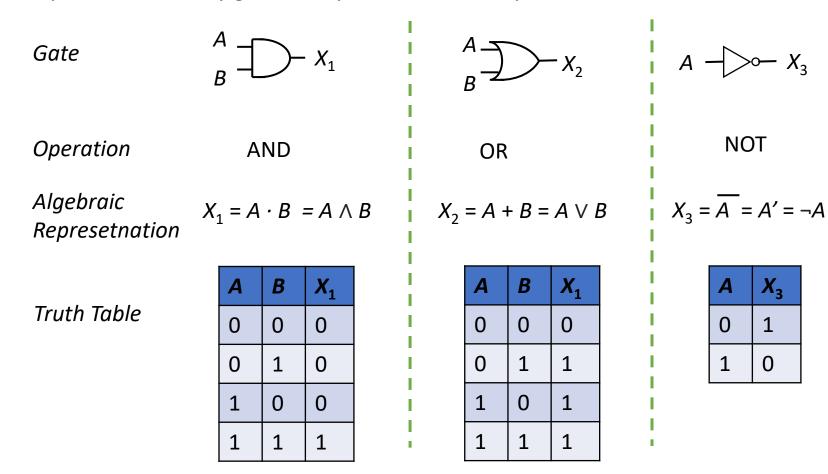
First choice use "Gates" to implement AND, OR and NOT operations.

(The Boolean function and interactions are gating the transmission of 1)

There may be other ways to implement the functions as well!

The Basic Gates

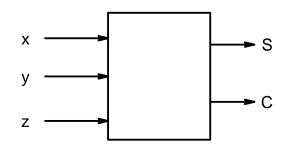
Popular introductory gates to represent Boolean operations: {AND, OR and NOT}

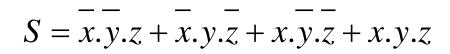


The three gates together form a basis set for representing Boolean relationship.

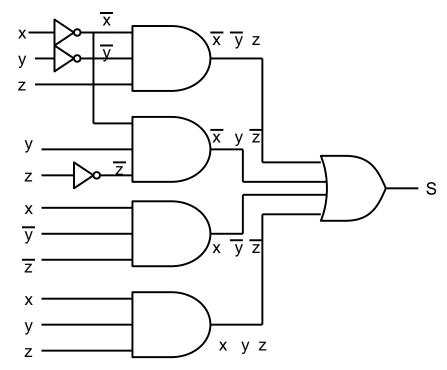
AND and OR gates with more than two inputs is a possibility and often used.

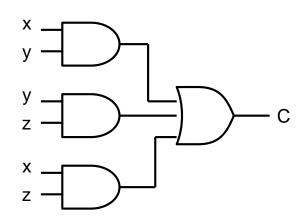
Implementing Boolean expressions using gates





$$C = x.y + x.z + y.z$$





Popular Universal Gates

Two gates are popular for implementing Boolean Logic in hardware

Gate

$$A \longrightarrow X_4$$

Operation

NAND

Algebraic Represetnation

$$X_4 = \overline{A \cdot B} = \overline{A} + \overline{B}$$

Truth Table

A	В	<i>X</i> ₁
0	0	1
0	1	1
1	0	1
1	1	0

$$A \longrightarrow X_5$$

NOR

$$X_5 = \overline{A + B} = \overline{A} \cdot \overline{B}$$

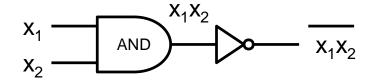
A	В	<i>X</i> ₁
0	0	1
0	1	0
1	0	0
1	1	0

<u>Each of these two gates</u> form a basis set for representing Boolean relationship They are examples of **Universal Gates** to implement Boolean functions.

More than two inputs NAND and NOR gates is a possibility and are often used.

Parsing the NAND and NOR Gates

NAND:
$$y = \overline{x_1 \cdot x_2}$$
 $x_1 \longrightarrow x_2$



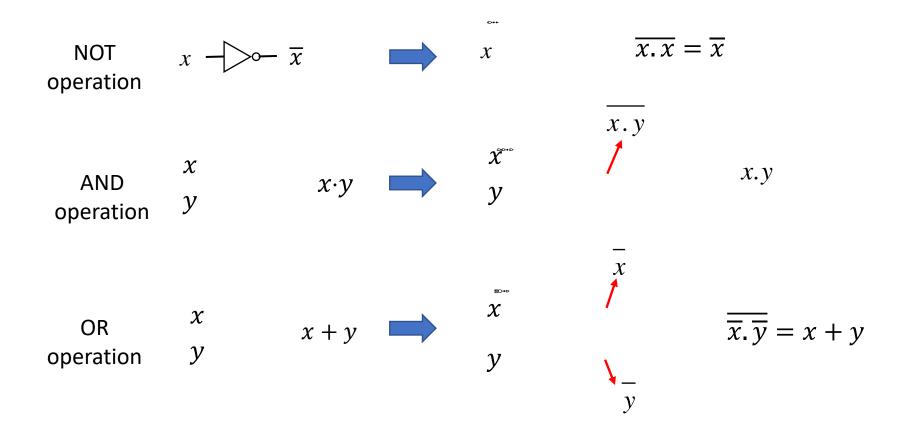


NOR:
$$y = \overline{x_1 + x_2}$$

NOR:
$$y = \overline{x_1 + x_2}$$
 $x_1 \longrightarrow \overline{x_1 + x_2}$ $x_2 \longrightarrow \overline{x_1 + x_2}$

$$x_1 \longrightarrow y$$

Basic Boolean Operations with NAND

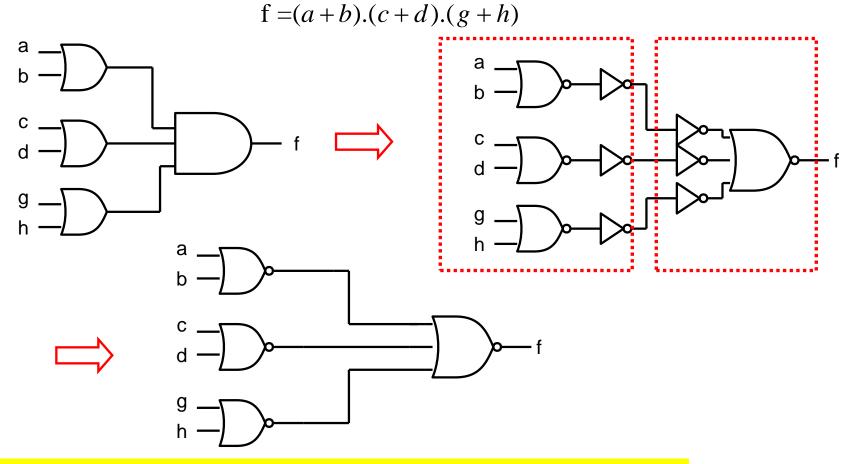


Exercise

Implement NOT, AND and OR with NOR gates

Implementing Boolean Function with Universal Gates

To implement using NOR gates, it is easiest to start with minimized Boolean expression in POS form

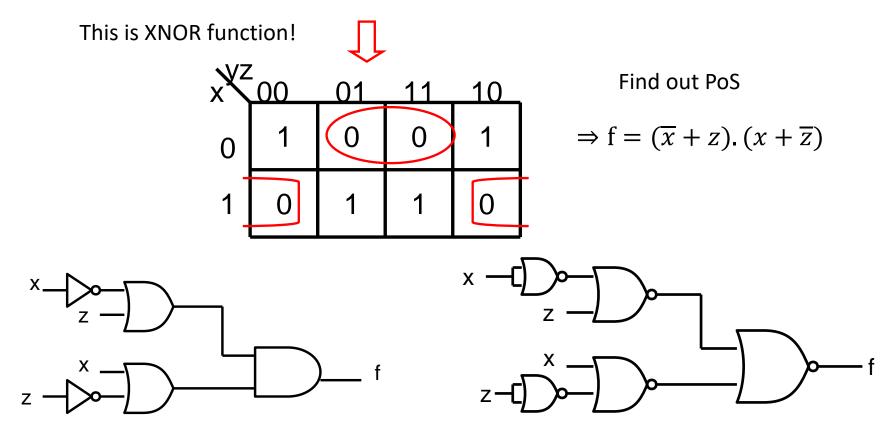


There is one-to-one mapping between OR-AND network and NOR network.

Similarly, there is a one-to-one mapping between AND-OR network and NAND network.

Example

Implement $f(x,z) = \overline{x} \cdot \overline{z} + x \cdot z$ using NOR gates



Similarly SoP expression can be implemented as NAND network.

→ first convert to SoP expression

→ then follow procedure outlined earlier