# Nadella Himavanth

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Portfolio

#### **Education**

**Vellore Institute of Technology** 

Sri Chaitanya Jr. Kalasala

2021 - Present Vellore, Tamil Nadu

Bachelor of Technology in Electronics & Communication (CGPA: 8.41 / 10.00)

2019 - 2021

Intermediate – MPC (Percentage: 86.4)

Hyderabad, Telangana

**Bhashyam High School** 

2011 - 2019

Secondary Education (GPA: 9.8 / 10.00)

Kurnool, Andhra Pradesh

## **Experience**

**BHEL** Hyderabad, Telangana

Project Intern | Cretificate

Aug 2023 - Sep 2023

• Comprehensive Study and Analysis: Conducted in-depth research and analysis on PLC and CNC machines, focusing on their functionalities, applications, and operational efficiencies.

## **Projects**

Smart Parking System using ESP8266 | Github | Arduino Programming, Internet of Things

• Developed an innovative parking system with the ESP8266 WiFi module and a web application template using Adafruit IO for real-time monitoring, management, and visualization of parking spaces.

Travelling Salesman Optimum Distance Calculator | Github | MatLab

 Developed a Travelling Salesman Optimum Distance Calculator to determine the shortest possible route that visits each city once and returns to the origin city, optimizing travel efficiency.

Accident Detection and Alert System | Github | AT89C51

 Developed an Accident Detection and Alert System using the AT89C51 microcontroller, automating accident notifications via GSM technology to alert registered mobile numbers, hospitals, and police stations.

**Obstacle Avoiding Vehicle** | Github | L298N Driver, Vehicle Chassis

• Developed an Arduino-based robot that uses Ultrasonic range finder sensors to avoid collisions.

**Life Metrics** | Github | MAX30100 Pulse Oximeter, Blynk app

 Developed an IoT-based health monitoring system using NodeMCU, MAX30100 Pulse Oximeter, OLED display, and Blynk app for continuous health tracking. Future enhancements include additional sensors and cloud-based analytics for comprehensive health assessment

#### Technical Skills

Programming Languages: Java, C, Embedded C, Verilog

Technologies: HTML, CSS, UI/UX

Course work: Internet of Things, Artificial Intelligence, Micro Processors and Micro controllers, Digital Systems and

Design, Embedded Systems, Computer Networks

Tools / Platforms: Figma, NI Multisim, LT Spice, AWR, CST Studio Suite, Cisco Packet, Thinker Cad, Canva, Adobe

Photoshop

# **Certifications** | **Achievements**

• Java (Basic) Certificate | SQL (Intermediate) Certificate

HackerRank

Technical Certificate Course Examinations in Lower and Higher Grades in Drawing.

**AP GOVT** 

#### Social Engagements

Chair Person: of Zero Waste Management - Student Club, VIT Vellore **Technical Head:** of Creativity Club - Student Club, VIT Vellore | Certificate

Sports-Engagements: Badminton, Chess, Cricket, Table-Tennis.

JAN 2024 - Present JAN 2023 - DEC 2023