**Ve270 Introduction to Logic Design Homework 6**

**Assigned: June 22, 2017**

**Due: June 29, 2017, at the beginning of the class.**

**The homework should be submitted in hard copies.**

1. Problem 4.2 (10 points)



1. Problem 4.3, using components to draw schematic (20 points)
2. Problem 4.46. Draw schematic. (20 Points).
3. Problem 4.57. Draw schematic (a), (b). (20 points)
4. Design a circuit called Receiver that receives two single bit signals, **Valid** and **Data\_in**, from another device called Transmitter. The **Valid** signal sent from the Transmitter will be a 1-clock cycle pulse. Once the Receiver receives the **Valid** pulse, it will start receiving 8 bits through port **Data\_in**, bit by bit. Once the 8 bit information is received, it should be copied into an 8-bit register called **RxReg**. Draw the schematic on component level (30 Points).