Name	Config	Description	Dagistan	Dogition	Volue	IO Command
Name	Config	Description	Register	Position	Value	IO Command
		PMIC_SLEEP_M0	0xFF388004	[0,2]	1	io -4 0xFF388004 0x00070001
	IOmux	PWM1_M0	0xFF388004	[0,2]	2	io -4 0xFF388004 0x00070002
		GPIO0_A4	0xFF388004	[0,2]	0	io -4 0xFF388004 0x00070000
	Pull	normal pullup	0xFF388038 0xFF388038	[8,9] [8,9]	0	io -4 0xFF388038 0x03000000 io -4 0xFF388038 0x03000100
	1 411	pulldown	0xFF388038	[8,9]	2	io -4 0xFF388038 0x03000200
	I/O Mode	input	0xFF380008	[4,4]	0	io -4 0xFF380008 0x00100000
	I/O Mode	output	0xFF380008	[4,4]	1	io -4 0xFF380008 0x00100010
	Output	low	0xFF380000	[4,4]	0	io -4 0xFF380000 0x00100000
GPIO0A4	•	high disable	0xFF380000 0xFF388030	[4,4] [4,4]	0	io -4 0xFF380000 0x00100010 io -4 0xFF388030 0x00100000
GF100A4	Input Ctrl	enable	0xFF388030	[4,4]	1	io -4 0xFF388030 0x00100000 io -4 0xFF388030 0x001000010
		disable	0xFF388058	[4,4]	0	io -4 0xFF388058 0x00100000
	Schmitt	enable	0xFF388058	[4,4]	1	io -4 0xFF388058 0x00100010
		level0	0xFF388018	[0,5]	1	io -4 0xFF388018 0x003F0001
	_	level1	0xFF388018	[0,5]	3	io -4 0xFF388018 0x003F0003
	Drive Strength	level2	0xFF388018	[0,5]	7	io -4 0xFF388018 0x003F0007
	_	level3	0xFF388018 0xFF388018	[0,5]	15 31	io -4 0xFF388018 0x003F000F io -4 0xFF388018 0x003F001F
		level5	0xFF388018	[0,5]	63	io -4 0xFF388018 0x003F001F
	ExtPort	read gpio data in input mode	0xFF380070	[4,4]	03	io -4 0xFF380070
		51 1		. , ,		
		DW/MO MO	0EE528000	[0 10]	1	is 40vEE528000 0v07000100
		PWM0_M0 AVS_ARM	0xFF538000 0xFF538000	[8,10]	1 2	io -4 0xFF538000 0x07000100 io -4 0xFF538000 0x07000200
	IOmux -	VICAP_D0_M1	0xFF538000	[8,10]	3	io -4 0xFF538000 0x07000300
		GPIO1_A2	0xFF538000	[8,10]	0	io -4 0xFF538000 0x07000000
	Pull	normal	0xFF5381C0	[4,5]	0	io -4 0xFF5381C0 0x00300000
		pullup	0xFF5381C0		1	io -4 0xFF5381C0 0x00300010
		pulldown	0xFF5381C0	[4,5]	2	io -4 0xFF5381C0 0x00300020
	I/O Mode	input output	0xFF530008 0xFF530008	[2,2]	0	io -4 0xFF530008 0x00040000 io -4 0xFF530008 0x00040004
		low	0xFF530000	[2,2]	0	io -4 0xFF530000 0x00040004
CDIO1 4 2	Output	high	0xFF530000	[2,2]	1	io -4 0xFF530000 0x00040004
GPIO1A2	Input Ctrl	disable	0xFF538180	[2,2]	0	io -4 0xFF538180 0x00040000
	input Cu1	enable	0xFF538180	[2,2]	1	io -4 0xFF538180 0x00040004
	Schmitt	disable	0xFF538280	[2,2]	0	io -4 0xFF538280 0x00040000
		enable level0	0xFF538280 0xFF538084	[2,2]	1 1	io -4 0xFF538280 0x00040004 io -4 0xFF538084 0x003F0001
		level1	0xFF538084	[0,5]	3	io -4 0xFF538084 0x003F0001
	D : G: 1	level2	0xFF538084	[0,5]	7	io -4 0xFF538084 0x003F0007
	Drive Strength	level3	0xFF538084	[0,5]	15	io -4 0xFF538084 0x003F000F
		level4	0xFF538084	[0,5]	31	io -4 0xFF538084 0x003F001F
		level5	0xFF538084	[0,5]	63	io -4 0xFF538084 0x003F003F
	ExtPort	read gpio data in input mode	0xFF530070	[2,2]		io -4 0xFF530070
		An Image may 25	0. 55550000	FO 103		. 40 5550000 0 0500000
	<u> </u>	A7_JTAG_TCK_M1	0xFF538008	[8,10]	1	io -4 0xFF538008 0x07000100
	IOmux	UART2_TX_M1 HPMCU_JTAG_TCK_M0	0xFF538008 0xFF538008	[8,10] [8,10]	3	io -4 0xFF538008 0x07000200 io -4 0xFF538008 0x07000300
	Tomax	LPMCU_JTAG_TCK_M0	0xFF538008	[8,10]	4	io -4 0xFF538008 0x07000300
		GPIO1_B2	0xFF538008	[8,10]	0	io -4 0xFF538008 0x07000000
		normal	0xFF5381C4	[4,5]	0	io -4 0xFF5381C4 0x00300000

Name	Config	Description	Register	Position	Value	IO Command
	Pull	pullup	0xFF5381C4	[4,5]	1	io -4 0xFF5381C4 0x00300010
	<u>-</u>	pulldown	0xFF5381C4	[4,5]	2	io -4 0xFF5381C4 0x00300020
	I/O M 1	input	0xFF530008	[10,10]	0	io -4 0xFF530008 0x04000000
	I/O Mode	output	0xFF530008	[10,10]	1	io -4 0xFF530008 0x04000400
	Output	low	0xFF530000	[10,10]	0	io -4 0xFF530000 0x04000000
GPIO1B2	Output	high	0xFF530000	[10,10]	1	io -4 0xFF530000 0x04000400
	Input Ctrl	disable	0xFF538184	[2,2]	0	io -4 0xFF538184 0x00040000
	Input cui	enable	0xFF538184	[2,2]	1	io -4 0xFF538184 0x00040004
	Schmitt	disable	0xFF538284	[2,2]	0	io -4 0xFF538284 0x00040000
-		enable	0xFF538284	[2,2]	1	io -4 0xFF538284 0x00040004
		level0	0xFF538094	[0,5]	1	io -4 0xFF538094 0x003F0001
	-	level1 level2	0xFF538094	[0,5]	7	io -4 0xFF538094 0x003F0003
	Drive Strength	level3	0xFF538094 0xFF538094	[0,5]	15	io -4 0xFF538094 0x003F0007 io -4 0xFF538094 0x003F000F
		level4	0xFF538094	[0,5]	31	io -4 0xFF538094 0x003F000F
		level5	0xFF538094	[0,5]	63	io -4 0xFF538094 0x003F003F
	ExtPort	read gpio data in input mode	0xFF530070	[10,10]	03	io -4 0xFF530070
	Extroit	A7_JTAG_TMS_M1	0xFF538008	[12,14]	1	io -4 0xFF538008 0x70001000
		UART2_RX_M1	0xFF538008	[12,14]	2	io -4 0xFF538008 0x70002000
	IOmux	HPMCU_JTAG_TMS_M0	0xFF538008	[12,14]	3	io -4 0xFF538008 0x70003000
		LPMCU_JTAG_TMS_M0	0xFF538008	[12,14]	4	io -4 0xFF538008 0x70004000
		GPIO1_B3	0xFF538008	[12,14]	0	io -4 0xFF538008 0x70000000
		normal	0xFF5381C4	[6,7]	0	io -4 0xFF5381C4 0x00C00000
	Pull	pullup	0xFF5381C4	[6,7]	1	io -4 0xFF5381C4 0x00C00040
		pulldown	0xFF5381C4	[6,7]	2	io -4 0xFF5381C4 0x00C00080
	I/O Mode	input	0xFF530008	[11,11]	0	io -4 0xFF530008 0x08000000
	I/O WIOGC	output	0xFF530008	[11,11]	1	io -4 0xFF530008 0x08000800
	Output	low	0xFF530000	[11,11]	0	io -4 0xFF530000 0x08000000
GPIO1B3	•	high	0xFF530000	[11,11]	1	io -4 0xFF530000 0x08000800
	Input Ctrl	disable	0xFF538184	[3,3]	0	io -4 0xFF538184 0x00080000
<u> </u>	_	enable	0xFF538184	[3,3]	1	io -4 0xFF538184 0x00080008
	Schmitt	disable	0xFF538284	[3,3]	0	io -4 0xFF538284 0x00080000
-		enable level0	0xFF538284 0xFF538094	[3,3]	1	io -4 0xFF538284 0x00080008 io -4 0xFF538094 0x3F000100
	-	level1	0xFF538094	[8,13] [8,13]	3	io -4 0xFF538094 0x3F000300
	F	level2	0xFF538094	[8,13]	7	io -4 0xFF538094 0x3F000700
	Drive Strength	level3	0xFF538094	[8,13]	15	io -4 0xFF538094 0x3F000F00
	ļ-	level4	0xFF538094	[8,13]	31	io -4 0xFF538094 0x3F001F00
		level5	0xFF538094	[8,13]	63	io -4 0xFF538094 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF530070	[11,11]		io -4 0xFF530070
		LCD_D7	0xFF538010	[0,2]	1	io -4 0xFF538010 0x00070001
		VICAP_D2_M1	0xFF538010	[0,2]	2	io -4 0xFF538010 0x00070002
	IOmux	PWM2_M2	0xFF538010	[0,2]	3	io -4 0xFF538010 0x00070003
	IOIIIux	SPI0_CS0N_M0	0xFF538010	[0,2]	4	io -4 0xFF538010 0x00070004
		SDMMC1_D1_M1	0xFF538010	[0,2]	5	io -4 0xFF538010 0x00070005
		GPIO1_C0	0xFF538010	[0,2]	0	io -4 0xFF538010 0x00070000
	_	normal	0xFF5381C8	[0,1]	0	io -4 0xFF5381C8 0x00030000
	Pull	pullup	0xFF5381C8	[0,1]	1	io -4 0xFF5381C8 0x00030001
		pulldown	0xFF5381C8	[0,1]	2	io -4 0xFF5381C8 0x00030002
	I/O Mode	input	0xFF53000C	[0,0]	0	io -4 0xFF53000C 0x00010000
-		output	0xFF53000C	[0,0]	0	io -4 0xFF53000C 0x00010001
GPIO1C0	Output	low high	0xFF530004 0xFF530004	[0,0]	1	io -4 0xFF530004 0x00010000 io -4 0xFF530004 0x00010001
<u> </u>		disable	0xFF530004 0xFF538188	[0,0]	0	io -4 0xFF530004 0x00010001 io -4 0xFF538188 0x00010000
	Input Ctrl	enable	0xFF538188	[0,0]	1	io -4 0xFF538188 0x00010000
	_	disable	0xFF538288	[0,0]	0	io -4 0xFF538288 0x00010000
	Schmitt	enable	0xFF538288	[0,0]	1	io -4 0xFF538288 0x00010001
		level0	0xFF5380A0	[0,5]	1	io -4 0xFF5380A0 0x003F0001
	ļ	level1	0xFF5380A0	[0,5]	3	io -4 0xFF5380A0 0x003F0003
1		level2	0xFF5380A0	[0,5]	7	io -4 0xFF5380A0 0x003F0007
1	Drive Strength	10 / 012	0.11 1 00 001 10			

Name	Config	Description	Register	Position	Value	IO Command
		level4	0xFF5380A0	[0,5]	31	io -4 0xFF5380A0 0x003F001F
	-	level5	0xFF5380A0	[0,5]	63	io -4 0xFF5380A0 0x003F003F
	ExtPort	read gpio data in input mode	0xFF530070	[16,16]		io -4 0xFF530070
	_	LCD_D6	0xFF538010	[4,6]	1	io -4 0xFF538010 0x00700010
	-	VICAP_D3_M1	0xFF538010	[4,6]	2	io -4 0xFF538010 0x00700020
	IOmux	PWM4_M2	0xFF538010	[4,6]	3	io -4 0xFF538010 0x00700030
	-	SPIO_CLK_MO	0xFF538010	[4,6]	4	io -4 0xFF538010 0x00700040
	-	SDMMC1_D0_M1	0xFF538010	[4,6]	5	io -4 0xFF538010 0x00700050
		GPIO1_C1	0xFF538010	[4,6]	0	io -4 0xFF538010 0x00700000
	Pull	normal pullup	0xFF5381C8 0xFF5381C8	[2,3] [2,3]	1	io -4 0xFF5381C8 0x000C0000 io -4 0xFF5381C8 0x000C0004
	T un	pulldown	0xFF5381C8	[2,3]	2	io -4 0xFF5381C8 0x000C0004
-		input	0xFF53000C	[1,1]	0	io -4 0xFF53000C 0x00020000
	I/O Mode	output	0xFF53000C	[1,1]	1	io -4 0xFF53000C 0x00020000
		low	0xFF530004	[1,1]	0	io -4 0xFF530004 0x00020002
GPIO1C1	Output	high	0xFF530004	[1,1]	1	io -4 0xFF530004 0x00020002
		disable	0xFF538188	[1,1]	0	io -4 0xFF538188 0x00020000
	Input Ctrl	enable	0xFF538188	[1,1]	1	io -4 0xFF538188 0x00020002
•	G 1 1	disable	0xFF538288	[1,1]	0	io -4 0xFF538288 0x00020000
	Schmitt	enable	0xFF538288	[1,1]	1	io -4 0xFF538288 0x00020002
		level0	0xFF5380A0		1	io -4 0xFF5380A0 0x3F000100
	•	level1	0xFF5380A0		3	io -4 0xFF5380A0 0x3F000300
	Duine Cturn eth	level2	0xFF5380A0	[8,13]	7	io -4 0xFF5380A0 0x3F000700
	Drive Strength	level3	0xFF5380A0	[8,13]	15	io -4 0xFF5380A0 0x3F000F00
		level4	0xFF5380A0	[8,13]	31	io -4 0xFF5380A0 0x3F001F00
		level5	0xFF5380A0	[8,13]	63	io -4 0xFF5380A0 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF530070	[17,17]		io -4 0xFF530070
		LCD_D5	0xFF538010	[8,10]	1	io -4 0xFF538010 0x07000100
		VICAP_D4_M1	0xFF538010	[8,10]	2	io -4 0xFF538010 0x07000200
	_	PWM5_M2	0xFF538010	[8,10]	3	io -4 0xFF538010 0x07000300
	IOmux	I2C4_SCL_M1	0xFF538010	[8,10]	4	io -4 0xFF538010 0x07000400
	-	SDMMC1_CLK_M1	0xFF538010	[8,10]	5	io -4 0xFF538010 0x07000500
		SPI0_MOSI_M0	0xFF538010	[8,10]	6	io -4 0xFF538010 0x07000600
		GPIO1_C2	0xFF538010	[8,10]	0	io -4 0xFF538010 0x07000000
	- 4	normal	0xFF5381C8		0	io -4 0xFF5381C8 0x00300000
	Pull	pullup	0xFF5381C8	[4,5]	1	io -4 0xFF5381C8 0x00300010
		pulldown	0xFF5381C8	[4,5]	2	io -4 0xFF5381C8 0x00300020
	I/O Mode	input	0xFF53000C	[2,2]	0	io -4 0xFF53000C 0x00040000 io -4 0xFF53000C 0x00040004
GPIO1C2		output low	0xFF53000C 0xFF530004	[2,2]	0	
GF101C2	Output	high	0xFF530004	[2,2] [2,2]	1	io -4 0xFF530004 0x00040000 io -4 0xFF530004 0x00040004
-		disable	0xFF538188	[2,2]	0	io -4 0xFF538188 0x00040004
	Input Ctrl	enable	0xFF538188	[2,2]	1	io -4 0xFF538188 0x00040000
	_	disable	0xFF538288	[2,2]	0	io -4 0xFF538288 0x00040004
	Schmitt -	enable	0xFF538288	[2,2]	1	io -4 0xFF538288 0x00040000
		level0	0xFF5380A4	[0,5]	1	io -4 0xFF5380A4 0x003F0001
		level1	0xFF5380A4	[0,5]	3	io -4 0xFF5380A4 0x003F0003
	D	level2	0xFF5380A4	[0,5]	7	io -4 0xFF5380A4 0x003F0007
	Drive Strength	level3	0xFF5380A4	[0,5]	15	io -4 0xFF5380A4 0x003F000F
	ļ	level4	0xFF5380A4	[0,5]	31	io -4 0xFF5380A4 0x003F001F
	ļ	level5	0xFF5380A4	[0,5]	63	io -4 0xFF5380A4 0x003F003F
	ExtPort	read gpio data in input mode	0xFF530070	[18,18]		io -4 0xFF530070
		LCD_D4	0xFF538010	[12,14]	1	io -4 0xFF538010 0x70001000
		VICAP_D5_M1	0xFF538010	[12,14]	2	io -4 0xFF538010 0x70002000
		PWM6_M2	0xFF538010	[12,14]	3	io -4 0xFF538010 0x70003000
	IOmux	I2C4_SDA_M1	0xFF538010	[12,14]	4	io -4 0xFF538010 0x70004000
		SDMMC1_CMD_M1	0xFF538010	[12,14]	5	io -4 0xFF538010 0x70005000
		SPI0_MISO_M0	0xFF538010	[12,14]	6	io -4 0xFF538010 0x70006000
		GPIO1_C3	0xFF538010	[12,14]	0	io -4 0xFF538010 0x70000000
		normal	0xFF5381C8	[6,7]	0	io -4 0xFF5381C8 0x00C00000
	Pull	pullup	0xFF5381C8	[6,7]	1	io -4 0xFF5381C8 0x00C00040

Name	Config	Description	Register	Position	Value	IO Command
		pulldown	0xFF5381C8	[6,7]	2	io -4 0xFF5381C8 0x00C00080
	I/O Mode	input	0xFF53000C	[3,3]	0	io -4 0xFF53000C 0x00080000
	I/O Wode	output	0xFF53000C	[3,3]	1	io -4 0xFF53000C 0x00080008
GPIO1C3	Output	low	0xFF530004	[3,3]	0	io -4 0xFF530004 0x00080000
	Guspur	high	0xFF530004	[3,3]	1	io -4 0xFF530004 0x00080008
	Input Ctrl	disable	0xFF538188	[3,3]	0	io -4 0xFF538188 0x00080000
_	-	enable	0xFF538188	[3,3]	1	io -4 0xFF538188 0x00080008
	Schmitt	disable enable	0xFF538288 0xFF538288	[3,3]	0	io -4 0xFF538288 0x00080000 io -4 0xFF538288 0x00080008
		level0	0xFF538288	[3,3]	1	io -4 0xFF538288 0x00080008 io -4 0xFF5380A4 0x3F000100
		level1	0xFF5380A4	[8,13]	3	io -4 0xFF5380A4 0x3F000300
		level2	0xFF5380A4	[8,13]	7	io -4 0xFF5380A4 0x3F000700
	Drive Strength	level3	0xFF5380A4	[8,13]	15	io -4 0xFF5380A4 0x3F000F00
		level4	0xFF5380A4	[8,13]	31	io -4 0xFF5380A4 0x3F001F00
		level5	0xFF5380A4	[8,13]	63	io -4 0xFF5380A4 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF530070	[19,19]		io -4 0xFF530070
		LCD_D3	0xFF538014	[0,2]	1	io -4 0xFF538014 0x00070001
		VICAP_D6_M1	0xFF538014	[0,2]	2	io -4 0xFF538014 0x00070002
	IOmux	PWM8_M1	0xFF538014	[0,2]	3	io -4 0xFF538014 0x00070003
	IOmux	UART4_RX_M1	0xFF538014	[0,2]	4	io -4 0xFF538014 0x00070004
		SDMMC1_D3_M1	0xFF538014	[0,2]	5	io -4 0xFF538014 0x00070005
		GPIO1_C4	0xFF538014	[0,2]	0	io -4 0xFF538014 0x00070000
		normal	0xFF5381C8	[8,9]	0	io -4 0xFF5381C8 0x03000000
	Pull	pullup	0xFF5381C8	[8,9]	1	io -4 0xFF5381C8 0x03000100
		pulldown	0xFF5381C8	[8,9]	2	io -4 0xFF5381C8 0x03000200
	I/O Mode	input	0xFF53000C	[4,4]	0	io -4 0xFF53000C 0x00100000
		output	0xFF53000C	[4,4]	1	io -4 0xFF53000C 0x00100010
GPIO1C4	Output	low	0xFF530004	[4,4]	0	io -4 0xFF530004 0x00100000
	•	high	0xFF530004	[4,4]	1	io -4 0xFF530004 0x00100010
	Input Ctrl	disable	0xFF538188	[4,4]	0	io -4 0xFF538188 0x00100000
		enable disable	0xFF538188	[4,4]	0	io -4 0xFF538188 0x00100010
	Schmitt	enable	0xFF538288 0xFF538288	[4,4] [4,4]	1	io -4 0xFF538288 0x00100000 io -4 0xFF538288 0x00100010
		level0	0xFF5380A8	[0,5]	1	io -4 0xFF5380A8 0x003F0001
		level1	0xFF5380A8		3	io -4 0xFF5380A8 0x003F0003
		level2	0xFF5380A8	[0,5]	7	io -4 0xFF5380A8 0x003F0007
	Drive Strength	level3	0xFF5380A8	[0,5]	15	io -4 0xFF5380A8 0x003F000F
		level4	0xFF5380A8	[0,5]	31	io -4 0xFF5380A8 0x003F001F
		level5	0xFF5380A8	[0,5]	63	io -4 0xFF5380A8 0x003F003F
	ExtPort	read gpio data in input mode	0xFF530070	[20,20]		io -4 0xFF530070
		LCD_D2	0xFF538014	[4,6]	1	io -4 0xFF538014 0x00700010
		VICAP_D7_M1	0xFF538014	[4,6]	2	io -4 0xFF538014 0x00700020
	IOmux	PWM9_M1	0xFF538014	[4,6]	3	io -4 0xFF538014 0x00700030
	TOTHUX	UART4_TX_M1	0xFF538014	[4,6]	4	io -4 0xFF538014 0x00700040
	_	SDMMC1_D2_M1	0xFF538014	[4,6]	5	io -4 0xFF538014 0x00700050
		GPIO1_C5	0xFF538014	[4,6]	0	io -4 0xFF538014 0x00700000
		normal	0xFF5381C8	[10,11]	0	io -4 0xFF5381C8 0x0C000000
	Pull	pullup	0xFF5381C8	[10,11]	1	io -4 0xFF5381C8 0x0C000400
		pulldown	0xFF5381C8	[10,11]	2	io -4 0xFF5381C8 0x0C000800
	I/O Mode	input	0xFF53000C	[5,5]	0	io -4 0xFF53000C 0x00200000
-		output	0xFF53000C	[5,5]	0	io -4 0xFF53000C 0x00200020
GPIO1C5	Output	low high	0xFF530004 0xFF530004	[5,5] [5,5]	1	io -4 0xFF530004 0x00200000 io -4 0xFF530004 0x00200020
		disable	0xFF538188	[5,5]	0	io -4 0xFF538188 0x00200020
	Input Ctrl	enable	0xFF538188	[5,5]	1	io -4 0xFF538188 0x00200000
		disable	0xFF538288	[5,5]	0	io -4 0xFF538288 0x00200020
	Schmitt	enable	0xFF538288	[5,5]	1	io -4 0xFF538288 0x00200020
		level0	0xFF5380A8	[8,13]	1	io -4 0xFF5380A8 0x3F000100
	ļ	level1	0xFF5380A8	[8,13]	3	io -4 0xFF5380A8 0x3F000300
	Drive Strength	level2	0xFF5380A8	[8,13]	7	io -4 0xFF5380A8 0x3F000700
	Liriya Stronath		0xFF5380A8	[8,13]	15	io -4 0xFF5380A8 0x3F000F00

Name	Config	Description	Register	Position	Value	IO Command
		level4	0xFF5380A8	[8,13]	31	io -4 0xFF5380A8 0x3F001F00
		level5	0xFF5380A8	[8,13]	63	io -4 0xFF5380A8 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF530070	[21,21]		io -4 0xFF530070
		LCD_D1	0xFF538014	[8,10]	1	io -4 0xFF538014 0x07000100
		VICAP_D8_M1	0xFF538014	[8,10]	2	io -4 0xFF538014 0x07000200
	IOmux	PWM10_M1	0xFF538014	[8,10]	3	io -4 0xFF538014 0x07000300
		UART4_RTSN_M1	0xFF538014	[8,10]	4	io -4 0xFF538014 0x07000400
		GPIO1_C6	0xFF538014	[8,10]	0	io -4 0xFF538014 0x07000000
		normal	0xFF5381C8	[12,13]	0	io -4 0xFF5381C8 0x30000000
	Pull	pullup	0xFF5381C8	[12,13]	1	io -4 0xFF5381C8 0x30001000
-		pulldown	0xFF5381C8	[12,13]	2	io -4 0xFF5381C8 0x30002000
	I/O Mode	input	0xFF53000C	[6,6]	0	io -4 0xFF53000C 0x00400000
		output low	0xFF53000C 0xFF530004	[6,6]	0	io -4 0xFF53000C 0x00400040 io -4 0xFF530004 0x00400000
GPIO1C6	Output	high	0xFF530004	[6,6] [6,6]	1	io -4 0xFF530004 0x00400000
Grioreo		disable	0xFF538188	[6,6]	0	io -4 0xFF538188 0x00400000
	Input Ctrl	enable	0xFF538188	[6,6]	1	io -4 0xFF538188 0x00400040
		disable	0xFF538288	[6,6]	0	io -4 0xFF538288 0x00400000
	Schmitt	enable	0xFF538288	[6,6]	1	io -4 0xFF538288 0x00400040
		level0	0xFF5380AC	[0,5]	1	io -4 0xFF5380AC 0x003F0001
		level1	0xFF5380AC	[0,5]	3	io -4 0xFF5380AC 0x003F0003
	Duine Channeth	level2	0xFF5380AC	[0,5]	7	io -4 0xFF5380AC 0x003F0007
	Drive Strength	level3	0xFF5380AC	[0,5]	15	io -4 0xFF5380AC 0x003F000F
		level4	0xFF5380AC	[0,5]	31	io -4 0xFF5380AC 0x003F001F
		level5	0xFF5380AC	[0,5]	63	io -4 0xFF5380AC 0x003F003F
	ExtPort	read gpio data in input mode	0xFF530070	[22,22]		io -4 0xFF530070
		LCD_D0	0xFF538014	[12,14]	1	io -4 0xFF538014 0x70001000
		VICAP_D9_M1	0xFF538014	[12,14]	2	io -4 0xFF538014 0x70002000
	IOmux	PWM11_IR_M1	0xFF538014	[12,14]	3	io -4 0xFF538014 0x70003000
		UART4_CTSN_M1	0xFF538014	[12,14]	4	io -4 0xFF538014 0x70004000
		GPIO1_C7	0xFF538014	[12,14]	0	io -4 0xFF538014 0x70000000
	Pull	normal	0xFF5381C8	[14,15]	0	io -4 0xFF5381C8 0xC0000000
		pullup	0xFF5381C8	[14,15]	1	io -4 0xFF5381C8 0xC0004000
		pulldown input	0xFF5381C8 0xFF53000C	[14,15] [7,7]	0	io -4 0xFF5381C8 0xC0008000 io -4 0xFF53000C 0x00800000
	I/O Mode	output	0xFF53000C	[7,7]	1	io -4 0xFF53000C 0x00800000
		low	0xFF530004	[7,7]	0	io -4 0xFF530004 0x00800000
GPIO1C7	Output	high	0xFF530004	[7,7]	1	io -4 0xFF530004 0x00800080
		disable	0xFF538188	[7,7]	0	io -4 0xFF538188 0x00800000
	Input Ctrl	enable	0xFF538188	[7,7]	1	io -4 0xFF538188 0x00800080
	0.1. 1	disable	0xFF538288	[7,7]	0	io -4 0xFF538288 0x00800000
	Schmitt	enable	0xFF538288	[7,7]	1	io -4 0xFF538288 0x00800080
		level0	0xFF5380AC	[8,13]	1	io -4 0xFF5380AC 0x3F000100
		level1	0xFF5380AC	[8,13]	3	io -4 0xFF5380AC 0x3F000300
	Drive Strength	level2	0xFF5380AC	[8,13]	7	io -4 0xFF5380AC 0x3F000700
	Dirve Strength	level3	0xFF5380AC	[8,13]	15	io -4 0xFF5380AC 0x3F000F00
		level4	0xFF5380AC	[8,13]	31	io -4 0xFF5380AC 0x3F001F00
		level5	0xFF5380AC	[8,13]	63	io -4 0xFF5380AC 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF530070	[23,23]		io -4 0xFF530070
		LCD_DEN	0xFF538018	[0,2]	1	io -4 0xFF538018 0x00070001
		VICAP_CLKIN_M1	0xFF538018	[0,2]	2	io -4 0xFF538018 0x00070002
	IOmux	PWM3_IR_M2	0xFF538018	[0,2]	3	io -4 0xFF538018 0x00070003
		UART5_RTSN_M1 UART3_TX_M1	0xFF538018 0xFF538018	[0,2]	5	io -4 0xFF538018 0x00070004 io -4 0xFF538018 0x00070005
		GPIO1_D0	0xFF538018 0xFF538018	[0,2]	0	io -4 0xFF538018 0x00070000
		normal	0xFF538018	[0,1]	0	io -4 0xFF538018 0x00070000 io -4 0xFF5381CC 0x00030000
	Pull	pullup	0xFF5381CC	[0,1]	1	io -4 0xFF5381CC 0x00030000
		pulldown	0xFF5381CC	[0,1]	2	io -4 0xFF5381CC 0x00030002
	7/0	input	0xFF53000C	[8,8]	0	io -4 0xFF53000C 0x01000000
	I/O Mode	output	0xFF53000C	[8,8]	1	io -4 0xFF53000C 0x01000100
CDIO1DO	0.1.1	low	0xFF530004	[8,8]	0	io -4 0xFF530004 0x01000000
CDIU1DU	Outnut		-	/ -	!	

Name	Config	Description	Dagistan	Docition	Value	IO Command
Name	Config	Description	Register	Position	vaiue	10 Command
<del>OI IOIDO</del>	Output	high	0xFF530004	[8,8]	1	io -4 0xFF530004 0x01000100
	Input Ctrl	disable	0xFF53818C	[0,0]	0	io -4 0xFF53818C 0x00010000
		enable	0xFF53818C	[0,0]	1	io -4 0xFF53818C 0x00010001
	Schmitt	disable	0xFF53828C	[0,0]	0	io -4 0xFF53828C 0x00010000
		enable	0xFF53828C	[0,0]	1	io -4 0xFF53828C 0x00010001
	-	level0	0xFF5380B0	[0,5]	1	io -4 0xFF5380B0 0x003F0001
	-	level1	0xFF5380B0	[0,5]	3	io -4 0xFF5380B0 0x003F0003
	Drive Strength	level2	0xFF5380B0	[0,5]	7	io -4 0xFF5380B0 0x003F0007
	-	level4	0xFF5380B0 0xFF5380B0	[0,5]	15 31	io -4 0xFF5380B0 0x003F000F io -4 0xFF5380B0 0x003F001F
	-	level5	0xFF5380B0	[0,5]	63	io -4 0xFF5380B0 0x003F001F
	ExtPort	read gpio data in input mode	0xFF530070	[24,24]	03	io -4 0xFF530070
	Extroit	LCD_HSYNC	0xFF538018	[4,6]	1	io -4 0xFF538018 0x00700010
	-	VICAP_HSYNC_M1	0xFF538018	[4,6]	2	io -4 0xFF538018 0x00700020
		PWM10_M2	0xFF538018	[4,6]	3	io -4 0xFF538018 0x00700030
	IOmux	UART5_CTSN_M1	0xFF538018	[4,6]	4	io -4 0xFF538018 0x00700040
		UART3_RX_M1	0xFF538018	[4,6]	5	io -4 0xFF538018 0x00700050
		GPIO1_D1	0xFF538018	[4,6]	0	io -4 0xFF538018 0x00700000
		normal	0xFF5381CC	[2,3]	0	io -4 0xFF5381CC 0x000C0000
	Pull	pullup	0xFF5381CC	[2,3]	1	io -4 0xFF5381CC 0x000C0004
		pulldown	0xFF5381CC	[2,3]	2	io -4 0xFF5381CC 0x000C0008
	I/O Mode	input	0xFF53000C	[9,9]	0	io -4 0xFF53000C 0x02000000
	I/O Mode	output	0xFF53000C	[9,9]	1	io -4 0xFF53000C 0x02000200
GPIO1D1	Output	low	0xFF530004	[9,9]	0	io -4 0xFF530004 0x02000000
GIIOIDI	Output	high	0xFF530004	[9,9]	1	io -4 0xFF530004 0x02000200
	Input Ctrl	disable	0xFF53818C	[1,1]	0	io -4 0xFF53818C 0x00020000
	input Cur	enable	0xFF53818C	[1,1]	1	io -4 0xFF53818C 0x00020002
	Schmitt	disable	0xFF53828C	[1,1]	0	io -4 0xFF53828C 0x00020000
	~	enable	0xFF53828C	[1,1]	1	io -4 0xFF53828C 0x00020002
	_	level0	0xFF5380B0	[8,13]	1	io -4 0xFF5380B0 0x3F000100
		level1	0xFF5380B0	[8,13]	3	io -4 0xFF5380B0 0x3F000300
	Drive Strength	level2	0xFF5380B0	[8,13]	7	io -4 0xFF5380B0 0x3F000700
		level3	0xFF5380B0	[8,13]	15	io -4 0xFF5380B0 0x3F000F00
		level4 level5	0xFF5380B0 0xFF5380B0	[8,13] [8,13]	31 63	io -4 0xFF5380B0 0x3F001F00 io -4 0xFF5380B0 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF530070	[25,25]	03	io -4 0xFF5380B0 0x5F003F00
	Extroit	LCD_VSYNC	0xFF538018	[8,10]	1	io -4 0xFF538018 0x07000100
	-	VICAP_VSYNC_M1	0xFF538018	[8,10]	2	io -4 0xFF538018 0x07000100
	_	I2C3_SDA_M1	0xFF538018	[8,10]	3	io -4 0xFF538018 0x07000200
		UART5_RX_M1	0xFF538018	[8,10]	4	io -4 0xFF538018 0x07000400
	IOmux	SPI0_CS1N_M0	0xFF538018	[8,10]	5	io -4 0xFF538018 0x07000500
		PWM0_M1	0xFF538018	[8,10]	6	io -4 0xFF538018 0x07000600
		DSMAUDIO_P	0xFF538018	[8,10]	7	io -4 0xFF538018 0x07000700
		GPIO1_D2	0xFF538018	[8,10]	0	io -4 0xFF538018 0x07000000
		normal	0xFF5381CC	[4,5]	0	io -4 0xFF5381CC 0x00300000
	Pull	pullup	0xFF5381CC	[4,5]	1	io -4 0xFF5381CC 0x00300010
		pulldown	0xFF5381CC	[4,5]	2	io -4 0xFF5381CC 0x00300020
	I/O Mode	input	0xFF53000C	[10,10]	0	io -4 0xFF53000C 0x04000000
GPIO1D2		output	0xFF53000C	[10,10]	1	io -4 0xFF53000C 0x04000400
0.10102	Output	low	0xFF530004	[10,10]	0	io -4 0xFF530004 0x04000000
	Jacpat	high	0xFF530004	[10,10]	1	io -4 0xFF530004 0x04000400
	Input Ctrl	disable	0xFF53818C	[2,2]	0	io -4 0xFF53818C 0x00040000
		enable	0xFF53818C	[2,2]	1	io -4 0xFF53818C 0x00040004
	Schmitt	disable	0xFF53828C	[2,2]	0	io -4 0xFF53828C 0x00040000
		enable	0xFF53828C	[2,2]	1	io -4 0xFF53828C 0x00040004
		level0	0xFF5380B4	[0,5]	1	io -4 0xFF5380B4 0x003F0001
	-	level1	0xFF5380B4	[0,5]	3	io -4 0xFF5380B4 0x003F0003
	Drive Strength	level2	0xFF5380B4	[0,5]	7	io -4 0xFF5380B4 0x003F0007
	-	level3	0xFF5380B4	[0,5]	15	io -4 0xFF5380B4 0x003F000F
	-	level4 level5	0xFF5380B4 0xFF5380B4	[0,5]	63	io -4 0xFF5380B4 0x003F001F io -4 0xFF5380B4 0x003F003F
1		ICVCIJ	UALT 3300D4	[0,5]	UJ	10 -4 0ALT 3300D4 0X003F003F

Name	Config	Description	Register	Position	Value	IO Command
	ExtPort	read gpio data in input mode	0xFF530070	[26,26]		io -4 0xFF530070
		LCD_CLK	0xFF538018	[12,14]	1	io -4 0xFF538018 0x70001000
		VICAP_CLKOUT_M1	0xFF538018	[12,14]	2	io -4 0xFF538018 0x70002000
		I2C3_SCL_M1	0xFF538018	[12,14]	3	io -4 0xFF538018 0x70003000
	IOmux	UART5_TX_M1	0xFF538018	[12,14]	4	io -4 0xFF538018 0x70004000
		PWM11_IR_M2	0xFF538018	[12,14]	5	io -4 0xFF538018 0x70005000
	_	DSMAUDIO_N	0xFF538018	[12,14]	7	io -4 0xFF538018 0x70007000
		GPIO1_D3	0xFF538018	[12,14]	0	io -4 0xFF538018 0x70000000
		normal	0xFF5381CC	[6,7]	0	io -4 0xFF5381CC 0x00C00000
	Pull	pullup	0xFF5381CC	[6,7]	1	io -4 0xFF5381CC 0x00C00040
		pulldown	0xFF5381CC	[6,7]	2	io -4 0xFF5381CC 0x00C00080
	I/O Mode	input	0xFF53000C	[11,11]	0	io -4 0xFF53000C 0x08000000
GPIO1D3		output	0xFF53000C	[11,11]	1	io -4 0xFF53000C 0x08000800
GPIOIDS	Output	low high	0xFF530004 0xFF530004	[11,11]	0	io -4 0xFF530004 0x08000000 io -4 0xFF530004 0x08000800
-		disable	0xFF53818C	[3,3]	0	io -4 0xFF53818C 0x00080000
	Input Ctrl	enable	0xFF53818C	[3,3]	1	io -4 0xFF53818C 0x00080000
-		disable	0xFF53818C	[3,3]	0	io -4 0xFF53818C 0x00080008
	Schmitt	enable	0xFF53828C	[3,3]	1	io -4 0xFF53828C 0x00080000
		level0	0xFF5380B4	[8,13]	1	io -4 0xFF5380B4 0x3F000100
	-	level1	0xFF5380B4	[8,13]	3	io -4 0xFF5380B4 0x3F000300
	-	level2	0xFF5380B4	[8,13]	7	io -4 0xFF5380B4 0x3F000700
	Drive Strength	level3	0xFF5380B4	[8,13]	15	io -4 0xFF5380B4 0x3F000F00
	-	level4	0xFF5380B4	[8,13]	31	io -4 0xFF5380B4 0x3F001F00
	-	level5	0xFF5380B4	[8,13]	63	io -4 0xFF5380B4 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF530070	[27,27]		io -4 0xFF530070
		SF 2		[,]		
	IOmux	SDMMC0_DET	0xFF558040	[4,6]	1	io -4 0xFF558040 0x00700010
	10111411	GPIO3_A1	0xFF558040	[4,6]	0	io -4 0xFF558040 0x00700000
		normal	0xFF5581E0	[2,3]	0	io -4 0xFF5581E0 0x000C0000
	Pull	pullup	0xFF5581E0	[2,3]	1	io -4 0xFF5581E0 0x000C0004
		pulldown	0xFF5581E0	[2,3]	2	io -4 0xFF5581E0 0x000C0008
	I/O Mode	input	0xFF550008	[1,1]	0	io -4 0xFF550008 0x00020000
		output	0xFF550008	[1,1]	1	io -4 0xFF550008 0x00020002
	Output	low	0xFF550000	[1,1]	0	io -4 0xFF550000 0x00020000
	•	high	0xFF550000	[1,1]	1	io -4 0xFF550000 0x00020002
GPIO3A1	Input Ctrl	disable	0xFF5581A0	[1,1]	0	io -4 0xFF5581A0 0x00020000
	_	enable	0xFF5581A0	[1,1]	1	io -4 0xFF5581A0 0x00020002
	Schmitt	disable	0xFF5582A0	[1,1]	0	io -4 0xFF5582A0 0x00020000
		enable	0xFF5582A0	[1,1]	1	io -4 0xFF5582A0 0x00020002
	-	level0	0xFF558100	[8,13]	3	io -4 0xFF558100 0x3F000100
	-	level1 level2	0xFF558100 0xFF558100	[8,13]	7	io -4 0xFF558100 0x3F000300 io -4 0xFF558100 0x3F000700
	Drive Strength	level3	0xFF558100	[8,13]	15	io -4 0xFF558100 0x3F000700
	-	level4	0xFF558100	[8,13]	31	io -4 0xFF558100 0x3F000F00
	-	level5	0xFF558100	[8,13]	63	io -4 0xFF558100 0x3F001F00
	ExtPort	read gpio data in input mode	0xFF550070	[1,1]	03	io -4 0xFF558100 0x5F005F00
	DAII OII	SDMMC0_D1	0xFF558040	[8,10]	1	io -4 0xFF558040 0x07000100
		UART2_TX_M0	0xFF558040	[8,10]	2	io -4 0xFF558040 0x07000100
	IOmux	TEST_CLK0_OUT	0xFF558040	[8,10]	3	io -4 0xFF558040 0x07000200
	10mm	PWM9_M0	0xFF558040	[8,10]	4	io -4 0xFF558040 0x07000300
	-	GPIO3_A2	0xFF558040	[8,10]	0	io -4 0xFF558040 0x07000000
ı .		01100_112	5.12 1 55 50 40	[0,10]		-5 . 5.12 1 5 5 5 1 5 GAO 10 000000

Name	Config	Description	Register	Position	Value	IO Command
		normal	0xFF5581E0	[4,5]	0	io -4 0xFF5581E0 0x00300000
	Pull	pullup	0xFF5581E0	[4,5]	1	io -4 0xFF5581E0 0x00300010
		pulldown	0xFF5581E0	[4,5]	2	io -4 0xFF5581E0 0x00300020
	I/O Mode	input	0xFF550008	[2,2]	0	io -4 0xFF550008 0x00040000
	2 0 1/1000	output	0xFF550008	[2,2]	1	io -4 0xFF550008 0x00040004
CDY CO A A	Output	low	0xFF550000	[2,2]	0	io -4 0xFF550000 0x00040000
GPIO3A2	_	high	0xFF550000	[2,2]	1	io -4 0xFF550000 0x00040004
	Input Ctrl	disable enable	0xFF5581A0 0xFF5581A0	[2,2]	0	io -4 0xFF5581A0 0x00040000 io -4 0xFF5581A0 0x00040004
-		disable	0xFF5581A0	[2,2]	0	io -4 0xFF5582A0 0x00040004
	Schmitt	enable	0xFF5582A0	[2,2]	1	io -4 0xFF5582A0 0x00040004
		level0	0xFF558104	[0,5]	1	io -4 0xFF558104 0x003F0001
	-	level1	0xFF558104	[0,5]	3	io -4 0xFF558104 0x003F0003
	-	level2	0xFF558104	[0,5]	7	io -4 0xFF558104 0x003F0007
	Drive Strength	level3	0xFF558104	[0,5]	15	io -4 0xFF558104 0x003F000F
	-	level4	0xFF558104	[0,5]	31	io -4 0xFF558104 0x003F001F
	•	level5	0xFF558104	[0,5]	63	io -4 0xFF558104 0x003F003F
	ExtPort	read gpio data in input mode	0xFF550070	[2,2]		io -4 0xFF550070
		SDMMC0_D0	0xFF558040	[12,14]	1	io -4 0xFF558040 0x70001000
		UART2_RX_M0	0xFF558040	[12,14]	2	io -4 0xFF558040 0x70002000
	IOmux	TEST_CLK1_OUT	0xFF558040	[12,14]	3	io -4 0xFF558040 0x70003000
		PWM8_M0	0xFF558040	[12,14]	4	io -4 0xFF558040 0x70004000
		GPIO3_A3	0xFF558040	[12,14]	0	io -4 0xFF558040 0x70000000
		normal	0xFF5581E0	[6,7]	0	io -4 0xFF5581E0 0x00C00000
	Pull	pullup	0xFF5581E0	[6,7]	1	io -4 0xFF5581E0 0x00C00040
		pulldown	0xFF5581E0	[6,7]	2	io -4 0xFF5581E0 0x00C00080
	I/O Mode	input	0xFF550008	[3,3]	0	io -4 0xFF550008 0x00080000
	I O Mode	output	0xFF550008	[3,3]	1	io -4 0xFF550008 0x00080008
	Output	low	0xFF550000	[3,3]	0	io -4 0xFF550000 0x00080000
GPIO3A3	- · · · · ·	high	0xFF550000	[3,3]	1	io -4 0xFF550000 0x00080008
	Input Ctrl	disable	0xFF5581A0	[3,3]	0	io -4 0xFF5581A0 0x00080000
-		enable	0xFF5581A0	[3,3]	1	io -4 0xFF5581A0 0x00080008
	Schmitt	disable	0xFF5582A0	[3,3]	0	io -4 0xFF5582A0 0x00080000
		enable level0	0xFF5582A0 0xFF558104	[3,3] [8,13]	1 1	io -4 0xFF5582A0 0x00080008 io -4 0xFF558104 0x3F000100
	_	level1	0xFF558104	[8,13]	3	io -4 0xFF558104 0x3F000100
	-	level2	0xFF558104	[8,13]	7	io -4 0xFF558104 0x3F000300
	Drive Strength	level3	0xFF558104	[8,13]	15	io -4 0xFF558104 0x3F000F00
	-	level4	0xFF558104	[8,13]	31	io -4 0xFF558104 0x3F001F00
	-	level5	0xFF558104	[8,13]	63	io -4 0xFF558104 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF550070	[3,3]	0.0	io -4 0xFF550070
		SDMMC0_CLK	0xFF558044	[0,2]	1	io -4 0xFF558044 0x00070001
	•	UART5_RTSN_M0	0xFF558044	[0,2]	2	io -4 0xFF558044 0x00070002
	10	I2C0_SCL_M2	0xFF558044	[0,2]	3	io -4 0xFF558044 0x00070003
	IOmux	LPMCU_JTAG_TCK_M1	0xFF558044	[0,2]	4	io -4 0xFF558044 0x00070004
		PWM10_M0	0xFF558044	[0,2]	5	io -4 0xFF558044 0x00070005
		GPIO3_A4	0xFF558044	[0,2]	0	io -4 0xFF558044 0x00070000
		normal	0xFF5581E0	[8,9]	0	io -4 0xFF5581E0 0x03000000
	Pull	pullup	0xFF5581E0	[8,9]	1	io -4 0xFF5581E0 0x03000100
		pulldown	0xFF5581E0	[8,9]	2	io -4 0xFF5581E0 0x03000200
	I/O Mode	input	0xFF550008	[4,4]	0	io -4 0xFF550008 0x00100000
	I/O IVIOUC	output	0xFF550008	[4,4]	1	io -4 0xFF550008 0x00100010
GPIO3A4	Output	low	0xFF550000	[4,4]	0	io -4 0xFF550000 0x00100000
	Surput	high	0xFF550000	[4,4]	1	io -4 0xFF550000 0x00100010
	Input Ctrl	disable	0xFF5581A0	[4,4]	0	io -4 0xFF5581A0 0x00100000
	P	enable	0xFF5581A0	[4,4]	1	io -4 0xFF5581A0 0x00100010
	Schmitt	disable	0xFF5582A0	[4,4]	0	io -4 0xFF5582A0 0x00100000
		enable	0xFF5582A0	[4,4]	1	io -4 0xFF5582A0 0x00100010
	-	level0	0xFF558108	[0,5]	1	io -4 0xFF558108 0x003F0001
	-	level1	0xFF558108	[0,5]	3	io -4 0xFF558108 0x003F0003
1	Drive Strength	level2	0xFF558108	[0,5]	7	io -4 0xFF558108 0x003F0007

Name	Config	Description	Register	Position	Value	IO Command
	Drive Suengui	level3	0xFF558108	[0,5]	15	io -4 0xFF558108 0x003F000F
	-	level4	0xFF558108	[0,5]	31	io -4 0xFF558108 0x003F001F
		level5	0xFF558108	[0,5]	63	io -4 0xFF558108 0x003F003F
	ExtPort	read gpio data in input mode	0xFF550070	[4,4]		io -4 0xFF550070
		SDMMC0_CMD	0xFF558044	[4,6]	1	io -4 0xFF558044 0x00700010
	_	UART5_CTSN_M0	0xFF558044	[4,6]	2	io -4 0xFF558044 0x00700020
	IOmux	I2C0_SDA_M2	0xFF558044	[4,6]	3	io -4 0xFF558044 0x00700030
	- "	LPMCU_JTAG_TMS_M1	0xFF558044	[4,6]	4	io -4 0xFF558044 0x00700040
	-	PWM11_IR_M0	0xFF558044	[4,6]	5	io -4 0xFF558044 0x00700050
-		GPIO3_A5	0xFF558044	[4,6]	0	io -4 0xFF558044 0x00700000
	Pull	normal pullup	0xFF5581E0 0xFF5581E0	[10,11]	0	io -4 0xFF5581E0 0x0C000000 io -4 0xFF5581E0 0x0C000400
	run	pulldown	0xFF5581E0	[10,11]	2	io -4 0xFF5581E0 0x0C000400
		input	0xFF550008	[5,5]	0	io -4 0xFF550008 0x00200000
	I/O Mode	output	0xFF550008	[5,5]	1	io -4 0xFF550008 0x00200000
		low	0xFF550000	[5,5]	0	io -4 0xFF550000 0x00200000
GPIO3A5	Output	high	0xFF550000	[5,5]	1	io -4 0xFF550000 0x00200020
	T C . 1	disable	0xFF5581A0	[5,5]	0	io -4 0xFF5581A0 0x00200000
	Input Ctrl	enable	0xFF5581A0	[5,5]	1	io -4 0xFF5581A0 0x00200020
	C -1 :++	disable	0xFF5582A0	[5,5]	0	io -4 0xFF5582A0 0x00200000
	Schmitt -	enable	0xFF5582A0	[5,5]	1	io -4 0xFF5582A0 0x00200020
		level0	0xFF558108	[8,13]	1	io -4 0xFF558108 0x3F000100
		level1	0xFF558108	[8,13]	3	io -4 0xFF558108 0x3F000300
	Drive Strength	level2	0xFF558108	[8,13]	7	io -4 0xFF558108 0x3F000700
		level3	0xFF558108	[8,13]	15	io -4 0xFF558108 0x3F000F00
		level4	0xFF558108	[8,13]	31	io -4 0xFF558108 0x3F001F00
		level5	0xFF558108	[8,13]	63	io -4 0xFF558108 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF550070	[5,5]		io -4 0xFF550070
	-	SDMMC0_D3	0xFF558044	[8,10]	1	io -4 0xFF558044 0x07000100
	IOmux	UART5_TX_M0	0xFF558044	[8,10]	2	io -4 0xFF558044 0x07000200
	IOIIIux	A7_JTAG_TMS_M0 HPMCU_JTAG_TMS_M1	0xFF558044 0xFF558044	[8,10] [8,10]	3 4	io -4 0xFF558044 0x07000300 io -4 0xFF558044 0x07000400
	_	GPIO3_A6	0xFF558044	[8,10]	0	io -4 0xFF558044 0x07000000
		normal	0xFF5581E0	[12,13]	0	io -4 0xFF5581E0 0x30000000
	Pull	pullup	0xFF5581E0		1	io -4 0xFF5581E0 0x30001000
		pulldown	0xFF5581E0	[12,13]	2	io -4 0xFF5581E0 0x30002000
	T/O M 1	input	0xFF550008	[6,6]	0	io -4 0xFF550008 0x00400000
	I/O Mode	output	0xFF550008	[6,6]	1	io -4 0xFF550008 0x00400040
	Output	low	0xFF550000	[6,6]	0	io -4 0xFF550000 0x00400000
GPIO3A6	Output -	high	0xFF550000	[6,6]	1	io -4 0xFF550000 0x00400040
	Input Ctrl	disable	0xFF5581A0	[6,6]	0	io -4 0xFF5581A0 0x00400000
	Input Cui	enable	0xFF5581A0	[6,6]	1	io -4 0xFF5581A0 0x00400040
	Schmitt	disable	0xFF5582A0	[6,6]	0	io -4 0xFF5582A0 0x00400000
		enable	0xFF5582A0	[6,6]	1	io -4 0xFF5582A0 0x00400040
	-	level0	0xFF55810C	[0,5]	1	io -4 0xFF55810C 0x003F0001
	-	level1	0xFF55810C	[0,5]	3	io -4 0xFF55810C 0x003F0003
	Drive Strength	level2	0xFF55810C	[0,5]	7 15	io -4 0xFF55810C 0x003F0007
	_	level3	0xFF55810C	[0,5]		io -4 0xFF55810C 0x003F000F
	-	level5	0xFF55810C 0xFF55810C	[0,5]	63	io -4 0xFF55810C 0x003F001F io -4 0xFF55810C 0x003F003F
	ExtPort	read gpio data in input mode	0xFF550070	[6,6]	03	io -4 0xFF550070
	LAH OIL	SDMMC0_D2	0xFF558044	[12,14]	1	io -4 0xFF558044 0x70001000
	ŀ	UART5_RX_M0	0xFF558044	[12,14]	2	io -4 0xFF558044 0x70002000
	IOmux	A7_JTAG_TCK_M0	0xFF558044	[12,14]	3	io -4 0xFF558044 0x70003000
	ļ	HPMCU_JTAG_TCK_M1	0xFF558044	[12,14]	4	io -4 0xFF558044 0x70004000
	ļ	GPIO3_A7	0xFF558044	[12,14]	0	io -4 0xFF558044 0x70000000
		normal	0xFF5581E0	[14,15]	0	io -4 0xFF5581E0 0xC0000000
	Pull	pullup	0xFF5581E0	[14,15]	1	io -4 0xFF5581E0 0xC0004000
		pulldown	0xFF5581E0	[14,15]	2	io -4 0xFF5581E0 0xC0008000
	I/O Mode	input	0xFF550008	[7,7]	0	io -4 0xFF550008 0x00800000
		output	0xFF550008	[7,7]	1	io -4 0xFF550008 0x00800080

	KV1105 01 10						
Name	Config	Description	Register	Position	Value	IO Command	
		low	0xFF550000	[7,7]	0	io -4 0xFF550000 0x00800000	
GPIO3A7	Output	high	0xFF550000	[7,7]	1	io -4 0xFF550000 0x00800080	
		disable	0xFF5581A0	[7,7]	0	io -4 0xFF5581A0 0x00800000	
	Input Ctrl	enable	0xFF5581A0	[7,7]	1	io -4 0xFF5581A0 0x00800080	
		disable	0xFF5582A0	[7,7]	0	io -4 0xFF5582A0 0x00800000	
	Schmitt	enable	0xFF5582A0	[7,7]	1	io -4 0xFF5582A0 0x00800080	
-		level0	0xFF55810C	[8,13]	1	io -4 0xFF55810C 0x3F000100	
	_	level1	0xFF55810C	[8,13]	3	io -4 0xFF55810C 0x3F000300	
	-	level2	0xFF55810C	[8,13]	7	io -4 0xFF55810C 0x3F000700	
	Drive Strength	level3	0xFF55810C	[8,13]	15	io -4 0xFF55810C 0x3F000F00	
	-	level4	0xFF55810C	[8,13]	31	io -4 0xFF55810C 0x3F001F00	
	-	level5	0xFF55810C	[8,13]	63	io -4 0xFF55810C 0x3F003F00	
-	E4D4				0.5		
	ExtPort	read gpio data in input mode	0xFF550070	[7,7]	1	io -4 0xFF550070	
	10	VICAP_D0_M0	0xFF558048	[0,2]	1	io -4 0xFF558048 0x00070001	
	IOmux	MIPI_LVDS_D3N	0xFF558048	[0,2]	2	io -4 0xFF558048 0x00070002	
-		GPIO3_B0	0xFF558048	[0,2]	0	io -4 0xFF558048 0x00070000	
	Pull						
	I/O Mode	input	0xFF550008	[8,8]	0	io -4 0xFF550008 0x01000000	
GPIO3B0		output	0xFF550008	[8,8]	1	io -4 0xFF550008 0x01000100	
0110020	Output	low	0xFF550000	[8,8]	0	io -4 0xFF550000 0x01000000	
	Output	high	0xFF550000	[8,8]	1	io -4 0xFF550000 0x01000100	
	Input Ctrl						
	Schmitt						
	Drive Strength						
	ExtPort	read gpio data in input mode	0xFF550070	[8,8]		io -4 0xFF550070	
	IOmux	VICAP_D1_M0	0xFF558048	[4,6]	1	io -4 0xFF558048 0x00700010	
		MIPI_LVDS_D3P	0xFF558048	[4,6]	2	io -4 0xFF558048 0x00700020	
		GPIO3_B1	0xFF558048	[4,6]	0	io -4 0xFF558048 0x00700000	
	Pull	<del>-</del>					
		input	0xFF550008	[9,9]	0	io -4 0xFF550008 0x02000000	
	I/O Mode	output	0xFF550008	[9,9]	1	io -4 0xFF550008 0x02000200	
GPIO3B1		low	0xFF550000	[9,9]	0	io -4 0xFF550000 0x02000000	
	Output	high	0xFF550000	[9,9]	1	io -4 0xFF550000 0x02000200	
-	Input Ctrl		0A1 1 22 0 0 0 0	[2,2]	1	10 1 0 11 13 3 0 0 0 0 0 10 2 0 0 2 0 0	
	Schmitt						
-	Drive Strength						
-	ExtPort ExtPort	read gpio data in input mode	0xFF550070	[9,9]		io -4 0xFF550070	
	Exti Oit	VICAP_D2_M0	0xFF558048		1	io -4 0xFF558048 0x07000100	
	IOmux						
	IOIIIux	MIPI_LVDS_CK1N	0xFF558048	[8,10]	2	io -4 0xFF558048 0x07000200	
-	D 11	GPIO3_B2	0xFF558048	[8,10]	0	io -4 0xFF558048 0x07000000	
-	Pull		0. EE550000	F10 101	0	: 4.0 FF550000 0 04000000	
	I/O Mode	input	0xFF550008	[10,10]	0	io -4 0xFF550008 0x04000000	
GPIO3B2		output	0xFF550008	[10,10]	1	io -4 0xFF550008 0x04000400	
	Output	low	0xFF550000	[10,10]	0	io -4 0xFF550000 0x04000000	
		high	0xFF550000	[10,10]	1	io -4 0xFF550000 0x04000400	
	Input Ctrl						
	Schmitt						
	Drive Strength						
	ExtPort	read gpio data in input mode	0xFF550070	[10,10]		io -4 0xFF550070	
		VICAP_D3_M0	0xFF558048	[12,14]	1	io -4 0xFF558048 0x70001000	
	IOmux	MIPI_LVDS_CK1P	0xFF558048	[12,14]	2	io -4 0xFF558048 0x70002000	
		GPIO3_B3	0xFF558048	[12,14]	0	io -4 0xFF558048 0x70000000	
	Pull						
Ī	I/O Mad-	input	0xFF550008	[11,11]	0	io -4 0xFF550008 0x08000000	
CDIO2D2	I/O Mode	output	0xFF550008	[11,11]	1	io -4 0xFF550008 0x08000800	
GPIO3B3		low	0xFF550000	[11,11]	0	io -4 0xFF550000 0x08000000	
	Output	high	0xFF550000	[11,11]	1	io -4 0xFF550000 0x08000800	
	Input Ctrl		1 1 2 2 3 3 3 3 3	L · - y - * J	-	22222333333333333	
	Schmitt						
	Drive Strength						
	ExtPort	read gpio data in input mode	0xFF550070	[11,11]		io -4 0xFF550070	
<u> </u>	LAU OIL	Teas Spio data in input mode	0AI I 330070	[11,11]	<u> </u>	10 T 0A11 330070	

Name	Config	Description	Register	Position	Value	IO Command
		VICAP_D4_M0	0xFF55804C	[0,2]	1	io -4 0xFF55804C 0x00070001
	IOmux	MIPI_LVDS_D2N	0xFF55804C	[0,2]	2	io -4 0xFF55804C 0x00070002
		GPIO3_B4	0xFF55804C	[0,2]	0	io -4 0xFF55804C 0x00070000
_	Pull					
	I/O Mode	input	0xFF550008	[12,12]	0	io -4 0xFF550008 0x10000000
GPIO3B4	I O Mode	output	0xFF550008	[12,12]	1	io -4 0xFF550008 0x10001000
GIIOSDI	Output	low	0xFF550000	[12,12]	0	io -4 0xFF550000 0x10000000
-		high	0xFF550000	[12,12]	1	io -4 0xFF550000 0x10001000
-	Input Ctrl					
-	Schmitt					
-	Drive Strength					
	ExtPort	read gpio data in input mode	0xFF550070	[12,12]		io -4 0xFF550070
		VICAP_D5_M0	0xFF55804C	[4,6]	1	io -4 0xFF55804C 0x00700010
	IOmux	MIPI_LVDS_D2P	0xFF55804C	[4,6]	2	io -4 0xFF55804C 0x00700020
		GPIO3_B5	0xFF55804C	[4,6]	0	io -4 0xFF55804C 0x00700000
	Pull					
	I/O Mode	input	0xFF550008	[13,13]	0	io -4 0xFF550008 0x20000000
GPIO3B5		output	0xFF550008	[13,13]	1	io -4 0xFF550008 0x20002000
	Output	low	0xFF550000	[13,13]	0	io -4 0xFF550000 0x20000000
		high	0xFF550000	[13,13]	1	io -4 0xFF550000 0x20002000
	Input Ctrl					
	Schmitt					
	Drive Strength					
	ExtPort	read gpio data in input mode	0xFF550070	[13,13]		io -4 0xFF550070
	-	VICAP_D6_M0	0xFF55804C	[8,10]	1	io -4 0xFF55804C 0x07000100
	IOmux	MIPI_LVDS_D1N	0xFF55804C	[8,10]	2	io -4 0xFF55804C 0x07000200
		GPIO3_B6	0xFF55804C	[8,10]	0	io -4 0xFF55804C 0x07000000
_	Pull					
	I/O Mode	input	0xFF550008	[14,14]	0	io -4 0xFF550008 0x40000000
GPIO3B6		output	0xFF550008	[14,14]	1	io -4 0xFF550008 0x40004000
	Output	low	0xFF550000	[14,14]	0	io -4 0xFF550000 0x40000000
		high	0xFF550000	[14,14]	1	io -4 0xFF550000 0x40004000
	Input Ctrl					
	Schmitt					
	Drive Strength					
	ExtPort	read gpio data in input mode	0xFF550070	[14,14]		io -4 0xFF550070
		VICAP_D7_M0	0xFF55804C	[12,14]	1	io -4 0xFF55804C 0x70001000
	IOmux	MIPI_LVDS_D1P	0xFF55804C	[12,14]	2	io -4 0xFF55804C 0x70002000
		GPIO3_B7	0xFF55804C	[12,14]	0	io -4 0xFF55804C 0x70000000
	Pull					
	I/O Mode	input	0xFF550008	[15,15]	0	io -4 0xFF550008 0x80000000
GPIO3B7		output	0xFF550008	[15,15]	1	io -4 0xFF550008 0x80008000
	Output	low	0xFF550000	[15,15]	0	io -4 0xFF550000 0x80000000
	7	high	0xFF550000	[15,15]	1	io -4 0xFF550000 0x80008000
	Input Ctrl					
	Schmitt					
	Drive Strength	1	0. EE550050	F15 153		. 40 55550050
	ExtPort	read gpio data in input mode	0xFF550070	[15,15]	4	io -4 0xFF550070
	10	VICAP_D8_M0	0xFF558050	[0,2]	1	io -4 0xFF558050 0x00070001
	IOmux	MIPI_LVDS_CK0N	0xFF558050	[0,2]	2	io -4 0xFF558050 0x00070002
	D11	GPIO3_C0	0xFF558050	[0,2]	0	io -4 0xFF558050 0x00070000
	Pull	immus	0.45550000	[0.0]	0	io 4 0vEE55000C 000010000
	I/O Mode	input	0xFF55000C	[0,0]	0	io -4 0xFF55000C 0x00010000 io -4 0xFF55000C 0x00010001
GPIO3C0		output	0xFF55000C			io -4 0xFF55000C 0x00010001
	Output	low	0xFF550004	[0,0]	0	
	Input Ct1	high	0xFF550004	[0,0]	1	io -4 0xFF550004 0x00010001
	Input Ctrl Schmitt					
	Drive Strength	road onic data in int 3	0xEE550070	[16 16]		io 40-EE550070
	ExtPort	read gpio data in input mode VICAP_D9_M0	0xFF550070 0xFF558050	[16,16] [4,6]	1	io -4 0xFF550070 io -4 0xFF558050 0x00700010
1 1	L	VICAL_D7_IVIU	071.1.220020	[4,0]	1	10 -4 OVI I 220020 OX00/00010

Name	Config	Description	Register	Position	Value	IO Command
	IOmux	MIPI_LVDS_CK0P	0xFF558050	[4,6]	2	io -4 0xFF558050 0x00700020
		GPIO3_C1	0xFF558050	[4,6]	0	io -4 0xFF558050 0x00700000
	Pull					
	I/O Mode	input	0xFF55000C	[1,1]	0	io -4 0xFF55000C 0x00020000
GPIO3C1		output	0xFF55000C	[1,1]	1	io -4 0xFF55000C 0x00020002
	Output	low	0xFF550004	[1,1]	0	io -4 0xFF550004 0x00020000
	In most Ctril	high	0xFF550004	[1,1]	1	io -4 0xFF550004 0x00020002
	Input Ctrl Schmitt					
	Drive Strength					
	ExtPort	read gpio data in input mode	0xFF550070	[17,17]		io -4 0xFF550070
	Extroit	VICAP_CLKIN_M0	0xFF558050	[8,10]	1	io -4 0xFF558050 0x07000100
	IOmux	MIPI_LVDS_D0N	0xFF558050	[8,10]	2	io -4 0xFF558050 0x07000200
		GPIO3_C2	0xFF558050	[8,10]	0	io -4 0xFF558050 0x07000000
	Pull	0.000_00		[*,-*]		
		input	0xFF55000C	[2,2]	0	io -4 0xFF55000C 0x00040000
CDIO2C2	I/O Mode	output	0xFF55000C	[2,2]	1	io -4 0xFF55000C 0x00040004
GPIO3C2	Overmost	low	0xFF550004	[2,2]	0	io -4 0xFF550004 0x00040000
	Output	high	0xFF550004	[2,2]	1	io -4 0xFF550004 0x00040004
	Input Ctrl					
	Schmitt					
	Drive Strength					
	ExtPort	read gpio data in input mode	0xFF550070	[18,18]		io -4 0xFF550070
		VICAP_HSYNC_M0	0xFF558050	[12,14]	1	io -4 0xFF558050 0x70001000
	IOmux	MIPI_LVDS_D0P	0xFF558050	[12,14]	2	io -4 0xFF558050 0x70002000
		GPIO3_C3	0xFF558050	[12,14]	0	io -4 0xFF558050 0x70000000
	Pull					
	I/O Mode	input	0xFF55000C	[3,3]	0	io -4 0xFF55000C 0x00080000
GPIO3C3		output	0xFF55000C	[3,3]	1	io -4 0xFF55000C 0x00080008
	Output	low	0xFF550004	[3,3]	0	io -4 0xFF550004 0x00080000
	Immust Ctul	high	0xFF550004	[3,3]	1	io -4 0xFF550004 0x00080008
	Input Ctrl Schmitt					
	Drive Strength					
	ExtPort	read gpio data in input mode	0xFF550070	[19,19]		io -4 0xFF550070
	Extroit	VICAP_CLKOUT_M0	0xFF558054	[0,2]	1	io -4 0xFF558054 0x00070001
	IOmux	MIPI_REFCLK_OUT0	0xFF558054	[0,2]	2	io -4 0xFF558054 0x00070002
		GPIO3_C4	0xFF558054	[0,2]	0	io -4 0xFF558054 0x00070000
		normal	0xFF5581E8	[8,9]	0	io -4 0xFF5581E8 0x03000000
	Pull	pullup	0xFF5581E8	[8,9]	1	io -4 0xFF5581E8 0x03000100
		pulldown	0xFF5581E8	[8,9]	2	io -4 0xFF5581E8 0x03000200
	I/O Mode	input	0xFF55000C	[4,4]	0	io -4 0xFF55000C 0x00100000
	I/O WIOde	output	0xFF55000C	[4,4]	1	io -4 0xFF55000C 0x00100010
	Output	low	0xFF550004	[4,4]	0	io -4 0xFF550004 0x00100000
	Gutput	high	0xFF550004	[4,4]	1	io -4 0xFF550004 0x00100010
GPIO3C4	Input Ctrl	disable	0xFF5581A8	[4,4]	0	io -4 0xFF5581A8 0x00100000
	r	enable	0xFF5581A8	[4,4]	1	io -4 0xFF5581A8 0x00100010
	Schmitt	disable	0xFF5582A8	[4,4]	0	io -4 0xFF5582A8 0x00100000
		enable	0xFF5582A8	[4,4]	1	io -4 0xFF5582A8 0x00100010
		level0	0xFF558128	[0,5]	1	io -4 0xFF558128 0x003F0001
	-	level1 level2	0xFF558128	[0,5]	7	io -4 0xFF558128 0x003F0003
	Drive Strength	level3	0xFF558128 0xFF558128	[0,5]	15	io -4 0xFF558128 0x003F0007 io -4 0xFF558128 0x003F000F
	-	level4	0xFF558128	[0,5]	31	io -4 0xFF558128 0x003F000F
	-	level5	0xFF558128	[0,5]	63	io -4 0xFF558128 0x003F003F
	ExtPort	read gpio data in input mode	0xFF550070	[20,20]	0.5	io -4 0xFF550070
		VICAP_VSYNC_M0	0xFF558054	[4,6]	1	io -4 0xFF558054 0x00700010
	IOmux	GPIO3_C5	0xFF558054	[4,6]	0	io -4 0xFF558054 0x00700000
		normal	0xFF5581E8	[10,11]	0	io -4 0xFF5581E8 0x0C000000
	Pull	pullup	0xFF5581E8	[10,11]	1	io -4 0xFF5581E8 0x0C000400
	ļ	pulldown	0xFF5581E8	[10,11]	2	io -4 0xFF5581E8 0x0C000800
		<u> </u>	1	- / -	<u> </u>	

Name	Config	Description	Register	Position	Value	IO Command
	I/O M - 1 -	input	0xFF55000C	[5,5]	0	io -4 0xFF55000C 0x00200000
	I/O Mode	output	0xFF55000C	[5,5]	1	io -4 0xFF55000C 0x00200020
	Output	low	0xFF550004	[5,5]	0	io -4 0xFF550004 0x00200000
	Output	high	0xFF550004	[5,5]	1	io -4 0xFF550004 0x00200020
GPIO3C5	Input Ctrl	disable	0xFF5581A8	[5,5]	0	io -4 0xFF5581A8 0x00200000
		enable	0xFF5581A8	[5,5]	1	io -4 0xFF5581A8 0x00200020
	Schmitt	disable	0xFF5582A8	[5,5]	0	io -4 0xFF5582A8 0x00200000
		enable	0xFF5582A8	[5,5]	1	io -4 0xFF5582A8 0x00200020
		level0 level1	0xFF558128 0xFF558128	[8,13] [8,13]	3	io -4 0xFF558128 0x3F000100 io -4 0xFF558128 0x3F000300
		level2	0xFF558128	[8,13]	7	io -4 0xFF558128 0x3F000300
	Drive Strength	level3	0xFF558128	[8,13]	15	io -4 0xFF558128 0x3F000F00
		level4	0xFF558128	[8,13]	31	io -4 0xFF558128 0x3F001F00
		level5	0xFF558128	[8,13]	63	io -4 0xFF558128 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF550070	[21,21]	0.5	io -4 0xFF550070
		VICAP_D10	0xFF558054	[8,10]	1	io -4 0xFF558054 0x07000100
	10	PWM7_IR_M2	0xFF558054	[8,10]	2	io -4 0xFF558054 0x07000200
	IOmux	MIPI_REFCLK_OUT1	0xFF558054	[8,10]	3	io -4 0xFF558054 0x07000300
		GPIO3_C6	0xFF558054	[8,10]	0	io -4 0xFF558054 0x07000000
		normal	0xFF5581E8	[12,13]	0	io -4 0xFF5581E8 0x30000000
	Pull	pullup	0xFF5581E8	[12,13]	1	io -4 0xFF5581E8 0x30001000
		pulldown	0xFF5581E8	[12,13]	2	io -4 0xFF5581E8 0x30002000
	I/O Mode	input	0xFF55000C	[6,6]	0	io -4 0xFF55000C 0x00400000
	I/O Mode	output	0xFF55000C	[6,6]	1	io -4 0xFF55000C 0x00400040
	Output	low	0xFF550004	[6,6]	0	io -4 0xFF550004 0x00400000
GPIO3C6	F	high	0xFF550004	[6,6]	1	io -4 0xFF550004 0x00400040
	Input Ctrl	disable	0xFF5581A8	[6,6]	0	io -4 0xFF5581A8 0x00400000
		enable	0xFF5581A8	[6,6]	1	io -4 0xFF5581A8 0x00400040
	Schmitt	disable	0xFF5582A8	[6,6]	0	io -4 0xFF5582A8 0x00400000
		enable	0xFF5582A8	[6,6]	1	io -4 0xFF5582A8 0x00400040
		level0 level1	0xFF55812C 0xFF55812C	[0,5] [0,5]	3	io -4 0xFF55812C 0x003F0001 io -4 0xFF55812C 0x003F0003
		level2	0xFF55812C	[0,5]	7	io -4 0xFF55812C 0x003F0007
	Drive Strength	level3	0xFF55812C	[0,5]	15	io -4 0xFF55812C 0x003F000F
		level4	0xFF55812C		31	io -4 0xFF55812C 0x003F001F
		level5	0xFF55812C	[0,5]	63	io -4 0xFF55812C 0x003F003F
	ExtPort	read gpio data in input mode	0xFF550070	[22,22]		io -4 0xFF550070
		VICAP_D11	0xFF558054	[12,14]	1	io -4 0xFF558054 0x70001000
	IOmux	UART5_TX_M2	0xFF558054	[12,14]	2	io -4 0xFF558054 0x70002000
		I2C4_SCL_M2	0xFF558054	[12,14]	3	io -4 0xFF558054 0x70003000
		GPIO3_C7	0xFF558054	[12,14]	0	io -4 0xFF558054 0x70000000
	Pull  I/O Mode  Output	normal	0xFF5581E8	[14,15]	0	io -4 0xFF5581E8 0xC0000000
		pullup	0xFF5581E8	[14,15]	1	io -4 0xFF5581E8 0xC0004000
		pulldown	0xFF5581E8	[14,15]	2	io -4 0xFF5581E8 0xC0008000
		input	0xFF55000C	[7,7]	0	io -4 0xFF55000C 0x00800000
		output	0xFF55000C	[7,7]	1	io -4 0xFF55000C 0x00800080
		low	0xFF550004	[7,7]	0	io -4 0xFF550004 0x00800000
GPIO3C7		high	0xFF550004	[7,7]	1	io -4 0xFF550004 0x00800080
	Input Ctrl	disable	0xFF5581A8	[7,7]	0	io -4 0xFF5581A8 0x00800000
ŀ		enable disable	0xFF5581A8 0xFF5582A8	[7,7] [7,7]	0	io -4 0xFF5581A8 0x00800080 io -4 0xFF5582A8 0x00800000
	Schmitt	enable	0xFF5582A8	[7,7]	1	io -4 0xFF5582A8 0x00800000
	Drive Strength	level0	0xFF55812C	[8,13]	1	io -4 0xFF55812C 0x3F000100
		level1	0xFF55812C	[8,13]	3	io -4 0xFF55812C 0x3F000300
		level2	0xFF55812C	[8,13]	7	io -4 0xFF55812C 0x3F000700
		level3	0xFF55812C	[8,13]	15	io -4 0xFF55812C 0x3F000F00
		level4	0xFF55812C	[8,13]	31	io -4 0xFF55812C 0x3F001F00
		level5	0xFF55812C	[8,13]	63	io -4 0xFF55812C 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF550070	[23,23]		io -4 0xFF550070
		VICAP_D12	0xFF558058	[0,2]	1	io -4 0xFF558058 0x00070001
	IOmuv	UART5_RX_M2	0xFF558058	[0,2]	2	io -4 0xFF558058 0x00070002

	KVIIUS GPIO						
Name	Config	Description	Register	Position	Value	IO Command	
	Юших	I2C4_SDA_M2	0xFF558058	[0,2]	3	io -4 0xFF558058 0x00070003	
		GPIO3_D0	0xFF558058	[0,2]	0	io -4 0xFF558058 0x00070000	
		normal	0xFF5581EC	[0,1]	0	io -4 0xFF5581EC 0x00030000	
	Pull	pullup	0xFF5581EC	[0,1]	1	io -4 0xFF5581EC 0x00030001	
		pulldown	0xFF5581EC	[0,1]	2	io -4 0xFF5581EC 0x00030002	
	I/O Mode	input	0xFF55000C	[8,8]	0	io -4 0xFF55000C 0x01000000	
	I O Mode	output	0xFF55000C	[8,8]	1	io -4 0xFF55000C 0x01000100	
	Output	low	0xFF550004	[8,8]	0	io -4 0xFF550004 0x01000000	
GPIO3D0	Guiput	high	0xFF550004	[8,8]	1	io -4 0xFF550004 0x01000100	
0110020	Input Ctrl	disable	0xFF5581AC	[0,0]	0	io -4 0xFF5581AC 0x00010000	
	input cur	enable	0xFF5581AC	[0,0]	1	io -4 0xFF5581AC 0x00010001	
	Schmitt	disable	0xFF5582AC	[0,0]	0	io -4 0xFF5582AC 0x00010000	
	Semme	enable	0xFF5582AC	[0,0]	1	io -4 0xFF5582AC 0x00010001	
		level0	0xFF558130	[0,5]	1	io -4 0xFF558130 0x003F0001	
		level1	0xFF558130	[0,5]	3	io -4 0xFF558130 0x003F0003	
	Drive Strength	level2	0xFF558130	[0,5]	7	io -4 0xFF558130 0x003F0007	
	Dire Suchgui	level3	0xFF558130	[0,5]	15	io -4 0xFF558130 0x003F000F	
		level4	0xFF558130	[0,5]	31	io -4 0xFF558130 0x003F001F	
		level5	0xFF558130	[0,5]	63	io -4 0xFF558130 0x003F003F	
	ExtPort	read gpio data in input mode	0xFF550070	[24,24]		io -4 0xFF550070	
		EMMC_D2	0xFF568000	[8,10]	1	io -4 0xFF568000 0x07000100	
	IOmux	FSPI_D2	0xFF568000	[8,10]	2	io -4 0xFF568000 0x07000100	
	IOIIIux	GPIO4_A2	0xFF568000	[8,10]	0	io -4 0xFF568000 0x07000200	
		normal	0xFF568070	[4,5]	0	io -4 0xFF568070 0x00300000	
	Pull				1		
	ruii	pullup	0xFF568070	[4,5]	2	io -4 0xFF568070 0x00300010	
		pulldown	0xFF568070 0xFF560008	[4,5]	0	io -4 0xFF568070 0x00300020 io -4 0xFF560008 0x00040000	
	I/O Mode	input	0xFF560008	[2,2]	1	io -4 0xFF560008 0x00040000	
ŀ		output low	0xFF560000		0	io -4 0xFF560000 0x00040004	
	Output	high	0xFF560000	[2,2]	1	io -4 0xFF560000 0x00040000 io -4 0xFF560000 0x00040004	
GPIO4A2	Input Ctrl	disable	0xFF568060	[2,2]	0	io -4 0xFF568060 0x00040004	
OI 104A2		enable	0xFF568060		1	io -4 0xFF568060 0x00040000	
	Schmitt -	disable	0xFF5680A0	[2,2]	0	io -4 0xFF5680A0 0x00040004	
		enable	0xFF5680A0	[2,2]	1	io -4 0xFF5680A0 0x00040004	
		level0	0xFF568024	[0,5]	1	io -4 0xFF568024 0x003F0001	
	Drive Strength –	level1	0xFF568024	[0,5]	3	io -4 0xFF568024 0x003F0003	
		level2	0xFF568024	[0,5]	7	io -4 0xFF568024 0x003F0007	
		level3	0xFF568024	[0,5]	15	io -4 0xFF568024 0x003F000F	
		level4	0xFF568024	[0,5]	31	io -4 0xFF568024 0x003F001F	
		level5	0xFF568024	[0,5]	63	io -4 0xFF568024 0x003F003F	
ŀ		read gpio data in input mode	0xFF560070	[2,2]	0.5	io -4 0xFF560070	
	Extroit	EMMC_D1	0xFF568000	[12,14]	1	io -4 0xFF568000 0x70001000	
	IOmux	FSPI_D1	0xFF568000	[12,14]	2	io -4 0xFF568000 0x70001000	
		GPIO4_A3	0xFF568000	[12,14]	0	io -4 0xFF568000 0x70000000	
	Pull I/O Mode	normal	0xFF568070	[6,7]	0	io -4 0xFF568070 0x00C00000	
		pullup	0xFF568070	[6,7]	1	io -4 0xFF568070 0x00C00040	
		pulldown	0xFF568070	[6,7]	2	io -4 0xFF568070 0x00C00040	
		input	0xFF560008	[3,3]	0	io -4 0xFF560008 0x00080000	
		output	0xFF560008	[3,3]	1	io -4 0xFF560008 0x00080008	
	Output	low	0xFF560000	[3,3]	0	io -4 0xFF560000 0x00080000	
		high	0xFF560000	[3,3]	1	io -4 0xFF560000 0x00080000	
GPIO4A3		disable	0xFF568060	[3,3]	0	io -4 0xFF568060 0x00080000	
21 10 17 13	Input Ctrl	enable	0xFF568060	[3,3]	1	io -4 0xFF568060 0x00080000	
		disable	0xFF5680A0	[3,3]	0	io -4 0xFF5680A0 0x00080000	
	Schmitt	enable	0xFF5680A0	[3,3]	1	io -4 0xFF5680A0 0x00080000	
		level0	0xFF568024	[8,13]	1	io -4 0xFF568024 0x3F000100	
		IEVEIU	UALT-300024	[0,13]	1	10 -4 UALT 3U0U24 UX3FUUUTUU	

Name	Config	Description	Register	Position	Value	IO Command
		level1	0xFF568024	[8,13]	3	io -4 0xFF568024 0x3F000300
	Drive Strength	level2	0xFF568024	[8,13]	7	io -4 0xFF568024 0x3F000700
	Dire Suchgui	level3	0xFF568024	[8,13]	15	io -4 0xFF568024 0x3F000F00
		level4	0xFF568024	[8,13]	31	io -4 0xFF568024 0x3F001F00
		level5	0xFF568024	[8,13]	63	io -4 0xFF568024 0x3F003F00
	ExtPort	read gpio data in input mode	0xFF560070	[3,3]		io -4 0xFF560070
	10	EMMC_D0	0xFF568004	[0,2]	1	io -4 0xFF568004 0x00070001
	IOmux	FSPI_D0	0xFF568004	[0,2]	2	io -4 0xFF568004 0x00070002
		GPIO4_A4	0xFF568004	[0,2]	0	io -4 0xFF568004 0x00070000
	Pull	normal pullup	0xFF568070	[8,9]	1	io -4 0xFF568070 0x03000000
	r un	pulldown	0xFF568070 0xFF568070	[8,9] [8,9]	2	io -4 0xFF568070 0x03000100 io -4 0xFF568070 0x03000200
		input	0xFF560008	[4,4]	0	io -4 0xFF560008 0x00100000
	I/O Mode	output	0xFF560008	[4,4]	1	io -4 0xFF560008 0x00100000
		low	0xFF560000	[4,4]	0	io -4 0xFF560000 0x00100010
	Output	high	0xFF560000	[4,4]	1	io -4 0xFF560000 0x00100010
GPIO4A4		disable	0xFF568060	[4,4]	0	io -4 0xFF568060 0x00100000
	Input Ctrl	enable	0xFF568060	[4,4]	1	io -4 0xFF568060 0x00100010
	~	disable	0xFF5680A0	[4,4]	0	io -4 0xFF5680A0 0x00100000
	Schmitt	enable	0xFF5680A0	[4,4]	1	io -4 0xFF5680A0 0x00100010
		level0	0xFF568028	[0,5]	1	io -4 0xFF568028 0x003F0001
		level1	0xFF568028	[0,5]	3	io -4 0xFF568028 0x003F0003
	D: 0: 1	level2	0xFF568028	[0,5]	7	io -4 0xFF568028 0x003F0007
	Drive Strength	level3	0xFF568028	[0,5]	15	io -4 0xFF568028 0x003F000F
		level4	0xFF568028	[0,5]	31	io -4 0xFF568028 0x003F001F
		level5	0xFF568028	[0,5]	63	io -4 0xFF568028 0x003F003F
	ExtPort	read gpio data in input mode	0xFF560070	[4,4]		io -4 0xFF560070
	IOmux	EMMC_D3	0xFF568004	[8,10]	1	io -4 0xFF568004 0x07000100
		FSPI_D3	0xFF568004	[8,10]	2	io -4 0xFF568004 0x07000200
		GPIO4_A6	0xFF568004	[8,10]	0	io -4 0xFF568004 0x07000000
		normal	0xFF568070	[12,13]	0	io -4 0xFF568070 0x30000000
	Pull	pullup	0xFF568070	[12,13]	1	io -4 0xFF568070 0x30001000
		pulldown	0xFF568070	[12,13]	2	io -4 0xFF568070 0x30002000
	I/O Mode	input	0xFF560008		0	io -4 0xFF560008 0x00400000
		output	0xFF560008	[6,6]	1	io -4 0xFF560008 0x00400040
	Output	low	0xFF560000	[6,6]	0	io -4 0xFF560000 0x00400000
CDIO446	-	high	0xFF560000	[6,6]	1	io -4 0xFF560000 0x00400040
GPIO4A6	Input Ctrl	disable	0xFF568060	[6,6]	0	io -4 0xFF568060 0x00400000
		enable disable	0xFF568060	[6,6]	0	io -4 0xFF568060 0x00400040
	Schmitt	enable	0xFF5680A0	[6,6]	1	io -4 0xFF5680A0 0x00400000 io -4 0xFF5680A0 0x00400040
		level0	0xFF5680A0 0xFF56802C	[6,6] [0,5]	1	io -4 0xFF56802C 0x003F0001
	-	level1	0xFF56802C	[0,5]	3	io -4 0xFF56802C 0x003F0003
		level2	0xFF56802C	[0,5]	7	io -4 0xFF56802C 0x003F0007
	Drive Strength	level3	0xFF56802C	[0,5]	15	io -4 0xFF56802C 0x003F0007
	<u> </u>	level4	0xFF56802C	[0,5]	31	io -4 0xFF56802C 0x003F001F
	<u> </u>	level5	0xFF56802C	[0,5]	63	io -4 0xFF56802C 0x003F003F
	ExtPort	read gpio data in input mode	0xFF560070	[6,6]	03	io -4 0xFF560070
		or or and in input mode	1230070	ر د, د ا		
	IOmux	EMMC_CMD	0xFF568008	[0,2]	1	io -4 0xFF568008 0x00070001
		FSPI_CS0N	0xFF568008	[0,2]	2	io -4 0xFF568008 0x00070002
		GPIO4_B0	0xFF568008	[0,2]	0	io -4 0xFF568008 0x00070000
	Pull  I/O Mode	normal	0xFF568074	[0,1]	0	io -4 0xFF568074 0x00030000
		pullup	0xFF568074	[0,1]	1	io -4 0xFF568074 0x00030001
		pulldown	0xFF568074	[0,1]	2	io -4 0xFF568074 0x00030002
[		input	0xFF560008	[8,8]	0	io -4 0xFF560008 0x01000000
		output	0xFF560008	[8,8]	1	io -4 0xFF560008 0x01000100
[		low	0xFF560000	[8,8]	0	io -4 0xFF560000 0x01000000
	Output	high	0xFF560000	[8,8]	1	io -4 0xFF560000 0x01000100
GPIO4B0	Input Ctrl	disable	0xFF568064	[0,0]	0	io -4 0xFF568064 0x00010000

Input Ctrl	Name	Config	Description	Register	Position	Value	IO Command
Schmitt		три Сит	enable	0xFF568064	[0,0]	1	io -4 0xFF568064 0x00010001
enable		C -1	disable		[0,0]	0	io -4 0xFF5680A4 0x00010000
Drive Strength		Schmitt	enable	0xFF5680A4	[0,0]	1	io -4 0xFF5680A4 0x00010001
Drive Strength			level0	0xFF568030	[0,5]	1	io -4 0xFF568030 0x003F0001
Drive Strength			level1	0xFF568030	[0,5]	3	io -4 0xFF568030 0x003F0003
Part		Duivo Strongth	level2	0xFF568030	[0,5]	7	io -4 0xFF568030 0x003F0007
ExtPort		Drive Strength	level3	0xFF568030	[0,5]	15	io -4 0xFF568030 0x003F000F
ExtPort			level4	0xFF568030	[0,5]	31	io -4 0xFF568030 0x003F001F
Politic   Poli			level5	0xFF568030	[0,5]	63	io -4 0xFF568030 0x003F003F
Pull		ExtPort	read gpio data in input mode	0xFF560070	[8,8]		io -4 0xFF560070
Pull			EMMC_CLK	0xFF568008	[4,6]	1	io -4 0xFF568008 0x00700010
Pull		IOmux	FSPI_CLK	0xFF568008	[4,6]	2	io -4 0xFF568008 0x00700020
Pull			GPIO4_B1	0xFF568008	[4,6]	0	io -4 0xFF568008 0x00700000
Publidown   OxFF568074   12.31   2   io. 4 OxFF568070 RX000C0008   Input   OxFF560008   19.91   1   io. 4 OxFF560008 Ox2000000   OxFF560008   OxFF560000   OxFF			normal	0xFF568074	[2,3]	0	io -4 0xFF568074 0x000C0000
FO Mode		Pull	pullup	0xFF568074	[2,3]	1	io -4 0xFF568074 0x000C0004
O Mode			pulldown	0xFF568074	[2,3]	2	io -4 0xFF568074 0x000C0008
Output		I/O Modo	input	0xFF560008	[9,9]	0	io -4 0xFF560008 0x02000000
Diput Crr		I/O Mode	output	0xFF560008	[9,9]	1	io -4 0xFF560008 0x02000200
Pight		Ovetmost	low	0xFF560000	[9,9]	0	io -4 0xFF560000 0x02000000
Input Ctrl		Output	high	0xFF560000	[9,9]	1	io -4 0xFF560000 0x02000200
Schmitt	GPIO4B1	Immust Ctul	disable	0xFF568064	[1,1]	0	io -4 0xFF568064 0x00020000
Schmitt		input Ctri	enable	0xFF568064	[1,1]	1	io -4 0xFF568064 0x00020002
Pul		C -1	disable	0xFF5680A4	[1,1]	0	io -4 0xFF5680A4 0x00020000
Drive Strength   level1		Schmitt	enable	0xFF5680A4	[1,1]	1	io -4 0xFF5680A4 0x00020002
Drive Strength			level0	0xFF568030		1	io -4 0xFF568030 0x3F000100
Drive Strength   level3			level1	0xFF568030	[8,13]	3	io -4 0xFF568030 0x3F000300
Pevelar		D: 0: 4	level2	0xFF568030	[8,13]	7	io -4 0xFF568030 0x3F000700
ExtPort   read gpio data in input mode   0xFF5680070   (9,9)		Drive Strength	level3	0xFF568030		15	io -4 0xFF568030 0x3F000F00
ExtPort   read gpio data in input mode   0xFF560070   [9,9]   io -4 0xFF560070   io -4 0xFF568010   0x00070001   io -4 0xFF568010 0x00070001   io -4 0xFF568010 0x00070001   io -4 0xFF568010 0x000700001   io -4 0xFF568010 0x000700001   io -4 0xFF568000 0x60000000   io -4 0xFF560000 0x60000000   io -4 0xFF568000 0x60000000   io -4 0xFF568000 0x600000000   io -4 0xFF568000 0x60000000   io -4 0x			level4	0xFF568030	[8,13]	31	io -4 0xFF568030 0x3F001F00
HOmux			level5	0xFF568030	[8,13]	63	io -4 0xFF568030 0x3F003F00
Pull		ExtPort	read gpio data in input mode	0xFF560070	[9,9]		io -4 0xFF560070
Pull		IO	ADC_IN0	0xFF568010	[0,2]	1	io -4 0xFF568010 0x00070001
Pull		IOmux	GPIO4_C0	0xFF568010		0	io -4 0xFF568010 0x00070000
Pulldown   OxFF5680C0   [13,14]   1   io -4 0xFF5680C0 0x60002000     Input			normal	0xFF5680C0	[13,14]	0	io -4 0xFF5680C0 0x60000000
Boundary			pullup	0xFF5680C0	[13,14]	3	io -4 0xFF5680C0 0x60006000
AD Mode			pulldown	0xFF5680C0	[13,14]	1	io -4 0xFF5680C0 0x60002000
Output			input	0xFF56000C	[0,0]	0	io -4 0xFF56000C 0x00010000
Display			output	0xFF56000C	[0,0]	1	io -4 0xFF56000C 0x00010001
Input Ctrl		Output	low	0xFF560004	[0,0]	0	io -4 0xFF560004 0x00010000
Input Ctrl	GPIO4C0	Output	high	0xFF560004	[0,0]	1	io -4 0xFF560004 0x00010001
Schmitt   Schmitt   Schmitt   Schmitt   Schmitt   Schmitt   Ievel0   OxFF5680C0   [10,11]   O   io -4 OxFF5680C0 0x000000000000000000000000000000000		Immust Ctul	disable	0xFF5680C0	[2,2]	0	io -4 0xFF5680C0 0x00040000
Drive Strength		input Ctri	enable	0xFF5680C0	[2,2]	1	io -4 0xFF5680C0 0x00040004
Drive Strength   level1   0xFF5680C0   [10,11]   1   io -4 0xFF5680C0 0x0C000400   level2   0xFF5680C0   [10,11]   2   io -4 0xFF5680C0 0x0C000800   level3   0xFF5680C0   [10,11]   3   io -4 0xFF5680C0 0x0C000C00   level3   0xFF5680C0   [10,11]   3   io -4 0xFF5680C0 0x0C000C00   level3   io -4 0xFF5680C0   level3   io -4 0xFF568010 0x007000C00   level3   io -4 0xFF568010 0x00700010   level3   io -4 0xFF568010 0x00700010   level3		Schmitt					
Drive Strength   level2			level0	0xFF5680C0	[10,11]	0	io -4 0xFF5680C0 0x0C000000
Revel 2		Duine Character	level1	0xFF5680C0	[10,11]	1	io -4 0xFF5680C0 0x0C000400
ExtPort   read gpio data in input mode   0xFF560070   [16,16]   io -4 0xFF560070     IOmux   PWM1_M1   0xFF568010   [4,6]   1   io -4 0xFF568010 0x00700010     PWM1_M1   0xFF568010   [4,6]   2   io -4 0xFF568010 0x00700020     GPIO4_C1   0xFF568010   [4,6]   0   io -4 0xFF568010 0x00700000     Pull   pullup   0xFF5680C0   [13,14]   0   io -4 0xFF5680C0 0x60000000     Pull   pullup   0xFF5680C0   [13,14]   3   io -4 0xFF5680C0 0x60000000     Pull   pullup   0xFF5680C0   [13,14]   1   io -4 0xFF5680C0 0x60002000     I/O Mode   input   0xFF56000C   [1,1]   0   io -4 0xFF56000C 0x00020000     I/O Mode   output   0xFF56000C   [1,1]   1   io -4 0xFF56000C 0x00020000     Iow   0xFF560004   [1,1]   1   io -4 0xFF560004 0x00020000     Input Ctrl   disable   0xFF5680C0   [3,3]   0   io -4 0xFF5680C0 0x00080000     Input Ctrl   enable   0xFF5680C0   [3,3]   1   io -4 0xFF5680C0 0x000800008     Iow   0xFF5680C0   [3,3]   1   io -4 0xFF5680C0 0x00080000     Iow   0xFF5680C0   [3,3]   1   io -4 0xFF5680C0 0x00080000     Iow   0xFF5680C0   [3,3]   1   io -4 0xFF5680C0 0x000800008		Drive Strength	level2	0xFF5680C0	[10,11]	2	io -4 0xFF5680C0 0x0C000800
IOmux			level3	0xFF5680C0	[10,11]	3	io -4 0xFF5680C0 0x0C000C00
IOmux		ExtPort	read gpio data in input mode	0xFF560070	[16,16]		io -4 0xFF560070
IOmux		IOmux		0xFF568010		1	io -4 0xFF568010 0x00700010
Pull				0xFF568010		2	
Pull   pullup   0xFF5680C0   [13,14]   0   io -4 0xFF5680C0 0x60000000			GPIO4_C1	0xFF568010		0	io -4 0xFF568010 0x00700000
Pull pullup 0xFF5680C0 [13,14] 3 io -4 0xFF5680C0 0x60006000 pulldown 0xFF5680C0 [13,14] 1 io -4 0xFF5680C0 0x60002000 [13,14] 1 io -4 0xFF5680C0 0x60002000 [17,1] 1 io -4 0xFF56000C 0x00020000 [17,1] 1 io -4 0xFF560004 0x00020000 [17,1] 1 io -4 0xFF560000 0x0000000000 [17,1] 1 io -4 0xFF560000 0x0000000000000000000000000000000		Pull		0xFF5680C0		0	io -4 0xFF5680C0 0x60000000
Dulldown   DxFF5680C0   [13,14]   1   io -4 0xFF5680C0 0x60002000			pullup	0xFF5680C0		3	
I/O Mode						1	
GPIO4C1 Output Output OxFF56000C [1,1] 1 io -4 0xFF56000C 0x00020002 low OxFF560004 [1,1] 0 io -4 0xFF560004 0x00020000 high OxFF560004 [1,1] 1 io -4 0xFF560004 0x00020000 low OxFF560004 [1,1] 1 io -4 0xFF560004 [1,1] Io -4 0x			•			0	
Output         low         0xFF560004         [1,1]         0         io -4 0xFF560004 0x00020000           high         0xFF560004         [1,1]         1         io -4 0xFF560004 0x00020002           Input Ctrl         disable         0xFF5680C0         [3,3]         0         io -4 0xFF5680C0 0x00080000           enable         0xFF5680C0         [3,3]         1         io -4 0xFF5680C0 0x00080008			•			1	
Number   N	CDIOACI		•			0	
Input Ctrl disable 0xFF5680C0 [3,3] 0 io -4 0xFF5680C0 0x00080000 enable 0xFF5680C0 [3,3] 1 io -4 0xFF5680C0 0x00080008	GPIO4C1	1					
enable 0xFF5680C0 [3,3] 1 io -4 0xFF5680C0 0x00080008							
		Input Ctrl					
		Schmitt	- 7777	322000	L- 7= J	-	

Name	Config	Description	Register	Position	Value	IO Command
	Drive Strength	level0	0xFF5680C0	[10,11]	0	io -4 0xFF5680C0 0x0C000000
		level1	0xFF5680C0	[10,11]	1	io -4 0xFF5680C0 0x0C000400
		level2	0xFF5680C0	[10,11]	2	io -4 0xFF5680C0 0x0C000800
		level3	0xFF5680C0	[10,11]	3	io -4 0xFF5680C0 0x0C000C00
	ExtPort	read gpio data in input mode	0xFF560070	[17,17]		io -4 0xFF560070