Mastering Embedded Systems

Online Diploma



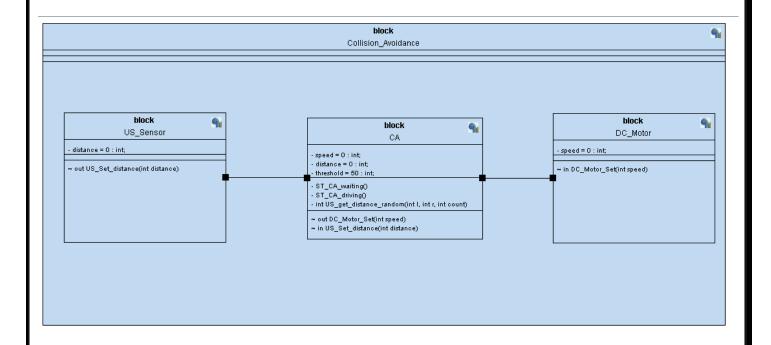
Unit 4 Lesson 2 Report

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Learn In Depth

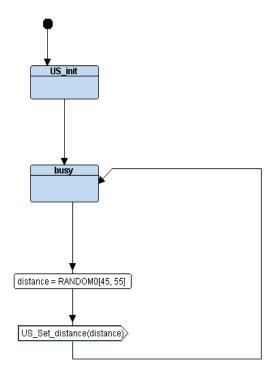
Be Professional In Embedded System Eng. Keroles Shenouda

Modules Level:



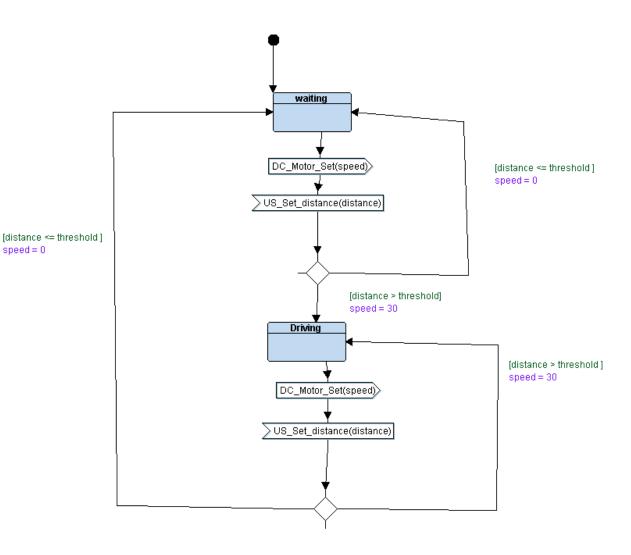
Logical Design

I. Ultrasonic Sensor

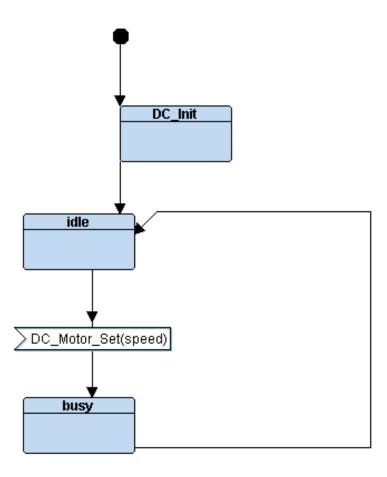


II. Collision Avoidance

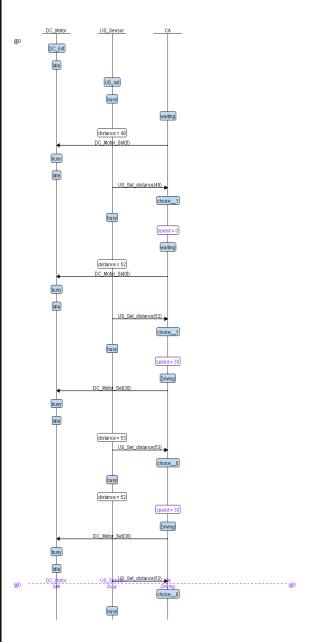
speed = 0



III. DC Motor



SW Logical verification



```
DC_init
US_waiting state: distance = 42
CA ----->DC
DC_busy state: speed = 0
US_waiting state: distance = 43
CA_waiting state: distance = 43 , speed = 0
CA -----speed=0----->DC
DC_busy state: speed = 0
US_waiting state: distance = 10
US -----> CA
CA_waiting state: distance = 10 , speed = 0
CA ----->DC
DC_busy state: speed = 0
US_waiting state: distance = 46
US -----> CA
CA_waiting state: distance = 46 , speed = 0
CA ----->DC
DC_busy state: speed = 0
US_waiting state: distance = 30
US -----> CA
CA_waiting state: distance = 30 , speed = 0
CA -----speed=0---->DC
DC_busy state: speed = 0
US_waiting state: distance = 50
US_waiting state: distance = 39
```

```
US -----> CA
CA_driving state: distance = 53 , speed = 0
CA ----->DC
DC_busy state: speed = 30
US_waiting state: distance = 7
US -----> CA
CA_waiting state: distance = 7, speed = 30
CA -----speed=0---->DC
DC busy state: speed = 0
US_waiting state: distance = 52
US -----> CA
CA_driving state: distance = 52 , speed = 0
CA -----speed=30---->DC
DC_busy state: speed = 30
US_waiting state: distance = 26
CA_waiting state: distance = 26 , speed = 30
CA -----speed=0----->DC
```