* Clock

- Concept →
  - Provide Pulse → to operation Processor
  - System Clock → Clock Provid Pulse to Enable Processor
  - detect Instruction Cycle speed time

ex → System Clk = 8 MHz → $\frac{1}{p}$ = 8000 000 Clk/sec
  - Risc → one Instruction in one Clk
    - speed → 8000 000 Instruction

- Provide Freq → Pulse
  - Calc Clock

→ Rcc

[Clock Sources]

- ① Internal oscillator
- ② External Crystal → PI
  volt → Pulse
- ③ RC oscillator
- ④ PLL Phase locked loop

Clock type

↳ ① System Clock ⟶ Processor

↳ ② Peripheral clock ⟶ timer / SPI / I2C
ADC

↳ ③ Watch dog Clock