

Memory

Memory Classification

↳ ① Volatile Memory

↳ Power loss / data loss

↳ ex. Ram

↳ ② Non Volatile Memory

↳ Power loss / data still stored.

↳ ex. Rom, Hard disk

Memory type

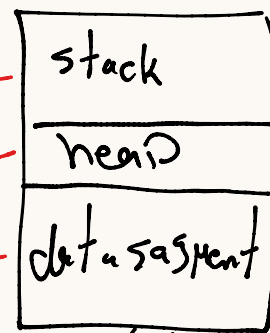
↳ ① Ram: Random Access Memory

↳ sections

static allocate

dynamic allocate

static allocate



local var

local var

global var

static local var

↳ keyword

data

Initialized

bss

unInitialized

↳ Manufacture

↳ Ram Base on Cap

↳ Cap charge = 1

↳ Cap Discharge = 0

↳ Called **DDRAM**

ex → use DDRAM in
PC / laptop / cell phone ⇐

↳ Adv → low cost
→ large size
→ 16G / 32G

↳ disAdv → need the
periodic charger

↳ Manufacture

↳ Ram Based on Transistor

Ex: E.S devices

Apple Macbook ⇐

↳ Bit →

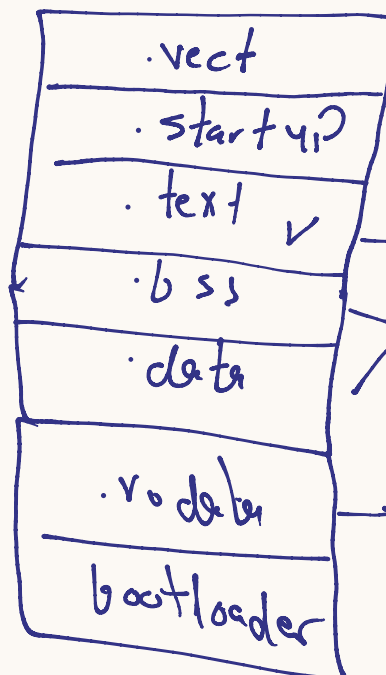
↳ Call SRAM
Static Ram

↳ Adv → save data

↳ dis → High cost
↳ low size

↳ ② ROM Read only Memory

↳ section →



→ Code Instruction

data global

→ Const var

↳ Rom manufacture

↳ Based on transistor (MOSFET)

↳ type

① Masked ROM →

↳ buy memory & code

② OTP → One Time Programmable

③ EPROM → Erasable Programmable
ROM [uv]

④ EEPROM → Electrical Erasable
PROM

⑤ Flash Memory → Write / Erase

* Different between Flash / EEPROM

Flash

↳ Code only

↳ Can't Access During
Runtime

↳ write !. Page size / word
No. of byte

↳ Erase !. Sector size
No. of Page

EEPROM

↳ For Code / For data

↳ Can Access During
Runtime

↳ write → byte size

↳ Erase → byte size

↳ Current Post → detect floor

X

Ram

Screen →

0
[2]

النور قطع
[3]

[0]

Current Post

↳ Rom | Flash →

X

[0]

[2]
[3]

EEPROM

ex for use EEPROM
For Data

↳ Access Runtime

↳ Non Volatile Memory.

IO / Memory

↳ it's Part From Pheri Pherl

H.W
Circuit

Memory.