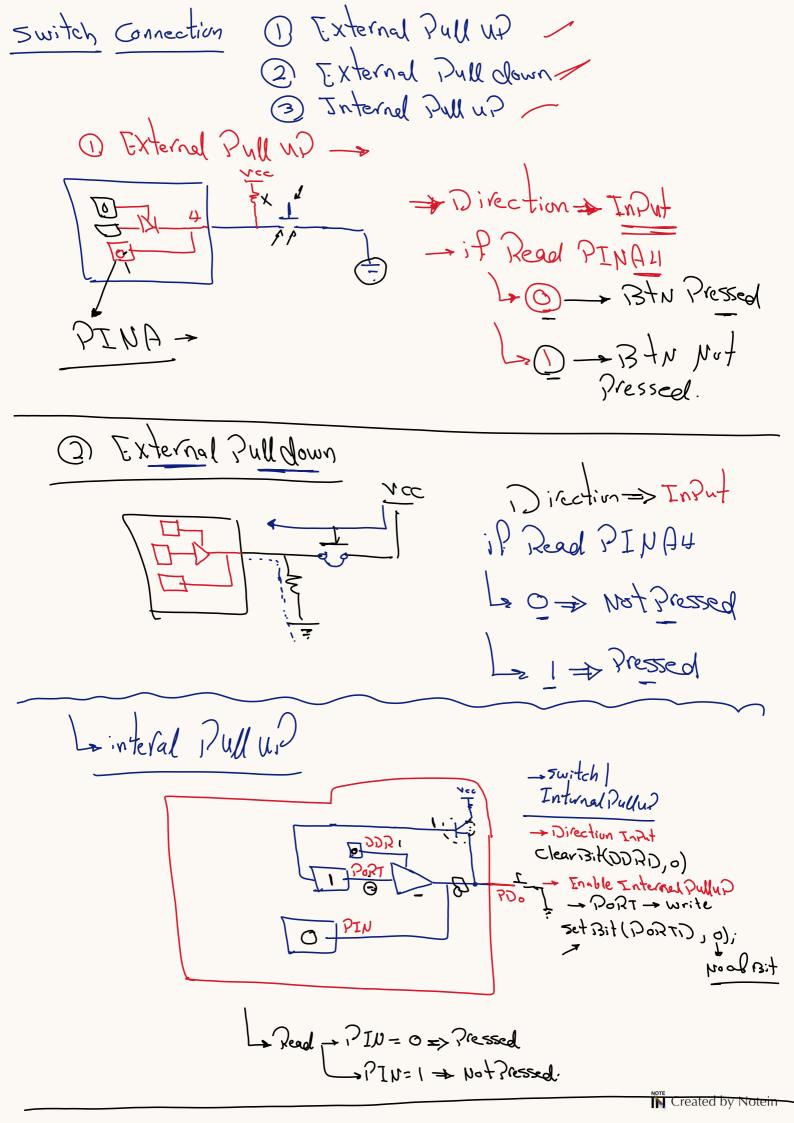
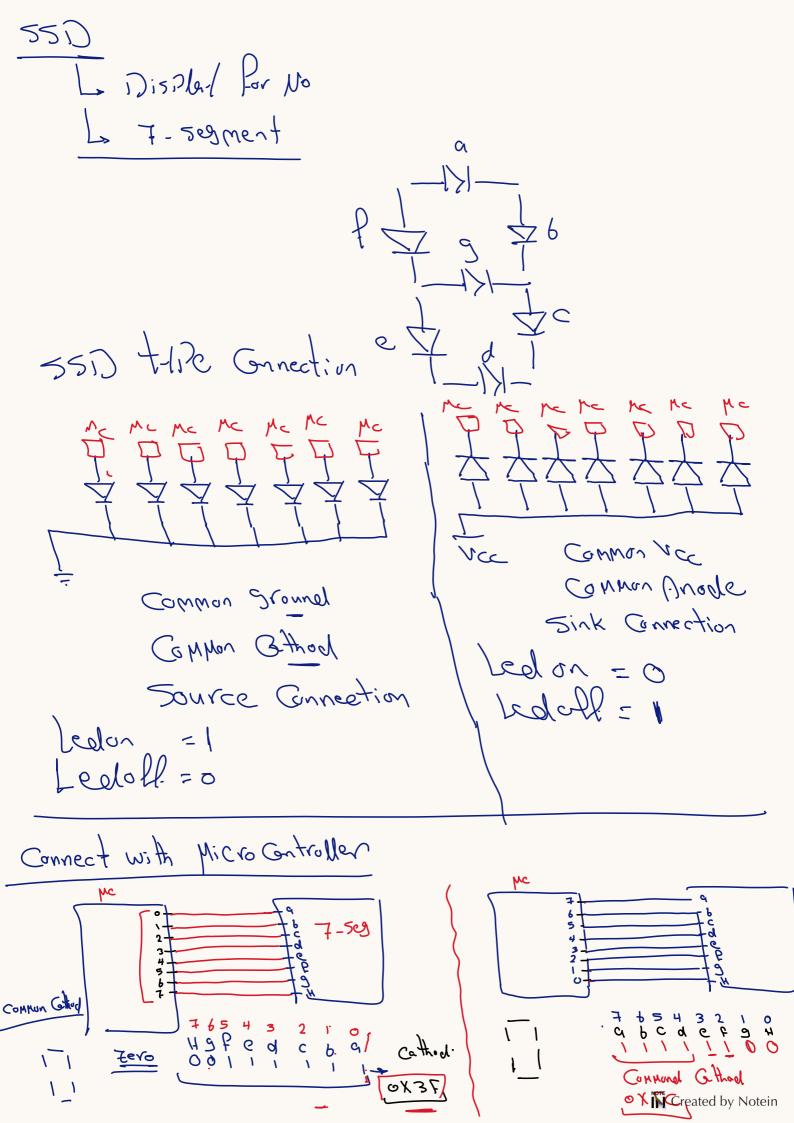


Source Connection Sink La O leolan sporto La B led on - Port 1 Lo led off -> Porto Le (2) leglo fl = Port Sink - 54 MC 54 20: 40 MA Source Cosic Switch -> Input devices 2 cleetricel (cleetrictiz)
Ruly | transisterant - 1) Mechincel Rush Button DIP Switch



waiting the in Mcu Lo Busy wait - inside loop enity Par(1=0; 1 < 40; 1++) S ("NO OJ"); S <util/delat.h>
Head Insut?!

-delat_Ms(2000); -> -debil-1/5 (40 2 tos -> Real time or Perating 5-leter



- table For AU Dumber From Zero to pipe 76543210 abcdef9H Control Cathool Control Cathool >01= | GX4F R 136ff-0 1>6/1-0 76543210 7 65 4 3 2 1 H 9 f e d c b abc de P9H Command Arad Command Wood 1200 =0 1200 =0 1= 1 sold = 1 1= 1 sold Break until 1:30 Holeline Cathodi_ Fero Cathodi - anc Cathadl _ Two # define Gthod2_ Zero H define Cathod1-three 0X4P