

# SYNCHRONOUS SESSION PLAN

## UNIT-I

Session Plan				Actual Delivery			
Lect.	Date	Topics to be Covered	CO Mapped	Lect.	Date	Topics Covered	CO Achieved
1		Evolution of Computer Systems, Von Neumann Architecture, Moore's Law, Computer Types	CO1				
2		Functional Units, Devices (Input, Output, Storage & Communication Devices)	CO1				
3		Memory System (RAM, ROM, Cache, VM, etc.)	CO1,CO2				
4		Introduction to Logic Gates, Truth Table	CO1,CO2				
5		K-Map	CO1,CO2				
6		K-Map Examples	CO1,CO2				
7		Latch (SR, D, T, JK)	CO1,CO2				
8		Flip Flops (SR, D)	CO1,CO2				
9		Flip Flops (T, JK),	CO1, CO2				
10		Encoder & Decoder	CO1,CO2				
11		MUX & DEMUX	CO1,CO2				
12		Registers & Counters	CO1,CO2				
13		Binary Number system, Overview of RISC/CISC, RISC vs. CISC.	CO1,CO2				

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## UNIT-II

Session Plan				Actual Delivery			
Lect.	Date	Topics to be Covered	CO Mapped	Lect.	Date	Topics Covered	CO Achieved
1		Computer Organization and Design, Instruction Codes, Op-Code	CO2				
2		Computer registers, Computer Instructions	CO2				
3		CPU stack Organization, Instruction Formats, Instruction types	CO2				
4		Timing and control, Instruction and Instruction sequencing	CO2				
5		Instruction Cycle, Memory Reference	CO2				
6		Instructions, Addressing modes	CO2				
7		Assignment 1	CO1,CO2				
8		Program Control, Types of Interrupts	CO2				
9		Adder & Subtractor	CO2				
10		Quiz 1	CO1,CO2				

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## UNIT-III

Session Plan				Actual Delivery			
Lect.	Date	Topics to be Covered	CO Mapped	Lect.	Date	Topics Covered	CO Achieved
1		Introduction Control Unit Design	CO3				
2		Instruction Interpretation & Execution	CO3				
3		Control Transfer, Fetch Cycle	CO3				
4		Micro programmed Control, Control Memory	CO3				
5		Micro programmed Control, Control Memory contd.	vCO3				
6		Hardwired Control Unit	CO3				
7		Micro programmed vs. Hardwired Control Unit	CO3				
8		Nano Programming	CO3				
9		Superscalar processing	CO3				

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### UNIT-IV

Session Plan				Actual Delivery			
Lect.	Date	Topics to be Covered	CO Mapped	Lect.	Date	Topics Covered	CO Achieved
1		Memory Locations & Addresses, Semiconductor Memory, Static and Dynamic Memory	CO1,CO4				
2		Main Memory, Auxiliary Memory, Associative Memory	CO1,CO4				
3		Cache Memory	CO1,CO4				

### UNIT-V

Session Plan				Actual Delivery			
Lect.	Date	Topics to be Covered	CO Mapped	Lect.	Date	Topics Covered	CO Achieved
1		I/O and their brief description	CO4				
2		Bus Interface, Bus arbitration, Data Transfer	CO4				
3		Assignment 2	CO3,CO4				
4		Types of Interrupts, I/O Interrupts	CO4				
5		Quiz 2	CO3,CO4				
6		Channels, Direct Memory Access	CO4				
7		I/O processing	CO4				
8		Test	CO1,CO2,CO3,CO4				