

# Hitik Kumar Nayak

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## WORK EXPERIENCE

<b>CMOS &amp; Verilog • Internship</b> NIT Raipur, Naya Raipur Summer internship in CMOS domain of ECE By ece faculty in NIT Raipur Learn and worked in LTspice, Cadence, Xilinx	May 2024 - Jun 2024
<b>Python Development • Internship</b> Softnetix, Virtual	Apr 2024 - May 2024
<b>Machine Learning • Internship</b> Softnetix, Virtual	Apr 2024 - Present
<b>Web Development • Internship</b> Zidio Development, Virtual	Apr 2024 - Present

## EDUCATION

<b>B.Tech, Electronics and Communication</b> Guru Ghasidas University	2021 - 2025
<b>Senior Secondary (XII), CBSE</b> Modern Public School Percentage: 76.80%	2021
<b>Secondary (X), CBSE</b> Maharshi Vidya Mandir Percentage: 77.00%	2019

## PORTFOLIO

[GitHub link ↗](#)

## PROJECTS

<b><a href="#">Hybrid Full Adder implimentation by CMOS VLSI ↗</a></b> May 2024 - Jun 2024	<b><a href="#">Weather website ↗</a></b> Apr 2024 - May 2024 Designing a weather website by using HTML, Javascript, CSS
<b><a href="#">Password Generator PYTHON ↗</a></b> Apr 2024 - May 2024 A basic password generator using Python, choose how long password to be and it has uppercase letters, lowercase letters, numbers, and special characters. Generator will make a random password .	<b><a href="#">Content Writing ↗</a></b> Feb 2022 - Mar 2022
<b><a href="#">Car number plate detector by Matlab ↗</a></b> Aug 2024 - Present	

## SKILLS

• VHDL	• HTML	• HTML&CSS
• JavaScript	• Verilog	• Content Writing
• MATLAB	• VLSI	