

Hung Lam

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EDUCATION

University of Virginia, College of Arts and Sciences

August 2023 - May 2027

Bachelor of Arts in Computer Science and Economics

Charlottesville, VA

- **Cumulative GPA:** 3.91/4.00 (Dean's List Honors)
- **Involvements:** Project Code at UVA, Echols Scholar, Hoos First, HooHacks, UVA Club Running (Marathon Finisher), VSA, OYFA, Elizabeth White Barr Scholar (\$7,000), Outside Scholarships Awarded (\$13,000)

EXPERIENCE

Zbooni

Dec 2025 – Jan 2026

Software Engineer Intern (J-Term)

Dubai, UAE

- Built a **Python/Django** internal **risk dashboard** to simulate real-time order verification for a connected-commerce platform processing **2,000 orders/day** and **\$600K GMV**
- Engineered a configurable **fraud risk-scoring engine** using **email heuristics**, **rule-based analytics**, **geolocation intelligence**, **behavioral velocity detection**, and **anomaly detection**, achieving a **193 ms** average per-order runtime
- Externalized **fraud logic** into **configurable, non-hard-coded JSON configurations**, enabling rapid tuning of **thresholds** and **weights**.
- Developed **pandas + SQLite** ingestion and simulation pipelines over **1M+ historical orders** spanning **2 years** to quantify **fraud catch-rate** versus **false-positive** tradeoffs and guide **production rollout**

Virginia Microelectronics Consortium (VMEC)

May 2025 – Aug 2025

Software Engineer Intern

Blacksburg, VA

- Built automated **data analysis pipelines** for **100,000+ rows** of **GRIMM** and **SEMS** experimental data, cutting processing time from **hours to minutes** and streamlining lab workflows.
- Implemented modular readers and analyzers using **Python**, **pandas**, and **openpyxl** to parse time-stamped sensor logs, compute upstream/downstream averages, and apply calibration factors.
- Designed a mobile-integrated workflow for a **Micro GC** analyzer enabling **real-time data transfer** to smartphones to support field experimentation and rapid analysis.
- Developed **Arduino** firmware with structured **control logic** to interface with laboratory hardware, manage sensor I/O, and automate experimental workflows.

PROJECTS

AccessFlow | *Python, Django, React, REST APIs, SQLite*

- Built a full-stack internal access-request management system to replace ad-hoc email and chat approvals with auditable, policy-driven workflows.
- Designed **Django REST** endpoints for submitting, approving, denying, and expiring access requests with role-based authorization and approval routing.
- Implemented a **React** frontend for request submission and status tracking, integrating with backend APIs for real-time updates.
- Enforced time-bound access controls with automatic expiration and persistent audit logs to support compliance and security reviews.

CourseReviews | *Java, JavaFX, SQLite, MVC Architecture*

- Built a desktop application using **JavaFX** following **MVC architecture**, supporting user authentication, course search, and review management.
- Designed modular **controller and service layers** to separate UI logic from business logic, improving maintainability and feature extensibility.
- Implemented persistent storage with **SQLite** and custom **DAO abstractions** to manage users, courses, and reviews with transactional integrity.

SKILLS

Programming Languages: Python, Java, JavaScript, SQL, C, R, HTML, CSS

Frameworks/Libraries: React.js, Next.js, Node.js, Express.js, Django, Django REST Framework, JavaFX, JUnit, pandas, openpyxl

DevOps: AWS, Git, CI/CD, Bash Scripting, PowerShell, Linux, Gradle

Databases: PostgreSQL, MySQL, SQLite