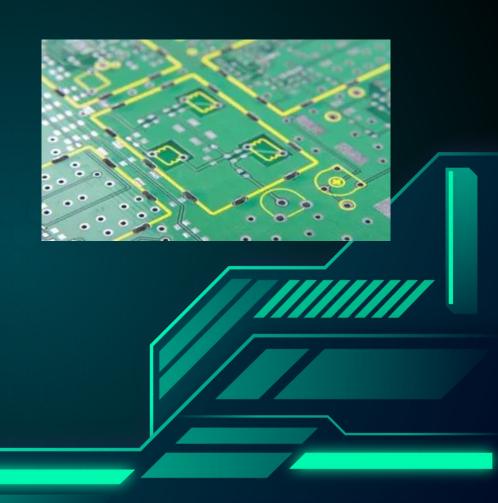




- 1. Introduction
- 2. Diagrams
- 3. Schematics
- 4. Code
- 5. Accomplishments/Obstacles
- 6. Future Plans



Intro



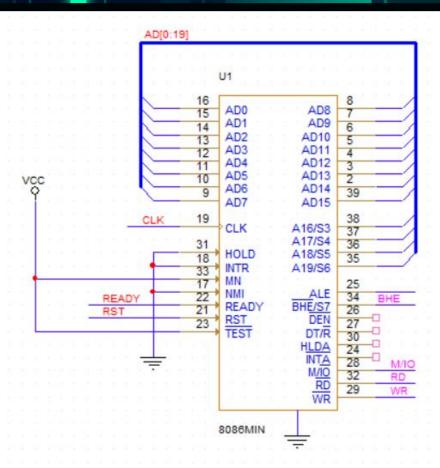
Goal:

Design and build the necessary components of simple clock tracking device

Design Specifications

- Has to sign people in and out
- Store employees and not erase (small company of 50 employees or less)
- Work without wifi

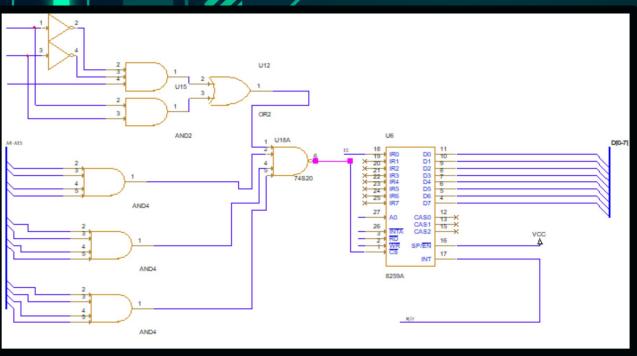




- Time calculation
- Sign In(1)/Out(0)
- OvertimeCalculations

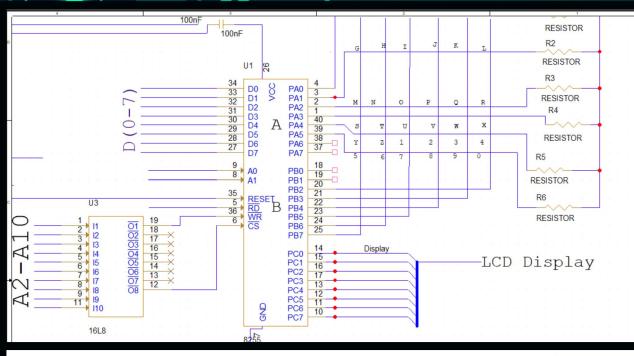
U4 AEN1 AEN2 CLK PCLK READY 10 RESET OSC 15MHZ CRYSTAL 15 **ASYNC** CSYNC RES 8284 VCC R7 RESISTOR DIODE C1 10uF SW T SPDT

- Simple reset generator
- Reset connects to 82C55 reset
- CLK for 8086
- Reset



- Necessary Decoding for chip select
- S/1
- IR71-IR6- No connects*
- D[0-7]

Schematic: 82C55



U17

VOUT

VCC

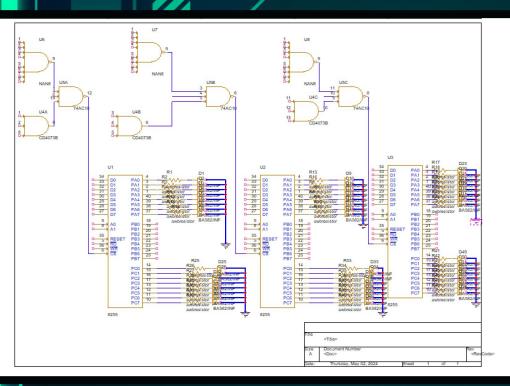
- Keyboard Matrix
- Port C for LCD Display output
- 16L8 necessary decoder for Chip select
- 01
- 08

C2

100nF

100nF

Common voltage source



- 3 units for multiple 1/0 support
- Can be connected to external numpad
- 3 different port addresses
- Allows more space for multiple people to sign in at once



Accomplishments & Obstacles

Accomplishments

- Interactive Menu
- FunctionalCalculator

Obstacles

- Tools
- External Resources
- TimeConstraint







Optimized Code

16 bit code

Card Scanning

No keyboard input

Memory

No file