

Chapter 4 - Threads and Processes

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Communication
 Primitives

semaphores message passing

monitors

2. **CPU**

Hardware circut within a computer that carries out instructions of a computer program by performing the basic arithmetic, logical, control and input/output (I/O) operations specified by the instructions.

3. CPU Pipeline

a set of data processing elements connected in series, where the output of one element is the input of the next one. The elements of a pipeline are often executed in parallel or in time-sliced fashion; in that case, some amount of buffer storage is often inserted between elements

4. Interprocess

Communication

a set of programming interfaces that allow a programmer to coordinate activities among different program processes that can run concurrently in an operating system. This allows a program to handle many user requests at the same time. Since even a single user request may result in multiple processes running in the operating system on the user's behalf, the processes need to communicate with each other. The IPC interfaces make this possible. Each IPC method has its own advantages and limitations so it is not unusual for a single program to use all of the IPC methods.

IPC methods include pipes and named pipes; message queueing; semaphores; shared memory; and sockets.

5. Interruption
Handler (First level)

The operating system instructs the hardware to jump to the first level interrupt handler whenever an interrupt occurs; there may be several FLIHs if interrupts are 'vectored' (i.e. different interrupts jump to different places); needs to save the machine state.

The first level interrupt handler (FLIH) is the part of the operating system which is responsible for responding to signals both from the outside world (interrupts) and from within the computing system itself (error traps and system calls). We shall refer collectively to both types of signal as interrupts, and use the adjectives 'external' and 'internal' to distinguish between them where necessary. The function of the FLIH is twofold:

- 1. to determine the source of the interrupt
- 2. to initiate service of the interrupt

6. Kernel

A component of the Operating System.

The kernel is a computer program that constitutes the central core of a computer's operating system. It has complete control over everything that occurs in the system. As such, it is the first program loaded on startup, and then manages the remainder of the startup, as well as input/output requests from software, translating them into data processing instructions for the central processing unit. It is also responsible for managing memory, and for managing and communicating with computing peripherals, like printers, speakers, etc. The kernel is a fundamental part of a modern computer's operating system.

Contents of the Kernel

1.First Level Interrupt Handler
2.Low level Scheduler (Dispatcher)
3.Communication Primitives

- 3 Types of Kernel
- 1. Monolithic
- 2. Microkernel
- 3.Hybrid

7. Low Level Scheduler

This performs process start-up and switching. Start-up is a special case of switching. Process start-up involves: allocate and initialise a Process Control Block (PCB); add the PCB to the list of runnable processes; update other system tables.

8. Process

An instance of a computer program that is being executed. It contains the code, and its current activity (state).

9. **Processes** Process

Threads

Each process provides the resources needed to execute a program. A process has a virtual address space, executable code, open handles to system objects, a security context, a unique process identifier, environment variables, a priority class, minimum and maximum working set sizes, and at least one thread of execution. Each process is started with a single thread, often called the primary thread, but can create additional threads from any of its threads.

Thread

A thread is the entity within a process that can be scheduled for execution. All threads of a process share its virtual address space and system resources. In addition, each thread maintains exception handlers, a scheduling priority, thread local storage, a unique thread identifier, and a set of structures the system will use to save the thread context until it is scheduled. The thread context includes the thread's set of machine registers, the kernel stack, a thread environment block, and a user stack in the address space of the thread's process. Threads can also have their own security context, which can be used for impersonating clients.

10. Processor

the logic circuitry that responds to and processes the basic instructions that drive a computer. Also called the CPU or Central Processing Unit

11. Scheduler

process scheduling is the activity of the process manager that handles the removal of the running process from the CPU and the selection of another process on the basis of a particular strategy.

3 Types of Scheduler

Long Term **Short Term** Medium Term

12. Thread

a collection of instructions which together form a specific task. Eg. Single-threaded, multi-tasking operating systems

Each processor provides a separate "thread of control" within a single program in memory.