

1. <b>Addressing - Direct</b>	requires destination requires sources also implicit	13. <b>mutual exclusion - interrupt disabling</b>	+process will run until interrupt +disabling interrupt guarantees mutual exclusion -efficiency could be degraded -does not work in multi-proc architecture
2. <b>Addressing - Indirect</b>	put in queue for receivers to check "port"	14. <b>mutual exclusion - special machine instruction</b>	+applicable to any # of processors +easy to verify -busy-waiting employed, cpu time wasted -starvation possible -deadlock possible
3. <b>atomic operation</b>	not breakable into smaller parts	15. <b>O.S. Concerns for concurrency</b>	OS must be able to keep track of various processes OS must be able to allocate and de-allocate resources for each active process OS must protect the data and physical resources of each process against interference by other processes OS must ensure the processes and outputs are independent of the processing speed
4. <b>Can relative speed of execution of processes be predicted on a uniprocessor?</b>	no	16. <b>Principles of Concurrency</b>	Interleaving overlapping
5. <b>common concurrency mechanisms</b>	semaphore mutex monitor event flags messages spinlocks	17. <b>producer/consumer problem</b>	producer places data into buffer consumer takes from buffer only one may access at a time
6. <b>critical section</b>	section of code requiring shared resources and will fail if run when another process is in corresponding section of code	18. <b>race condition</b>	...
7. <b>deadlock</b>	two or more processes are each waiting for the other to do something	19. <b>Resource Competition</b>	- must be enforced - halts must not interfere with other processes - no deadlock or starvation - must not be denied access to critical section when nothing else is using it - no assumptions about processor speed - processor remains inside critical section for finite time
8. <b>Difficulties of concurrency</b>	sharing of global resources mgmt of optimal resource allocation results are not deterministic & reproducible, so difficult to locate programming errors	20. <b>semaphore operations</b>	1. initialization 2. decrease (semWait) 3. increase (semSignal)
9. <b>livelock</b>	two processes continually change states cancelling each other out	21. <b>semaphore status levels</b>	sv>0 = free sv==0 = used, no waiting sv<0 = used, process waiting
10. <b>message passing</b>	synchronization & communication 3 types both sender and receiver are blocked until msg delivered - tight synch nonblocking sender, blocking receiver - most useful (e.g. service provides resource to other services) neither blocked	22. <b>semSignal &amp; semWait</b>	atomic primitives implemented in firmware also via software algorithm
11. <b>monitors</b>	data structure - control variable like semaphore, but easier to control uses conditional variables		
12. <b>mutual exclusion</b>	when one process is in crit sect, no other proc can be in a crit section accessing shared resources		

