

1. <b>3 approaches to achieving mutual exclusion</b>	1.] Software - let the application enforce it themselves through coding 2.] Hardware - disable interrupts or have special machine instructions 3.] Operating System or language: let the OS or programming language offer support for this capability	10. <b>Cooperation by communication</b>	There is no mutual exclusion here, since no resources are being shared. It's all about communicating between processors. Note that starvation and deadlock is still present though. Deadlock in a sense, two processes waiting for each other message. Starvation in a sense that (p1,p2,p3) p3 is not getting any message back.
2. <b>3 types of process interaction</b>	1.] process not aware of each other 2.] process indirectly aware of each other 3.] process directly aware of each other	11. <b>Counting semaphore</b>	-general semaphore with n values
3. <b>Basic commands in Message Passing are</b>	receive and send	12. <b>Difference between monitors and semaphores</b>	Monitors are more easier to control
4. <b>binary semaphore</b>	-can only take a value of 1 or 0 -wait() - if semaphore value is 1, set the value to zero and keep executing the process. If semaphore value is 0, then block the process. -signal() - check if any process in the queue is blocked and remove it from queue. If queue is empty set the value to one.	13. <b>Direct Addressing</b>	specify destination of process
5. <b>The combinations are possible in message passing for synchronization</b>	1.] Blocking send, blocking receive. Both sender and receiver are blocked until message is delivered 2.] Nonblocking send, block receiver. Sender continues sending messages while receiver is blocked until he receives a message. 3.] Nonblocking send, Nonblocking receive. Neither party is waiting	14. <b>Hardware support for mutual exclusion</b>	1.] Interrupt disabling 2.] Special machine instructions
6. <b>Concurrency occurs in which of the following contexts</b>	Multiple Applications, Structured Applications, OS Applications	15. <b>Hardware Support: Interrupt disabling</b>	problems in interrupt disabling is that it does not work in a multiprocessor environment, since multiple processes execute at once regardless of interrupts.
7. <b>Concurrent</b>	-running at the same time, refers to concurrent activities	16. <b>Hardware support: Special Machine Instructions</b>	A special machine instruction could be designed that carries out two operations atomically, without interruption. Works on uniprocessor and share memory multiprocessor.
8. <b>Concurrent occurs in</b>	Multiprogramming, Multiprocessors, and distributed OS	17. <b>In case of competing resources, 3 control problem must be faced</b>	First is the need for mutual exclusion. The enforcement of mutual exclusion creates two additional problems. One is deadlock. Final control problem is starvation. A process competing for a resource never receives it due to scheduling of other processes.
9. <b>Cooperation among process by sharing</b>	Same problem of mutual exclusion, deadlock and starvation. A new problem is data coherence, which means the problem of keeping data in a consistent state.	18. <b>Indirect Addressing</b>	send to mailbox and let receiving process pickup whenever necessary
		19. <b>Message Passing has two requirements...</b>	One is synchronization and the other is communication.

20. <b>A monitor is a software module with each of the following characteristics</b>	A monitor is a software module with these chief characteristics: Local data variables are accessible only by the monitor Process enters monitor by invoking one of its procedures Only one process may be executing in the monitor at a time, others are suspended waiting for the monitor to become available
21. <b>Monitor uses what for synchronization</b>	condition variables
22. <b>Passing control back and forth in software solutions is called...</b>	coroutine
23. <b>Process directly aware of each other</b>	Process that communicate each other by process ID and that are designed to work jointly on some activity. Such process exhibits cooperation
24. <b>Process indirectly aware of each other</b>	Process that are not necessarily aware of each other by the respective process IDs but that share access to some object, such as IO buffer. Such process exhibit cooperation in sharing the common object
25. <b>Process unaware of each other</b>	These are independent processes that are not intended to work together. These can either be batch jobs or interactive sessions. Processes are not working together, but OS needs to be concerned about the competition for resources. For example, two independent applications may both want to access the same disk or file or printer. OS must regulate these accesses
26. <b>Producer/Consumer Problem</b>	In binary semaphores, the values must be saved inside. You can make a counter semaphore too, but make sure where your placement of semaphore is done. You don't want to deadlock the system.
27. <b>Reader/Writer Example</b>	Read slide

28. <b>Requirements for mutual exclusion</b>	1.] Enforcement-only one processor at a time 2.] Non-interference - a process that halts in non-critical section does not affect other processors 3.] No deadlock or starvation needing to enter critical section 4.] When critical section open, any processor can go 5.] No assumption should be made about speed of process 6.] Finite time inside critical section
29. <b>Semaphore</b>	-wait() decrements a value -signal() increments a value -the value may be initialized to nonnegative number
30. <b>Some of the problems with concurrency</b>	Sharing of resources, allocation of resources, debugging
31. <b>Strong semaphore</b>	-queue uses FIFO ordering. There is no starvation
32. <b>The various schemes for specifying processes in send and receive primitives fall into two categories:</b>	direct addressing and indirect addressing
33. <b>Weak Semaphore</b>	- No ordering in queue. Probably would have a starvation
34. <b>What are OS design concerns in concurrency?</b>	-allocation of resources(Deadlock), keep track of various processes using PCB, protect data and resources from interference of another process, process must be independent of results
35. <b>What are some problems with Hardware support: Special Machine Instructions</b>	1.] Busy-waiting consumes processor times 2.] Starvation possible 3.] Deadlock possible if higher priority process comes in. p2 is higher priority and p1 is low priority. p1 is interrupted because p2 is high priority, but p1 can't go inside because of mutual exclusion but its higher priority so it has to go