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MSP43_IO

Clock_Drivers

Surrond_Channel

Up_Channel

Front_Right_Channel

Front_Left_Channel

Center_Channel

Subwoofer_Channel

Power_Supplies

Analog_Io_Board

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NDG

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Title: **Amplificateur Audio (Base Saine)**

Size: A3Date: 2025-05-19KiCad E.D.A. 9.0.0

Rev: 1.0Id: 1/11

1

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4

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A

B

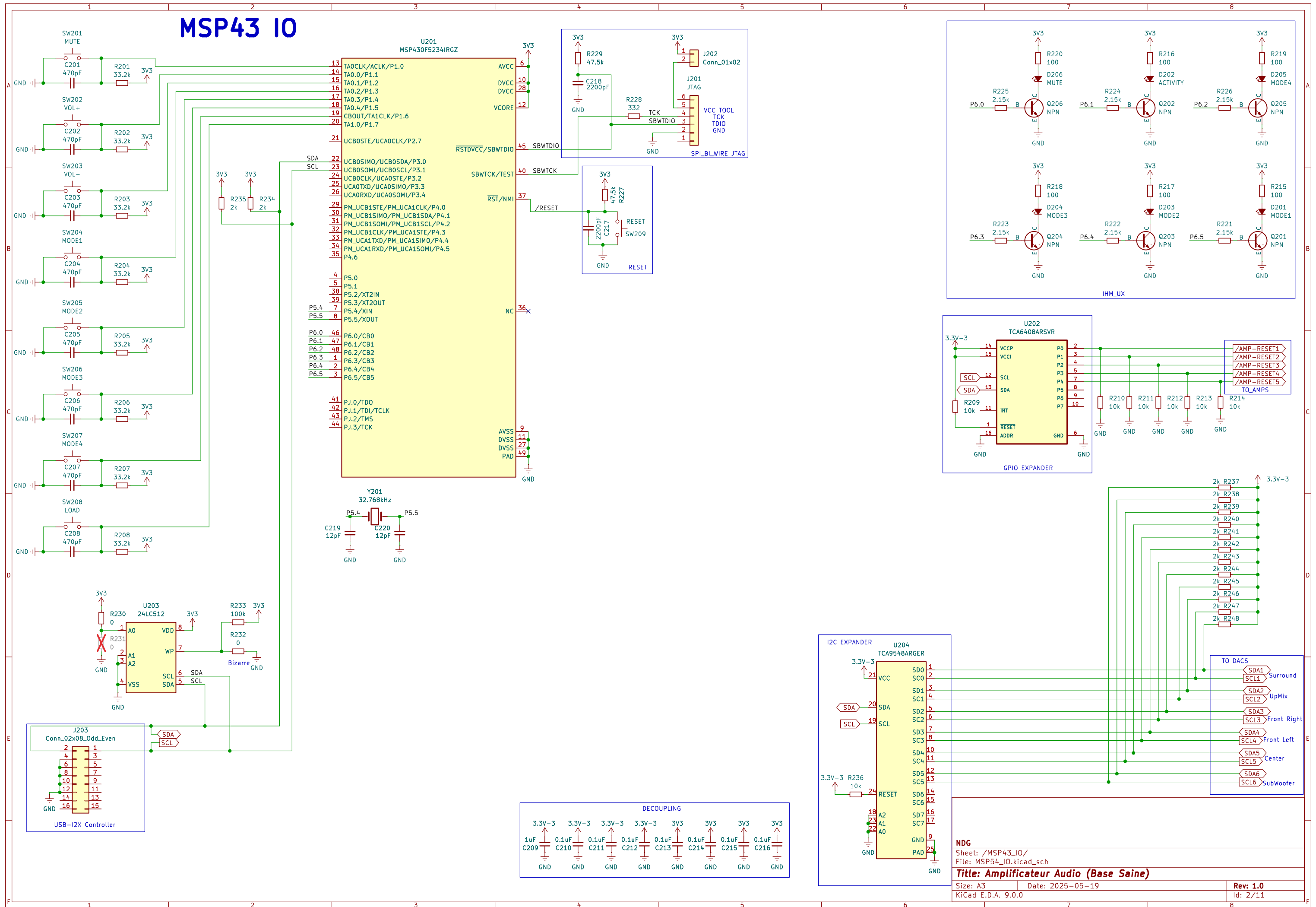
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D

E

F

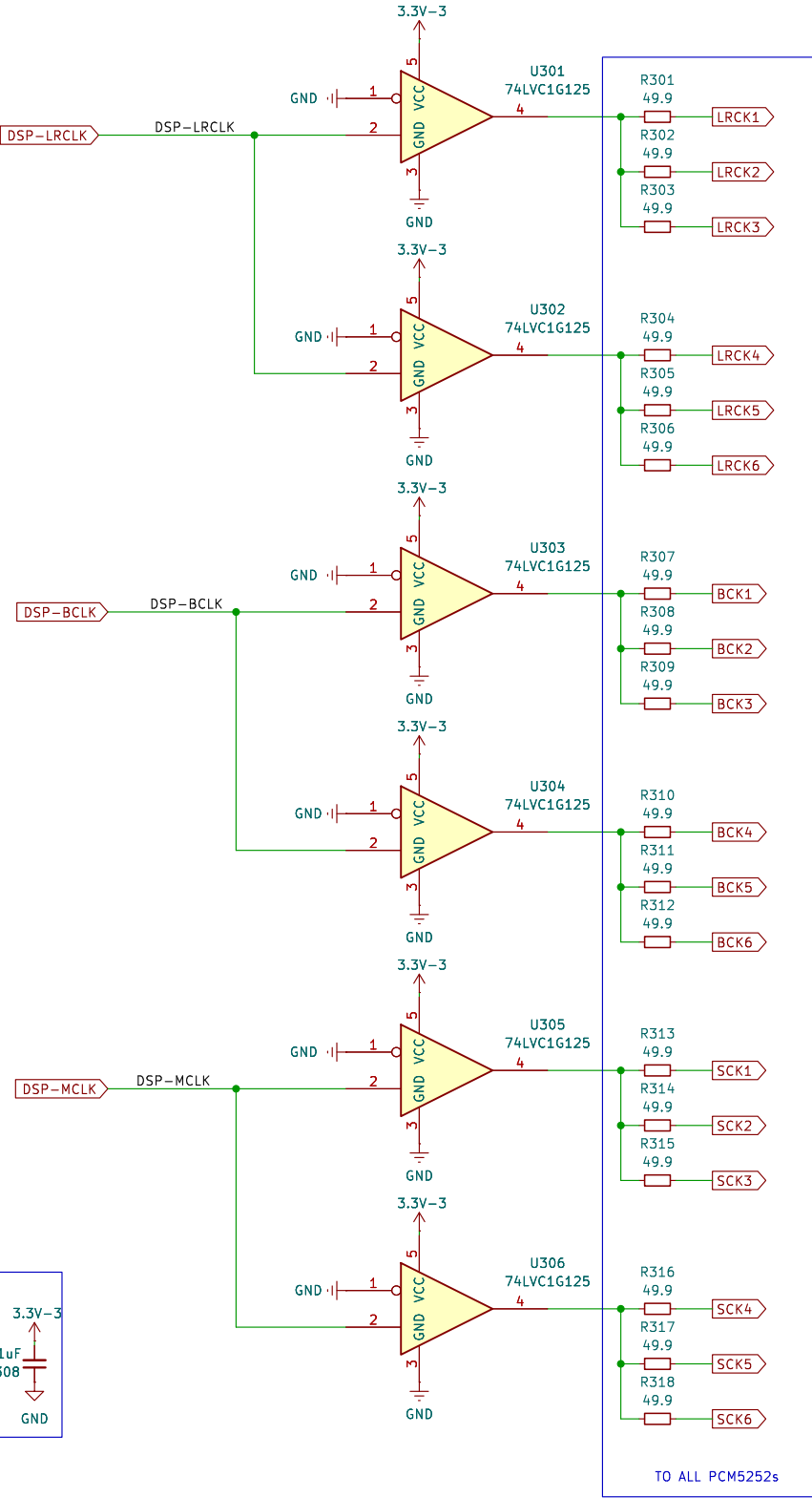
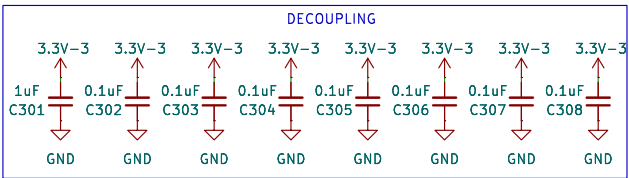
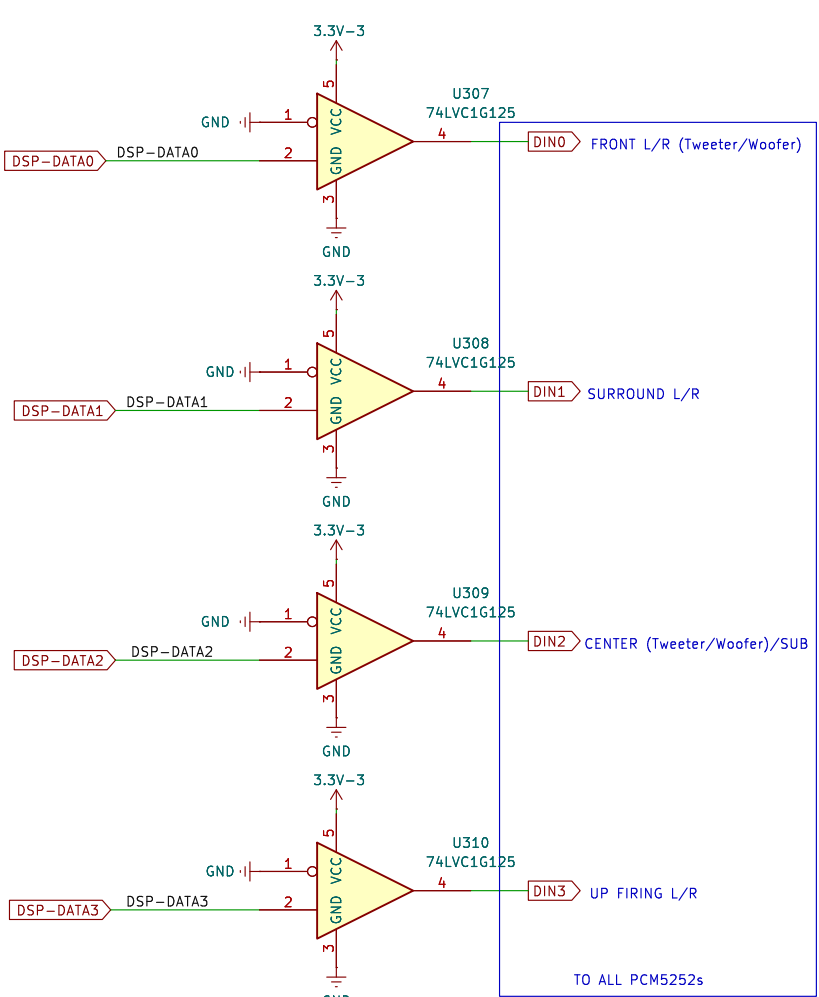
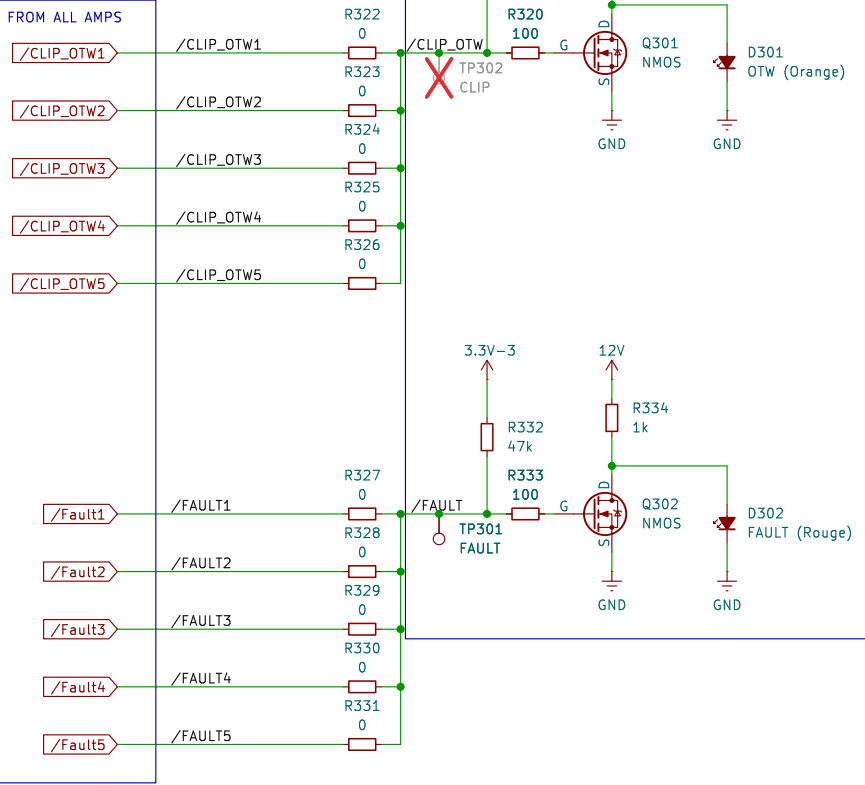
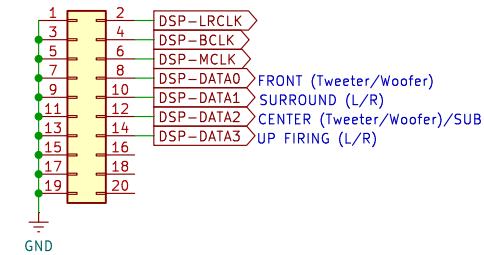
MSP43 IO



Clock Drivers

DIGITAL INPUT CONNECTOR

J301
Conn_02x10_Odd_Even



NDG

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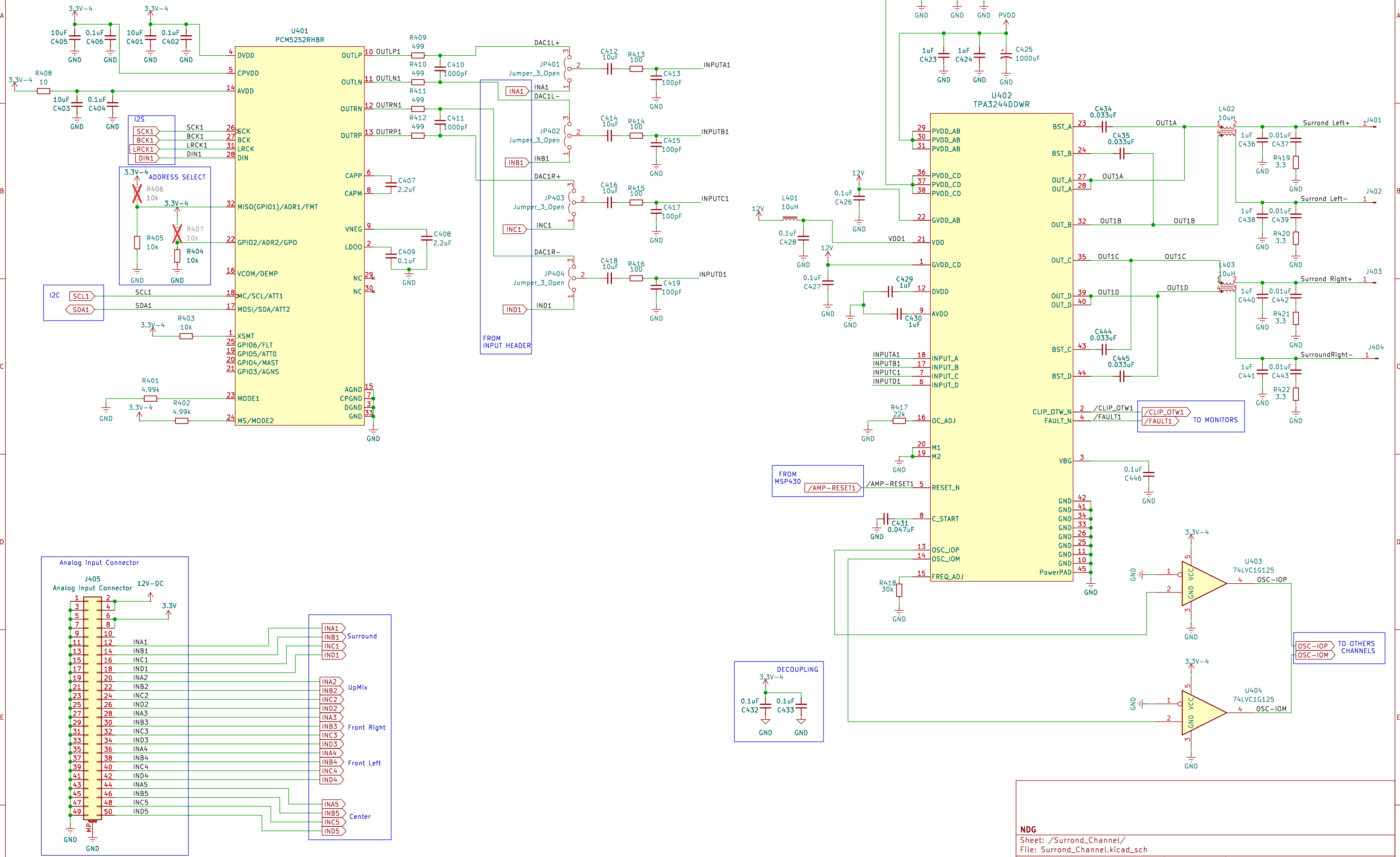
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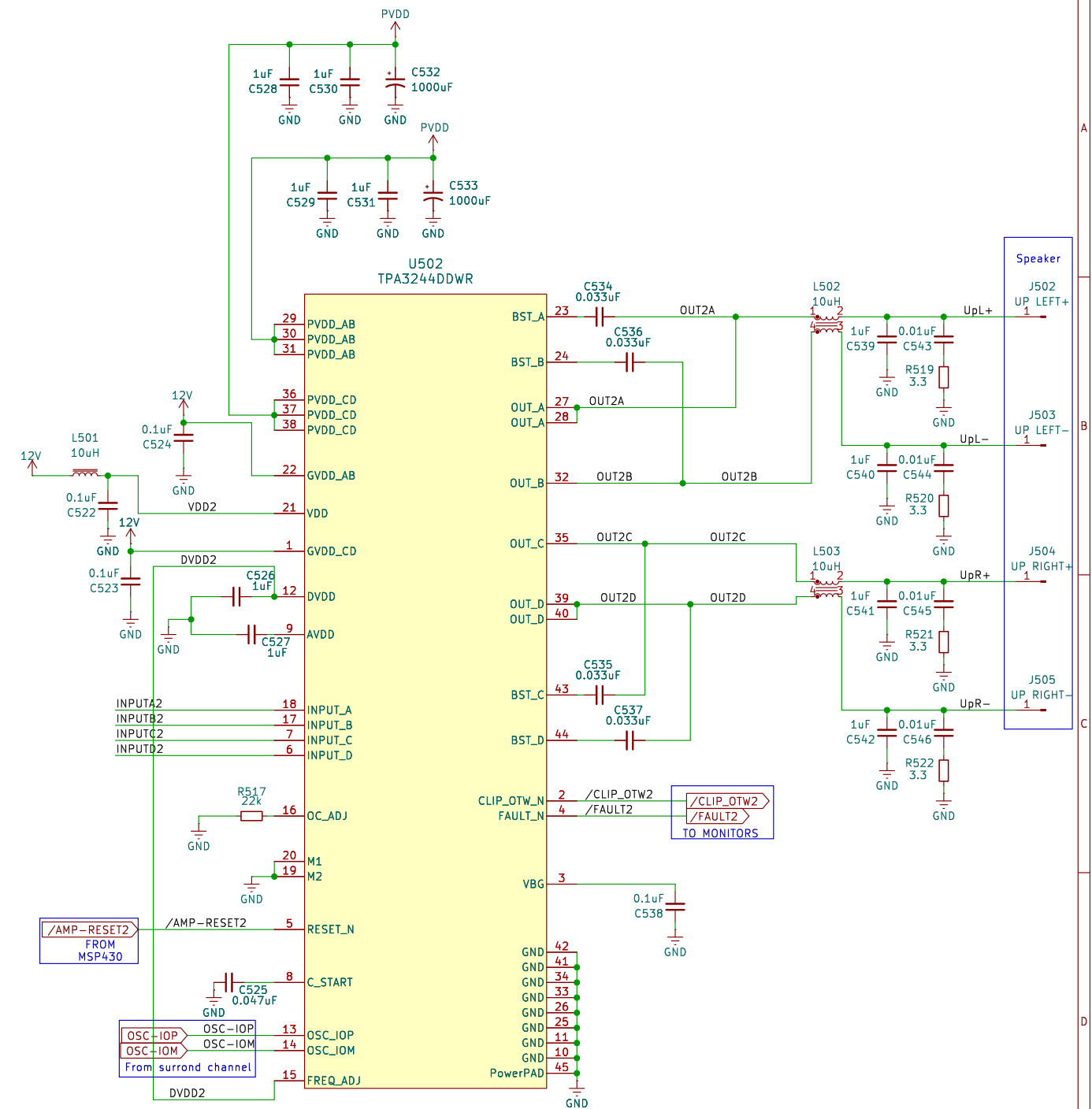
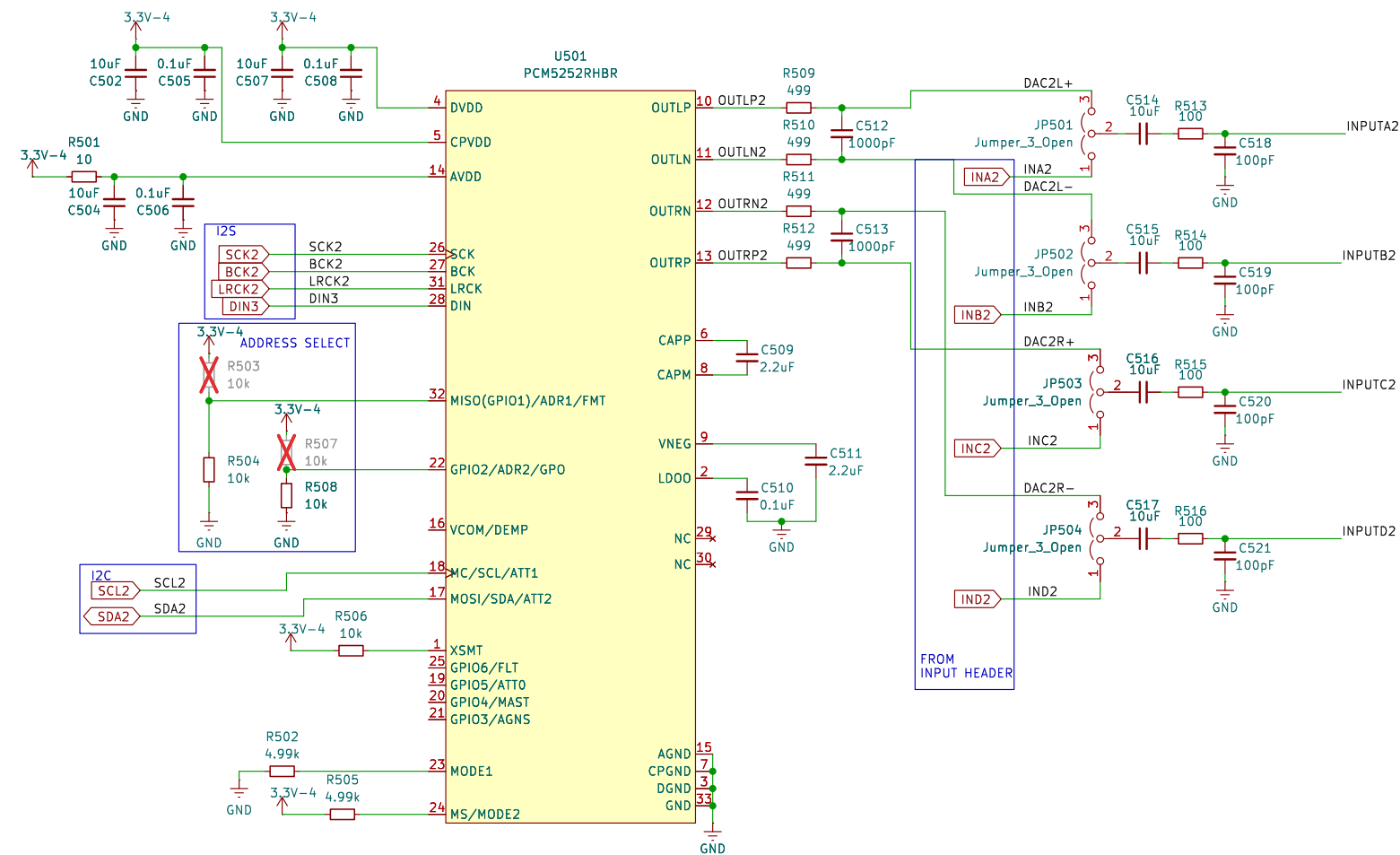
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SURROUND CHANNEL



UP CHANNEL



NDG

Sheet: /Up_Channel/
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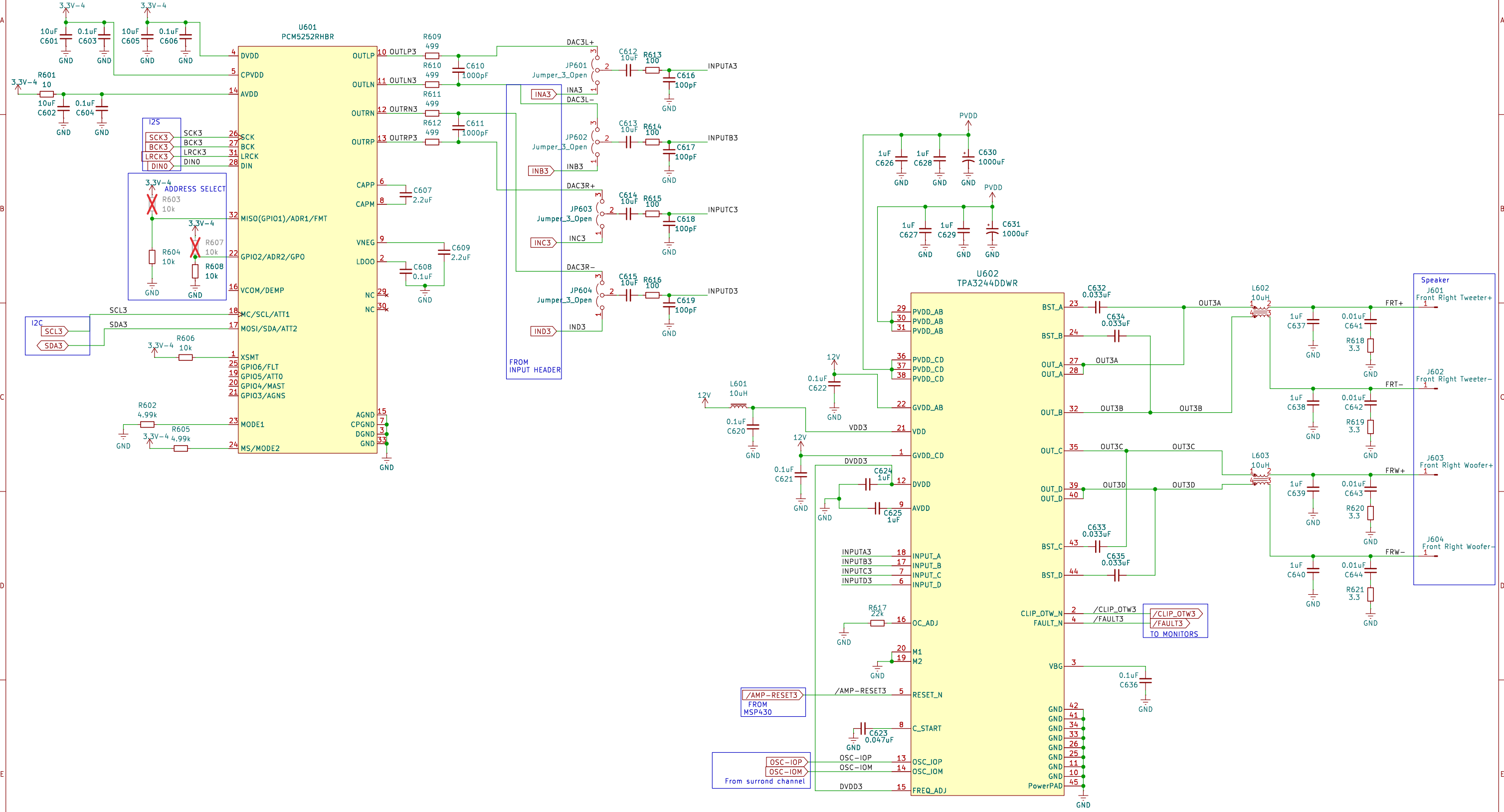
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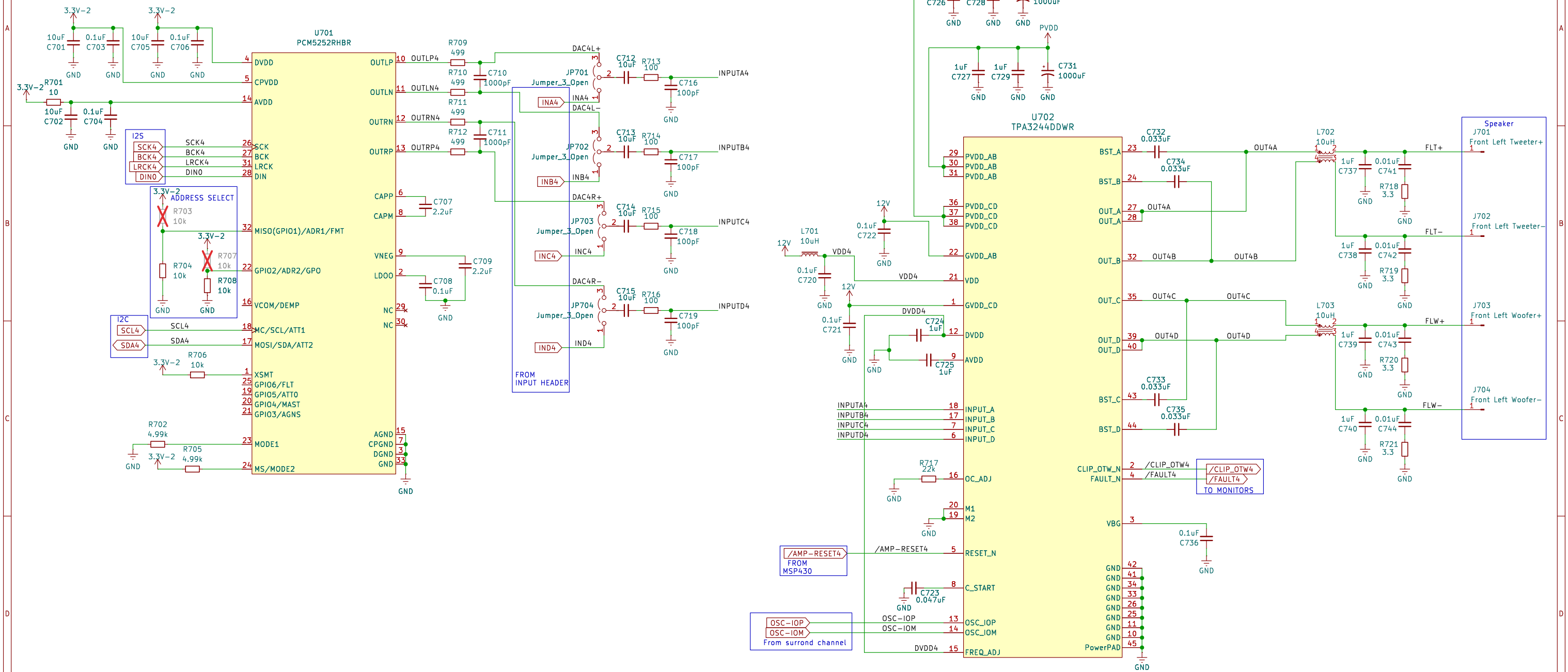
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Rev: 1.0

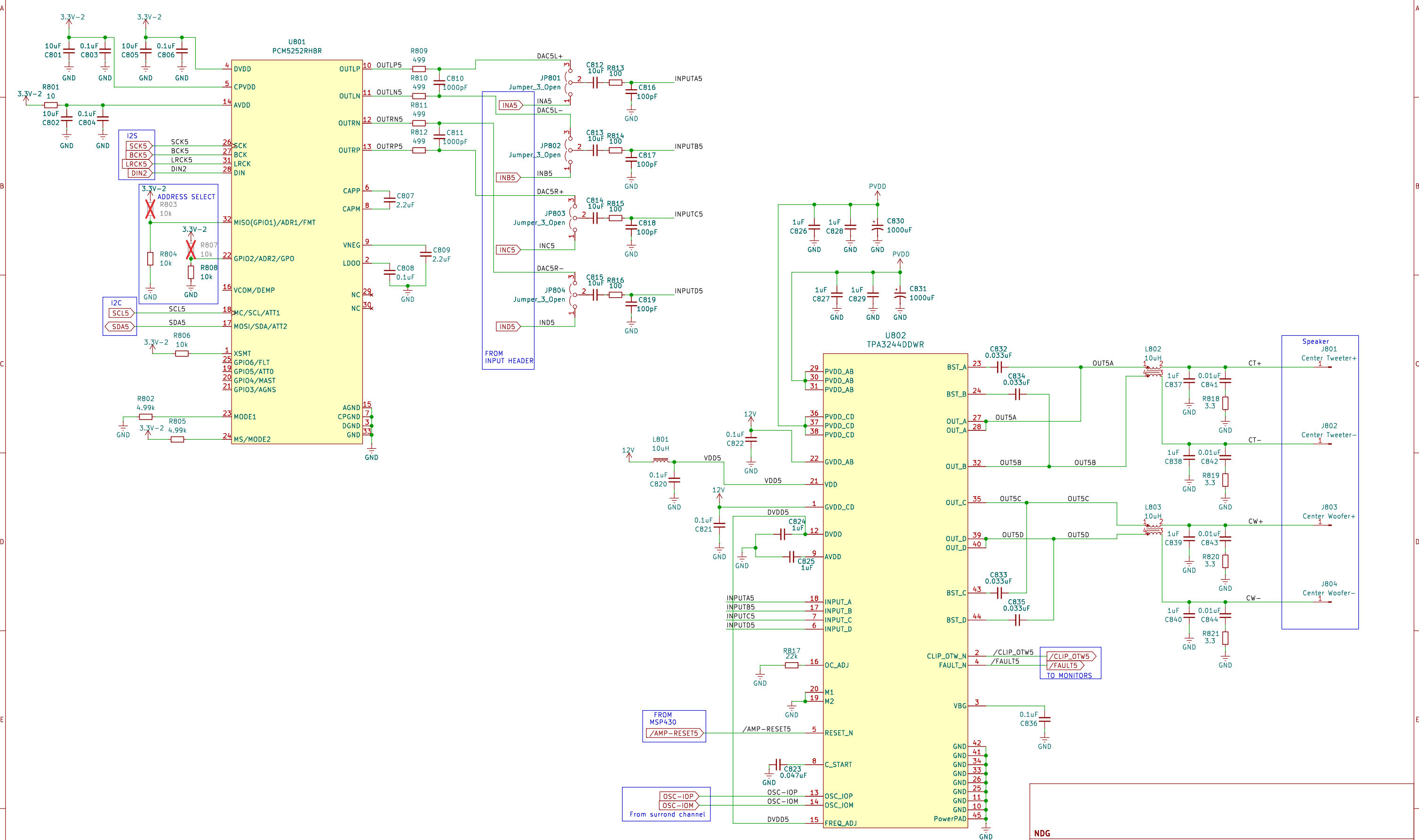
FRONT RIGHT CHANNEL



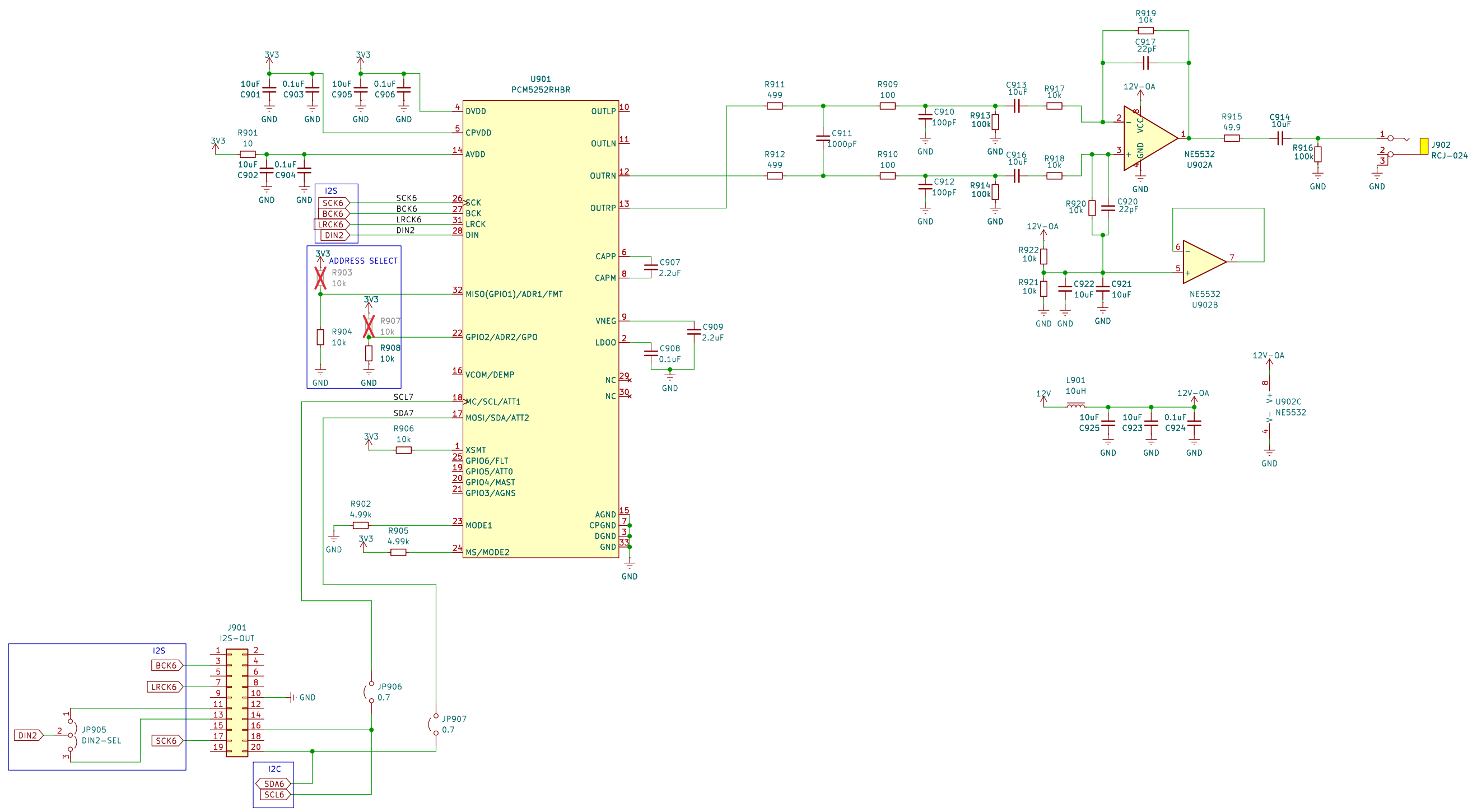
FRONT LEFT CHANNEL

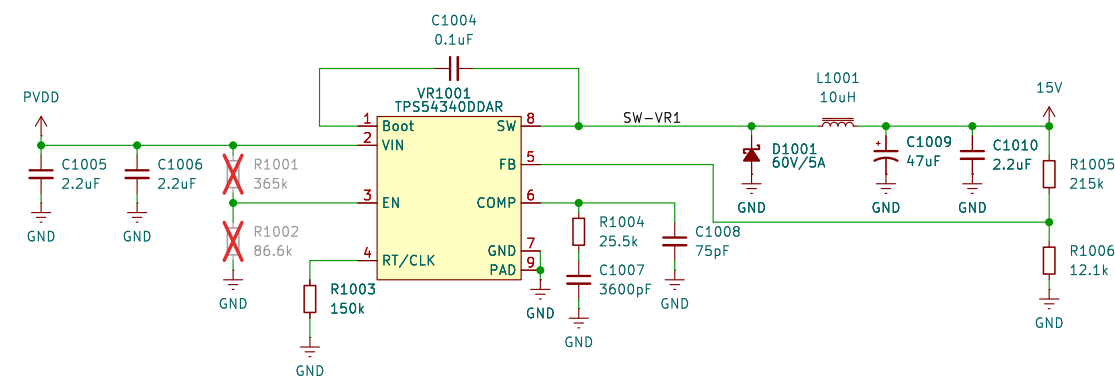


CENTER CHANNEL



SUBWOOFER CHANNEL





The three circuit diagrams illustrate the power supply and signal conditioning for the VR1002, VR1003, and VR1005 modules. Each diagram includes a 15V input, a 12V output, and a 3.3V output, with various capacitors and resistors labeled.

VR1002 LM2940CT-12/NOPB: This circuit shows a 15V input connected to the IN pin (pin 1) of the LM2940CT-12/NOPB regulator. The GND pin (pin 2) is connected to ground. The OUT pin (pin 3) is connected to the 12V output. A 12V output is also shown with a 10uF capacitor (C1015) and a 10uF capacitor (C1016). The 3.3V output is connected to the 3.3V-VR6 line.

VR1003 LM2940CT-12/NOPB: This circuit shows a 15V input connected to the IN pin (pin 1) of the LM2940CT-12/NOPB regulator. The GND pin (pin 2) is connected to ground. The OUT pin (pin 3) is connected to the 12V output. A 12V output is also shown with a 10uF capacitor (C1021) and a 10uF capacitor (C1022). The 3.3V output is connected to the 3.3V-1 line.

VR1005 TLV1117-33IKVURG3: This circuit shows a 12V input connected to the IN pin (pin 3) of the TLV1117-33IKVURG3 regulator. The GND pin (pin 1) is connected to ground. The OUT pin (pin 4) is connected to the 3.3V output. A 3.3V output is also shown with a 10uF capacitor (C1023) and a 10uF capacitor (C1024). The 3.3V output is connected to the 3.3V-2 line.

