K2GEVM

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REV	A
VER	1.00

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Texas Instruments

MISTRAL

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REVISION HISTORY

VER #	DATE	DESCRIPTION OF CHANGES	AUTHOR
0.1	17th July 2017	Drafted from "MS_TI_K2GEVM_SCH_REVD_1V5" document.	Mistral Design Team
0.2	17th July 2018	- Changed 66AK2G02 (600MHz) to X66AK2G12ABY100(1GHz) Part - Changed PMIC IC TPS659118 to TPS65911A - Changed Load switch TPS22907YZTR to TPS22913BYZVR	Mistral Design Team
0.3	18th July 2019	For Review	Mistral Design Team
1.0	18th July 2018	Baselined	Mistral Design Team

NOTE: Refer to MS_TI_K2GEVM_REVD_ECR_ECN document for changes implemented on version 1.6

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Title	REVISION HISTORY	
Size: C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev: A
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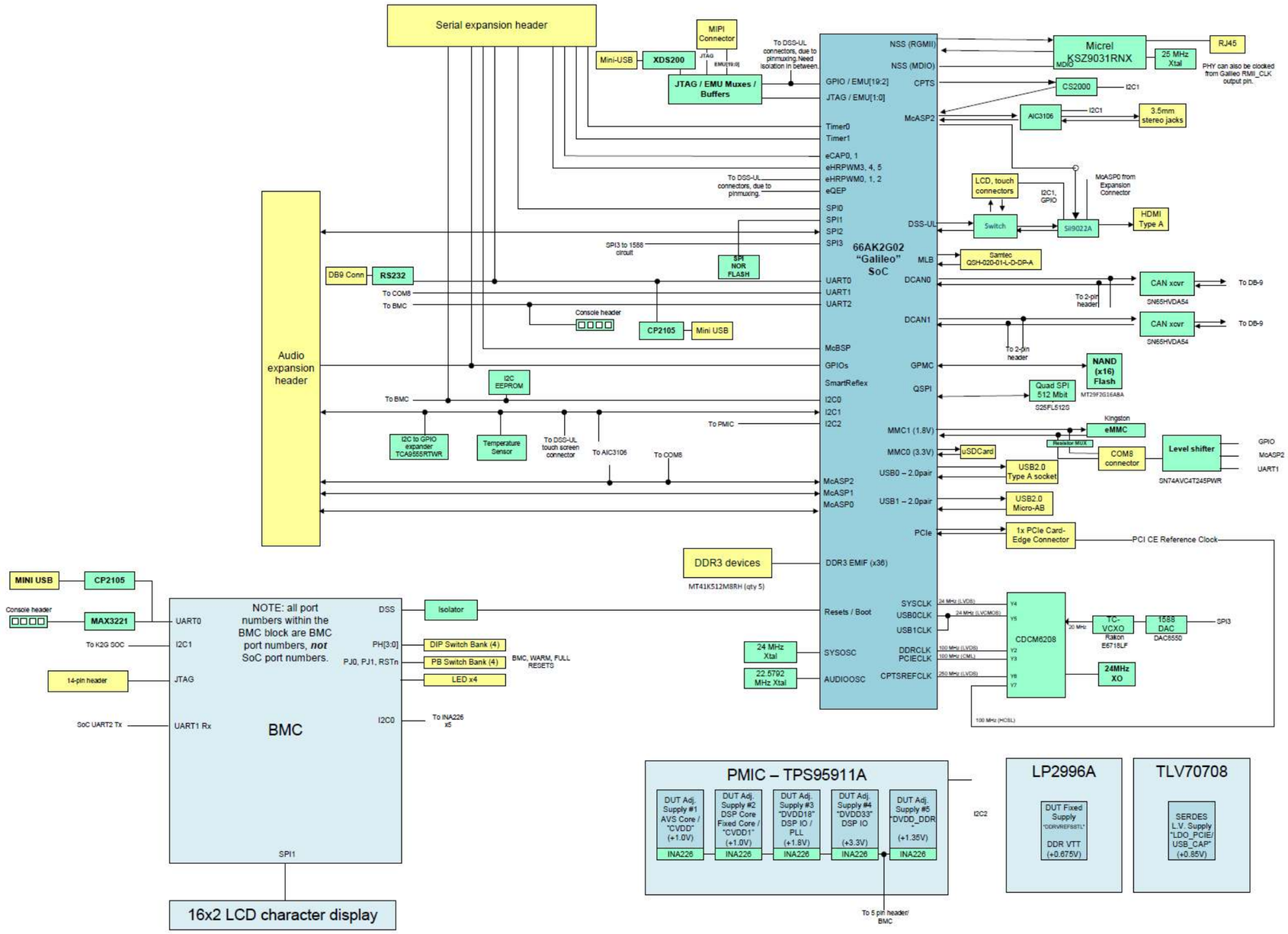
BLOCK DIAGRAM - K2GEVM



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Title	BLOCK DIAGRAM - K2G EVM	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
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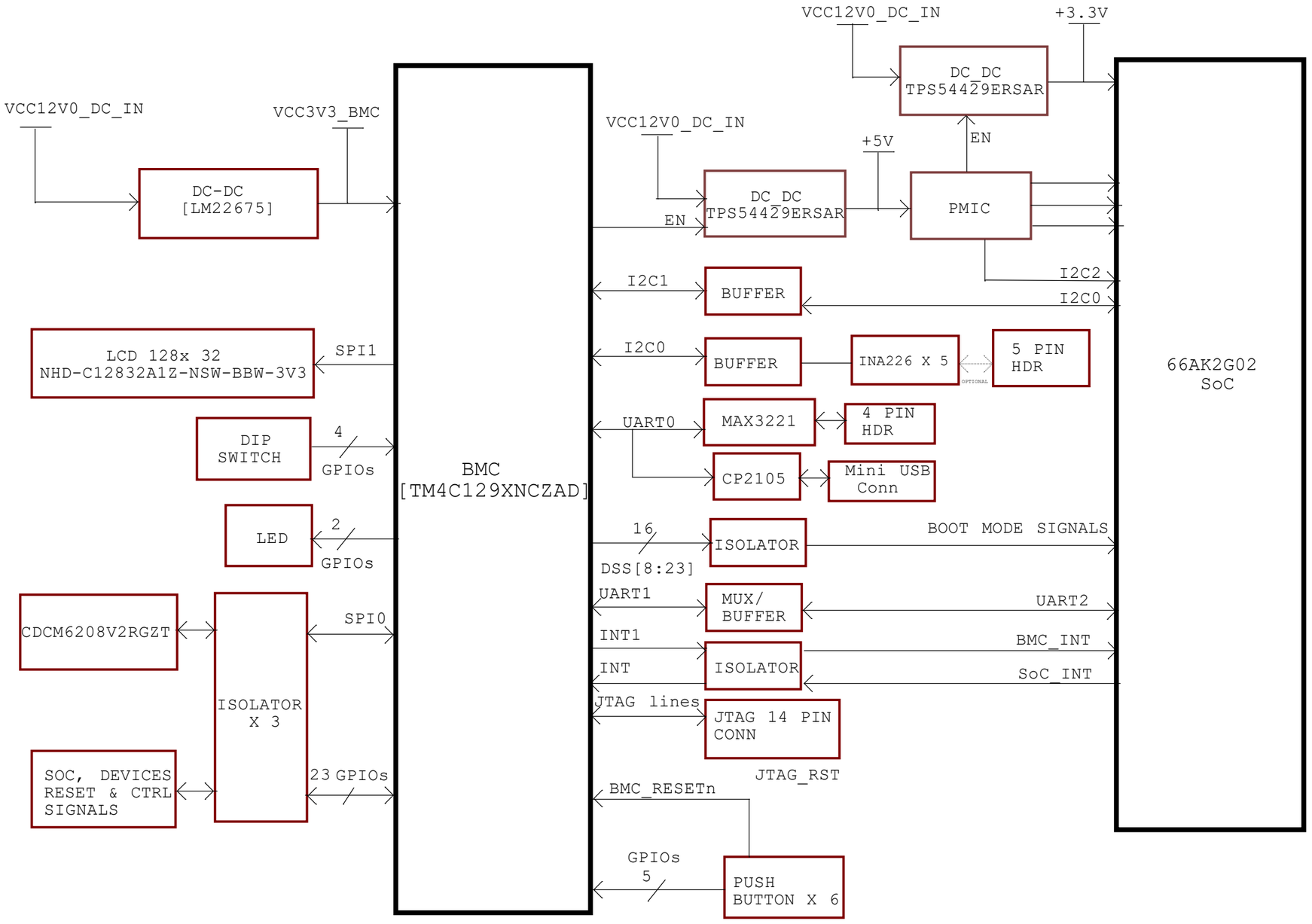
BLOCK DIAGRAM - BMC



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Title	BLOCK DIAGRAM - BMC	
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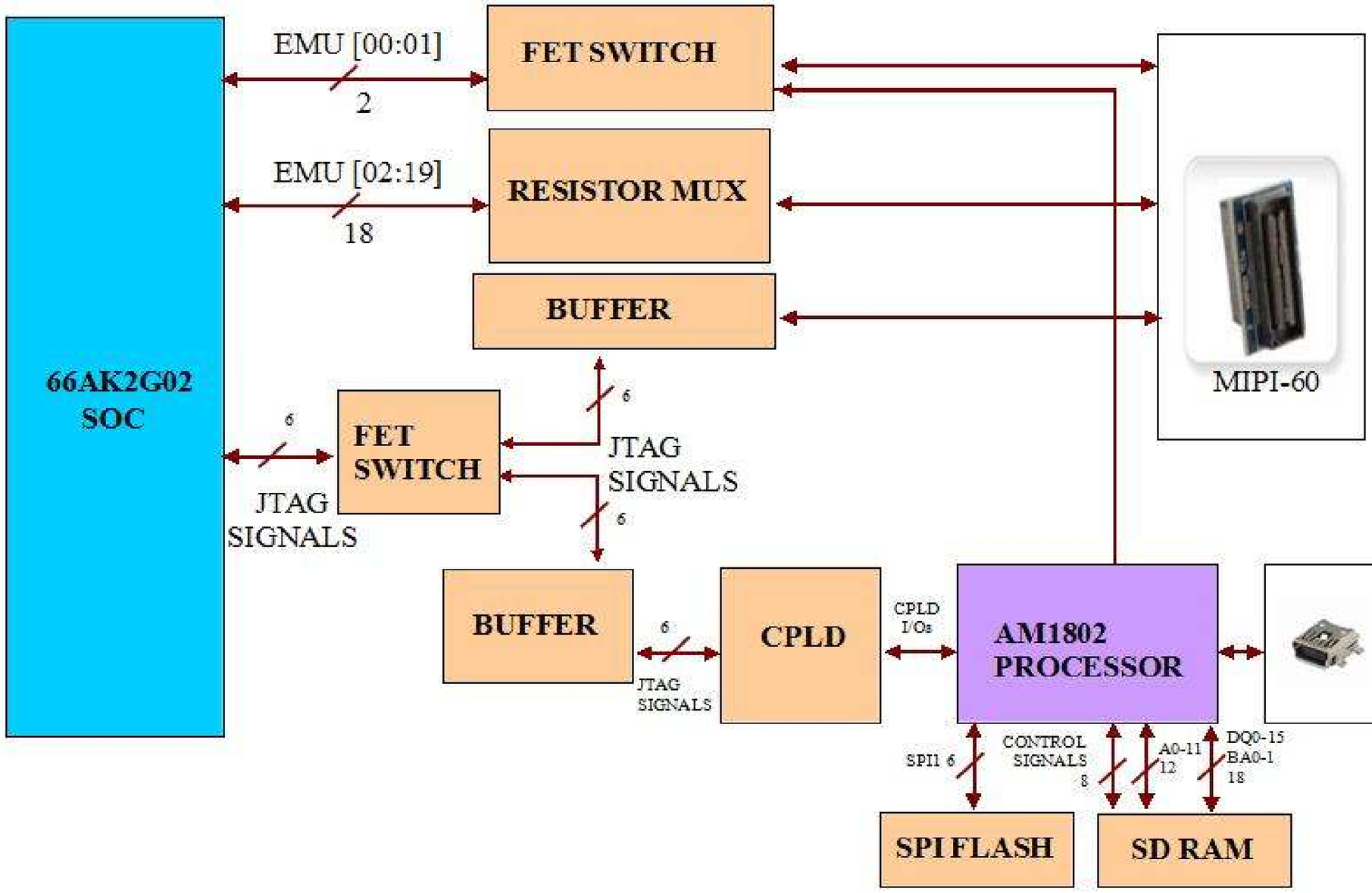
BLOCK DIAGRAM - XDS200



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Title	BLOCK DIAGRAM - XDS200	
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev
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POWER FLOW DIAGRAM

DC IN
+12V
7.6A

OVP CKT

LED

VCC12V0_MLB
+12V

VCC12V0
+12V
1243mA

TPS54429 EN

PMIC_GPIO2

VCC12V0_PCIE
+12V
2000mA

PCIE CONNECTOR

3300mA

MLB CONNECTOR

370mA

400mA

DCAN R1Z-3.305
x2

VCC12V0_DC_IN
+12V
1160mA

5V_EN

TPS54620RGY EN

VCC5V0_DCDC
+5V
4741mA

VCC12V0_DC_IN
+12V
1679mA

VCC3V3_DCIN

VCC12V0_AUD_EXP
+12V
750mA

EN

AUDIO EXPANSION CONNECTOR

VCC12V0_DC_IN
+12V
310mA

LM22675MR

VCC3V3_BMC
+3.3V
924mA

LED

BOARD MANAGEMENT
CONTROLLER(BMC)
TM4C129XNCZADI3

EN

TPS650006RTET

VCC_VBUS
_XDS

VCC1V2
_XDS
+1.2V
38mA

VCC1V8
_XDS
+1.8V
18.3mA

VCC3V3
_XDS
+3.3V
365mA

LED x 3

SPI FLASH
IS25CD010-JNLE

PROCESSOR
AM1802

SDRAM
MT48LC4M16A2B4
-75G

CPLD
XC2C64A-5VQ44C

EN
VCC3V3

TPS79601DRBR

VCOM_BAT
1000mA

COM8 CONN

LED

VCC12V0_DC_IN
+12V
380mA

VCC12V0_DC_IN
+12V
780mA

TPS54429 EN

PMIC_VIO

VCC3V3_DC_IN

+3.3V
2554mA

VCC5V0_DC_DC

PMIC TPS95911A

EN
VCC3V3

TPS71712

VCC1V2
+1.2V
60mA

HDMI TX
SiI9022ACNU

TPS61081

VLED+
+19.2V
40mA

LCD BACKLIGHT
NHD-4.3-480272EF-ATXL-CTP

LP3985IM5-3.0

VCC3V0_BMC_LCD
+3V
35mA

BMC DISPLAY
NHD-C12832A1Z-NSW-BBW-3V3

VCC5V0_USBx 1000mA

USB0 LOAD SWITCH
TPS2051BD

USB1 LOAD SWITCH
TPS2051BD

TLV707085DQNT

SOC_GPIO

SD#

LP2996A

PROCESSOR
AM1802

CPLD
XC2C64A-5VQ44C

VDDR_VTT
+0.675V
272mA

VCC_DDR_VREF
+0.675V
0.05mA

VDD_DDR
+1.35V
1175mA

DDR3 x5
MT41K512M8RH-125:E

VCC1V8
LDO2
+1.8V
276mA

PROCESSOR
AM1802

AUDIO CODEC
TLV320AIC3106IRGZT

Level Translator x3
SN74AVC4T245PW

COM8 VBAT
LDO

EMMC16G-S100

AVDDA_XPLL DVDD18 +1.8V 300mA	DVDD_DDR +1.35V 284mA	CVDD +1.0V 4654mA	CVDD1 +1.0V 464mA	DDR_VREF +0.675V 0.010mA	DVDD33, VCC3V3 +3.3V 133mA	LDO_PCIE /USB_CAP, +0.85V 109mA

K2G PROCESSOR (66AK2G02)

SPI EPROM
N25Q128A13ESF40F

MICRO SD CARD

12.288MHz

I2C EEPROM
AT24CM01

AUDIO CODEC
TLV320AIC3106IRGZT

QSPI
S25FL512SAGMFI011

NAND FLASH
MT29F2G16ABAFAWP:F

GPIO EXPANDER
TCA9555RTWR

LEDs x7

Level Translator x3
SN74AVC4T245PW

EMMC16G-S100

UART TO USB
CP2105

DCAN0 & 1
ISO1050

CS2000CP-CZZ

DAC 8550IBDGKT

TCA9517DGKR

AUD EXP

INA226 x5

TEMP SENSOR
TMP102AIDRLT

CLOCK
CDCM6208V2RGZT

ETHERNET PHY
KSZ9031RNXCA

RJ45 LEDs x2

TOUCH SCREEN & LCD

HDMI
SiI9022ACNU

DSS BUFFER x2

CLOCK
CDCM6208V2RGZT

LDO3_1V8,+1.8V,90mA

NOTE:

REGULATORS

DEVICES

Multiple powered device

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TEXAS INSTRUMENTS

MISTRAL

Title	POWER FLOW DIAGARAM	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
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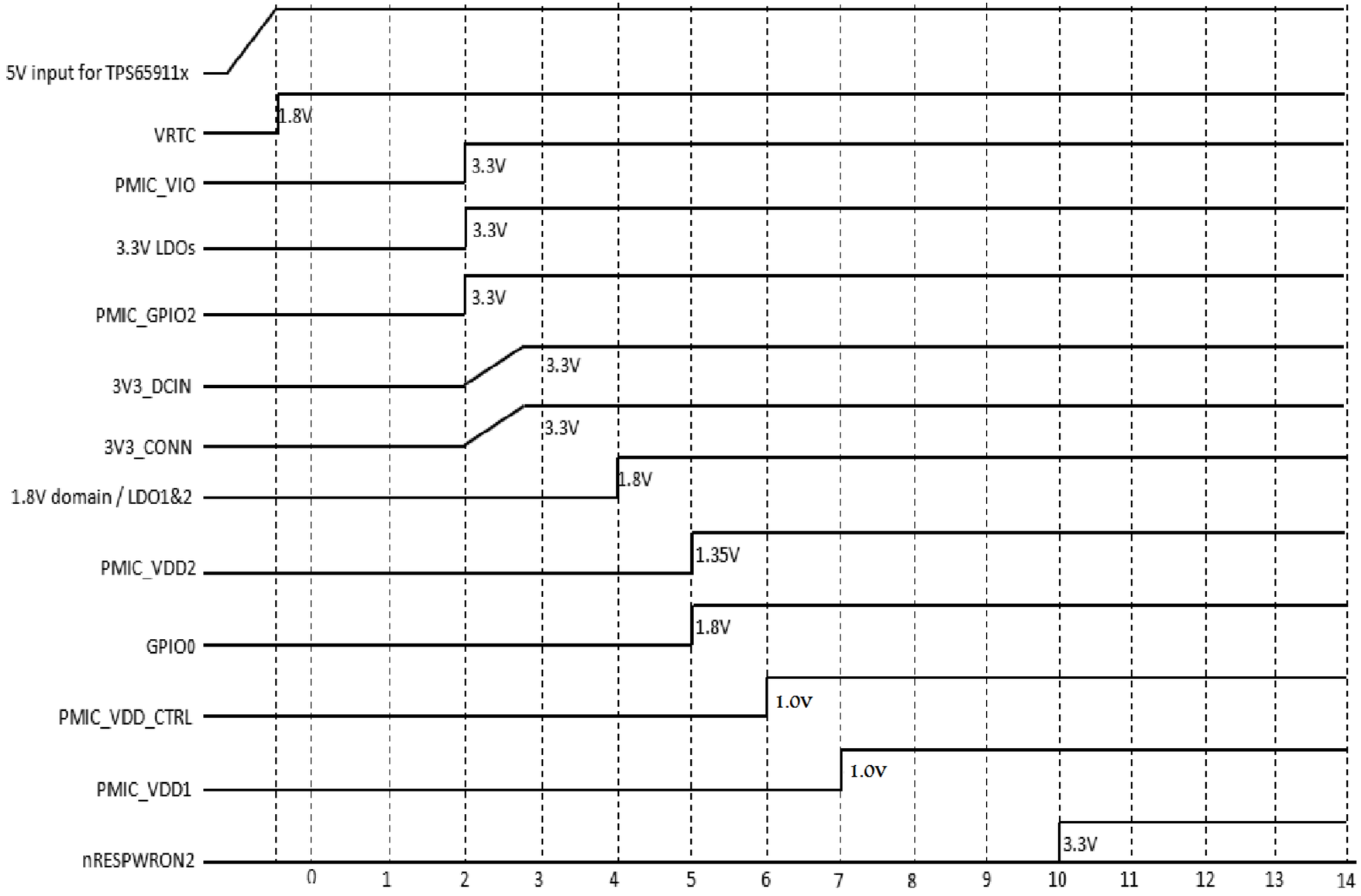
BOARD POWER UP SEQUENCE



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Title	BOARD POWER SEQUENCE DIAGARAM	
Size: C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev: A
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SoC I2C ADDRESS TABLE

I2C0 DEVICES	7 BIT ADDRESS
EEPROM	0x50 (PAGE 0)
	0x51 (PAGE 1)
BMC	0x0 (PROGRAMMABLE)
SERIAL EXPANSION CONNECTOR	TBD
PCIe CONNECTOR	TBD

I2C2 DEVICES	7 BIT ADDRESS
PMIC	0x2D (GENERAL PURPOSE) TBD
	0x12 (VOLTAGE SCALING) TBD

I2C1 DEVICES	7 BIT ADDRESS
HDMI TRANSMITTER	0x3B
	0x3F
	0x62
AUDIO CODEC	0x1B
TOUCH SCREEN	0x70 (WRITE)
	0x71 (READ)
I/O EXPANDER	0x20
TEMPERTURE SENSOR	0x48
MLB CONNECTOR	TBD
AUDIO EXPANSION CONNECTOR	0x4A,0x4B,0x4C,0x4D
CLOCK GENREATOR (CS2000CP-CZZ)	0x4F

BMC I2C ADDRESS TABLE

BMC I2C0 DEVICES	7 BIT ADDRESS
PMBUS	0x40 : 3.3V
	0x41 : CVDD
	0x42 : CVDD1
	0x43 : 1.8V
	0x44 : DVDD_DDR

BMC I2C1 DEVICES	7 BIT ADDRESS
PROCESSOR	ALWAYS MASTER

McASP CONFIGURATION

McASP PORT	DEVICES	DEVICE MODE
McASP0	DAC- PCM1690 x2	SLAVE
	I2S HEADER	MASTER
McASP1	ADC- PCM1865 x2	SLAVE
McASP2	HDMI TRANSMITTER (DEFAULT)	SLAVE
	AUDIO CODEC AIC3106	
	DIR9001(DEFAULT)	MASTER
	COM8	

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Title: I2C TREE

Size: C | Variant Name = Proj PROC046 002 OPN# EVMK2GXS | Rev: A

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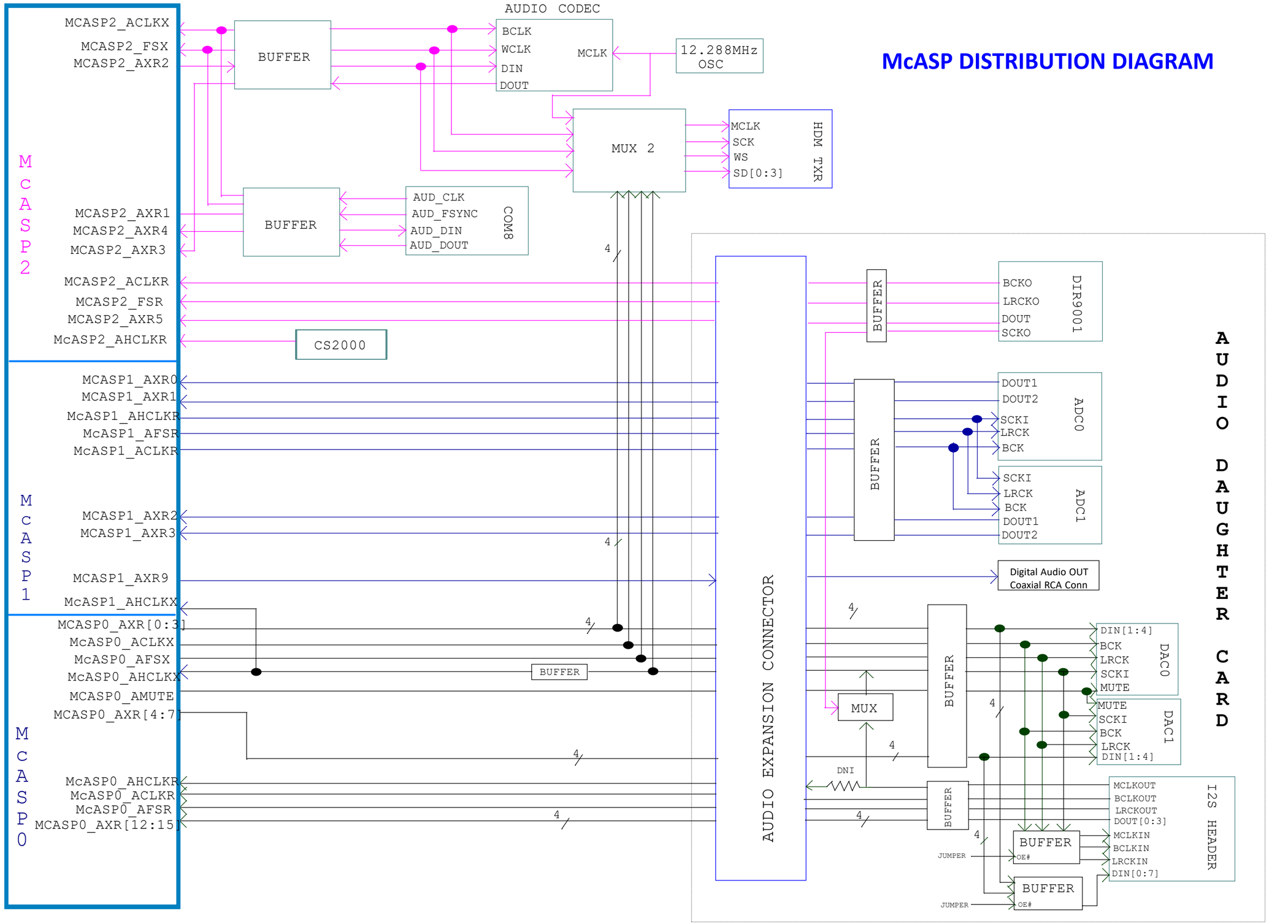
McASP DISTRIBUTION DIAGRAM



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TEXAS INSTRUMENTS

MISTRAL

Title	MCASP TREE	
Size: C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev: A
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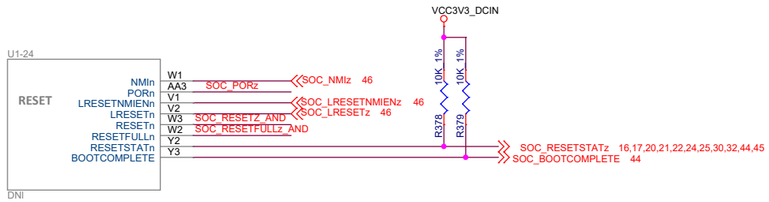
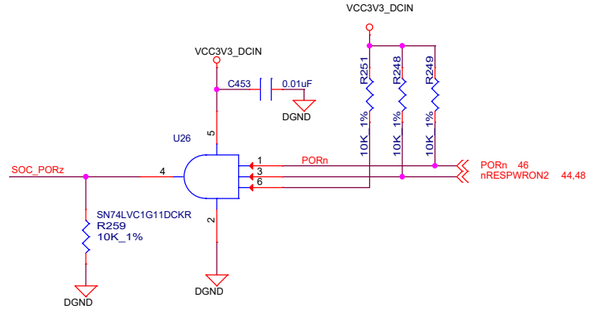
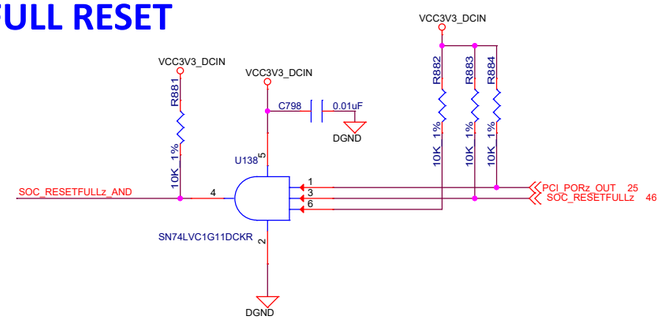
SoC RESET



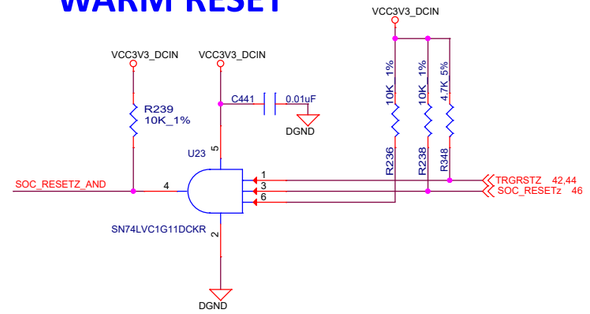
POWER ON RESET



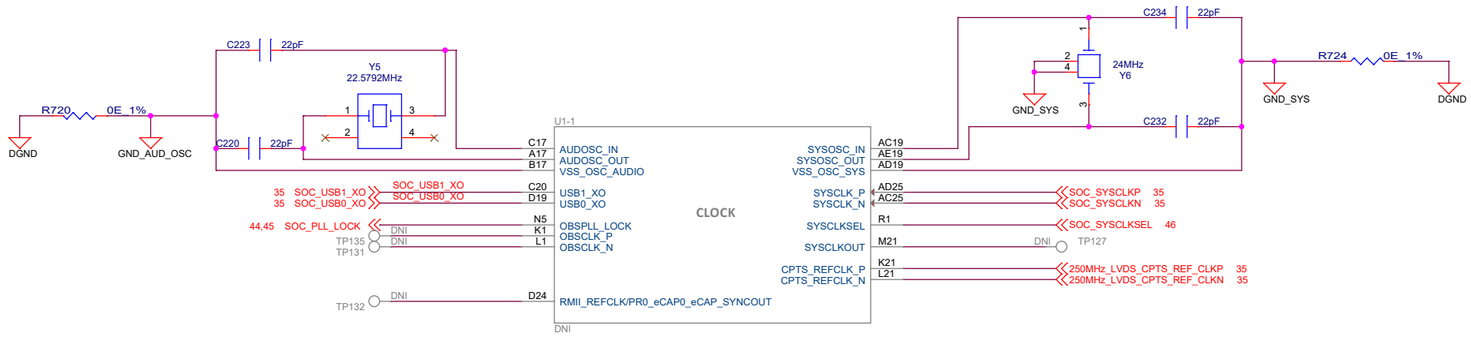
FULL RESET



WARM RESET



SoC CLOCK



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Title	SOC CLOCK & RESET	
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev
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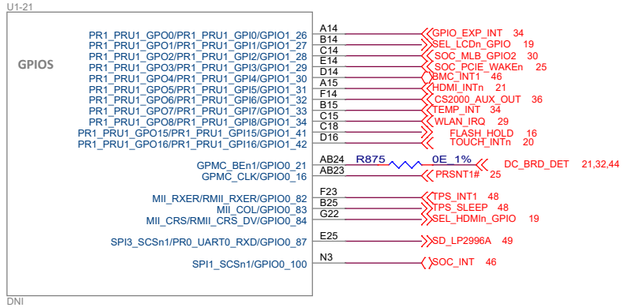
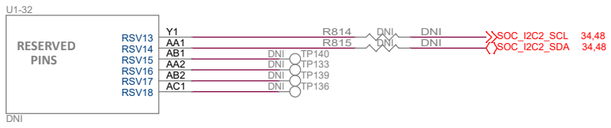
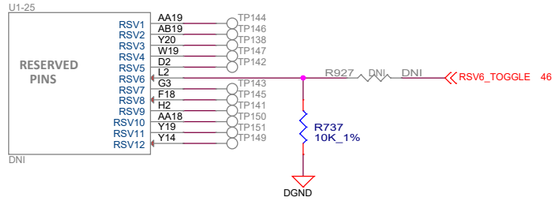
SoC GPIOs



RESERVED PINS





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Title	SOC GPIO, SMART REFLEX & TEST OUT	
Size: C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev: A
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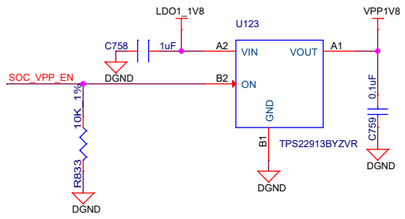
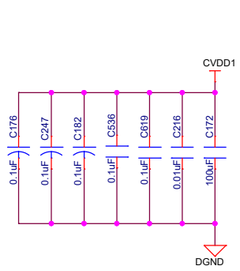
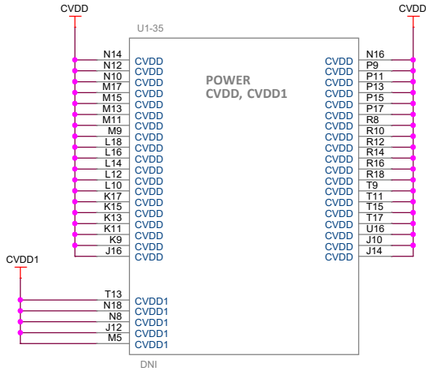
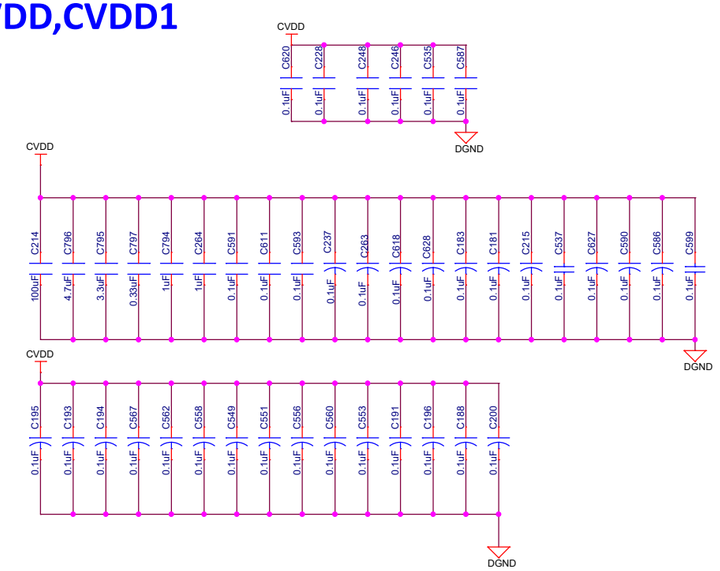
VPP POWER GENERATION



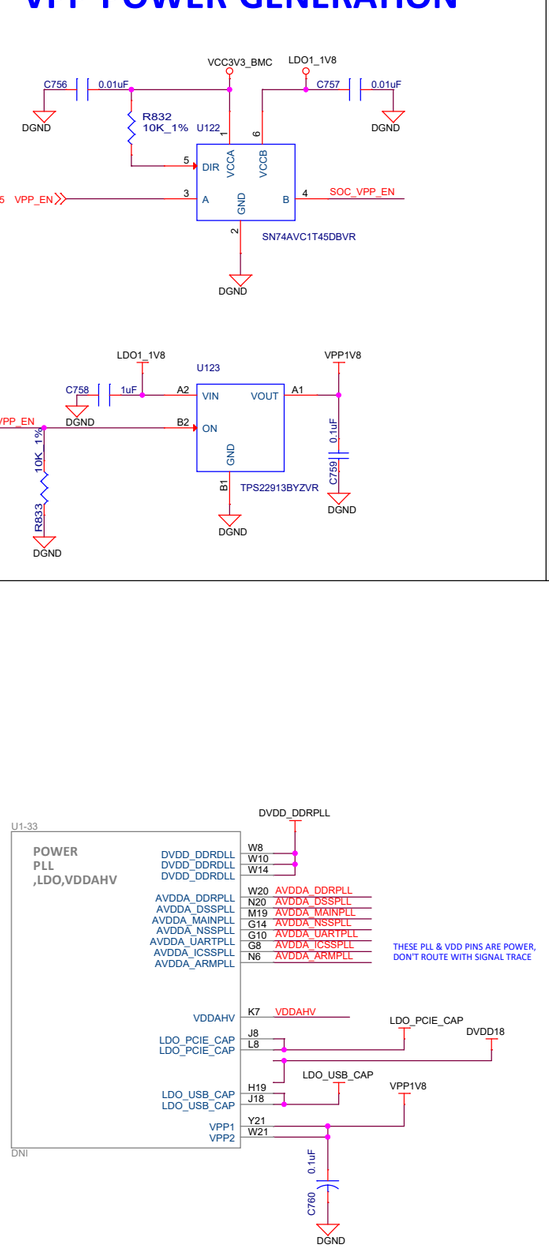
SoC POWER -CVDD,CVDD1

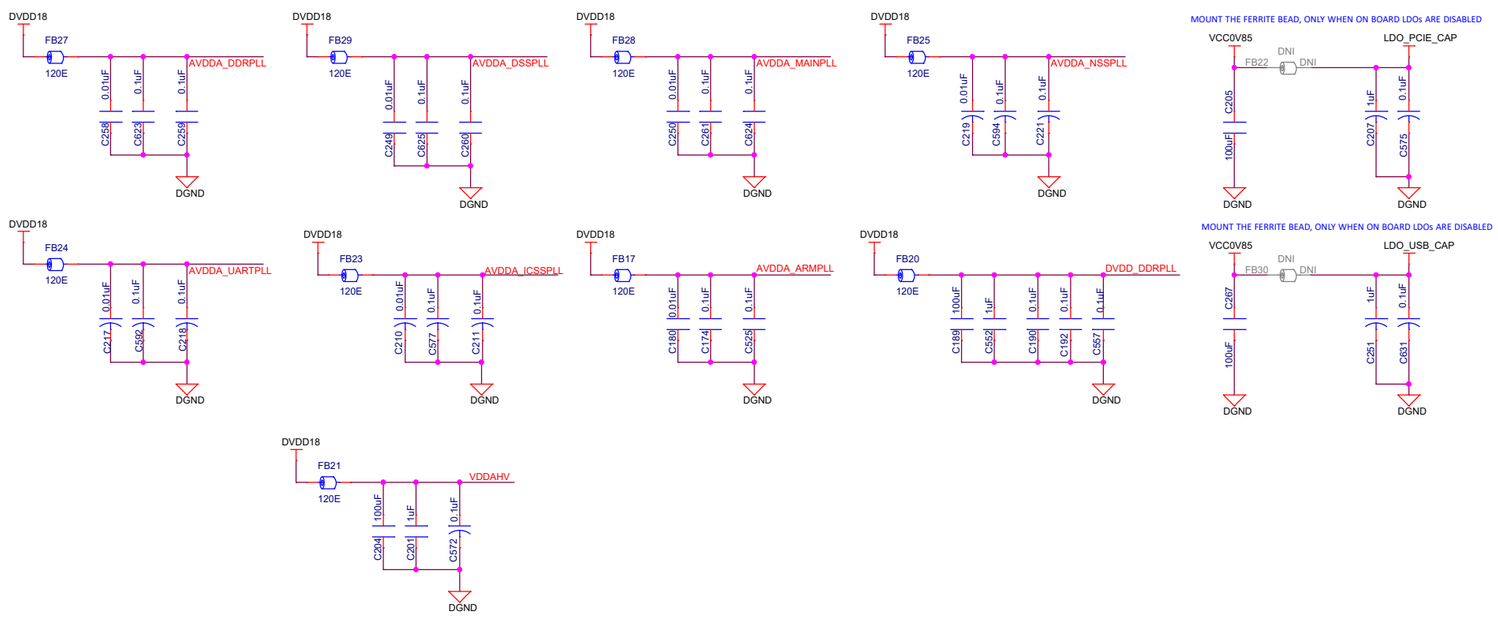


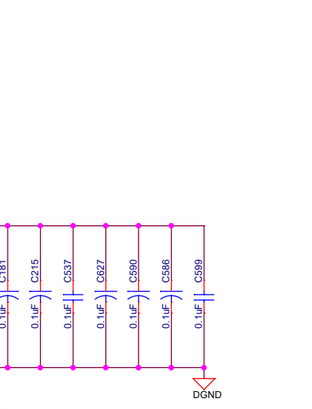




SoC POWER - PLL







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Title	SOC POWER - CVDD,CVDD1 & PLL	
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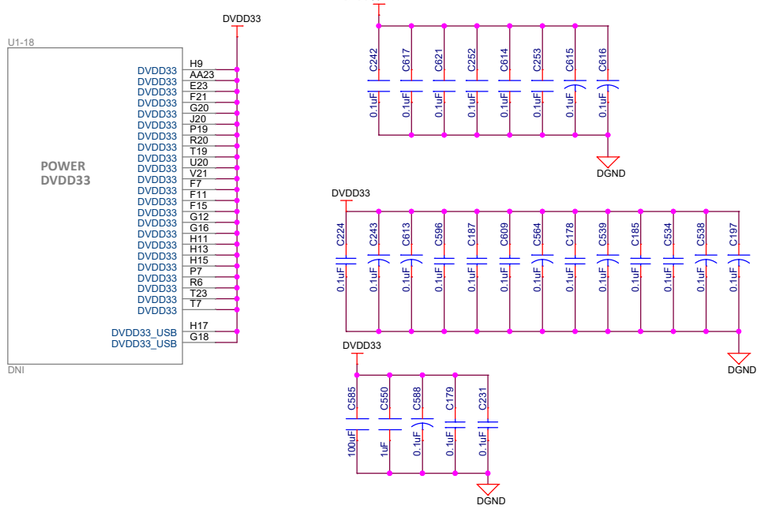
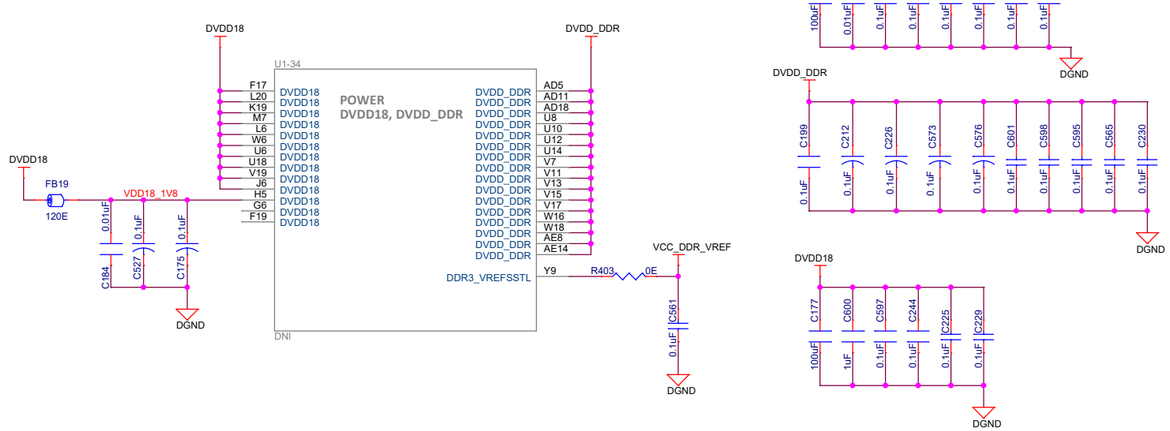
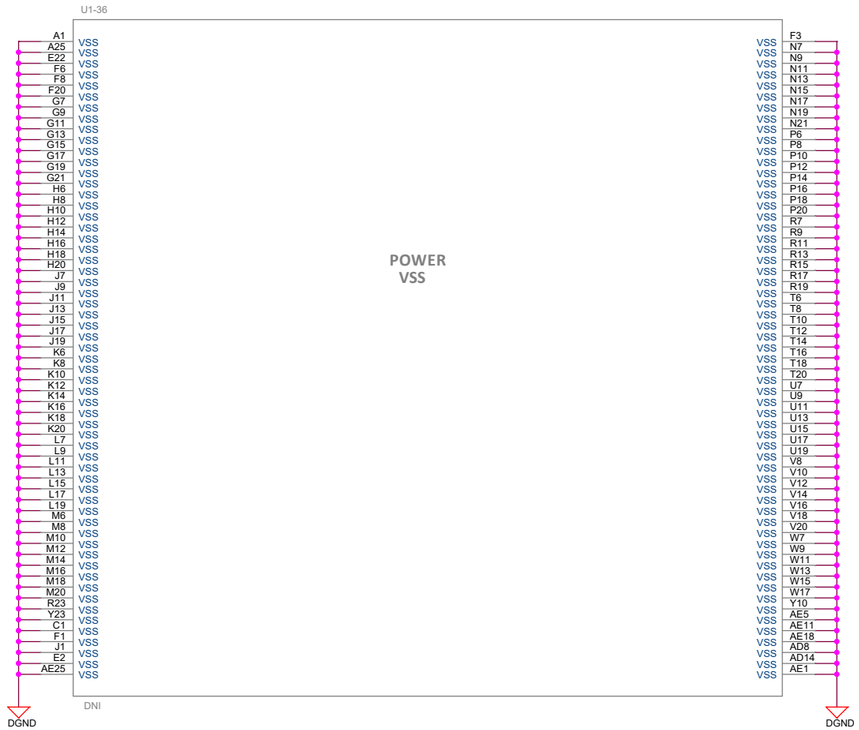
SoC POWER - DVDD33



SoC POWER - DVDD18, DVDD_DDR



SoC POWER - VSS



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Title	SOC POWER - DVDD33, DVDD18, DVDD_DDR & VSS		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
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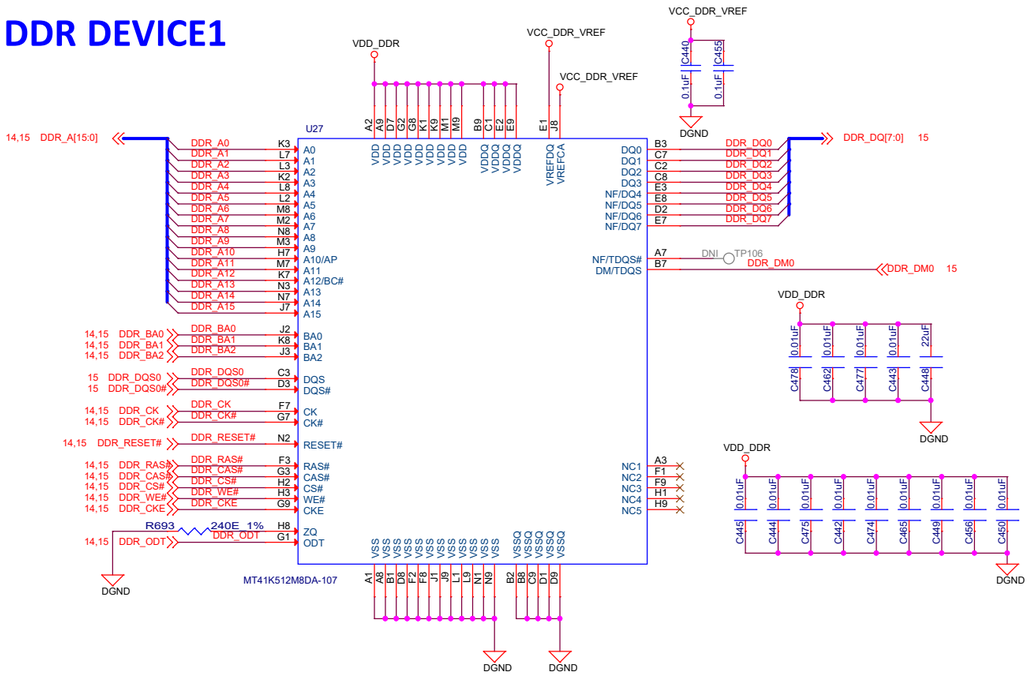
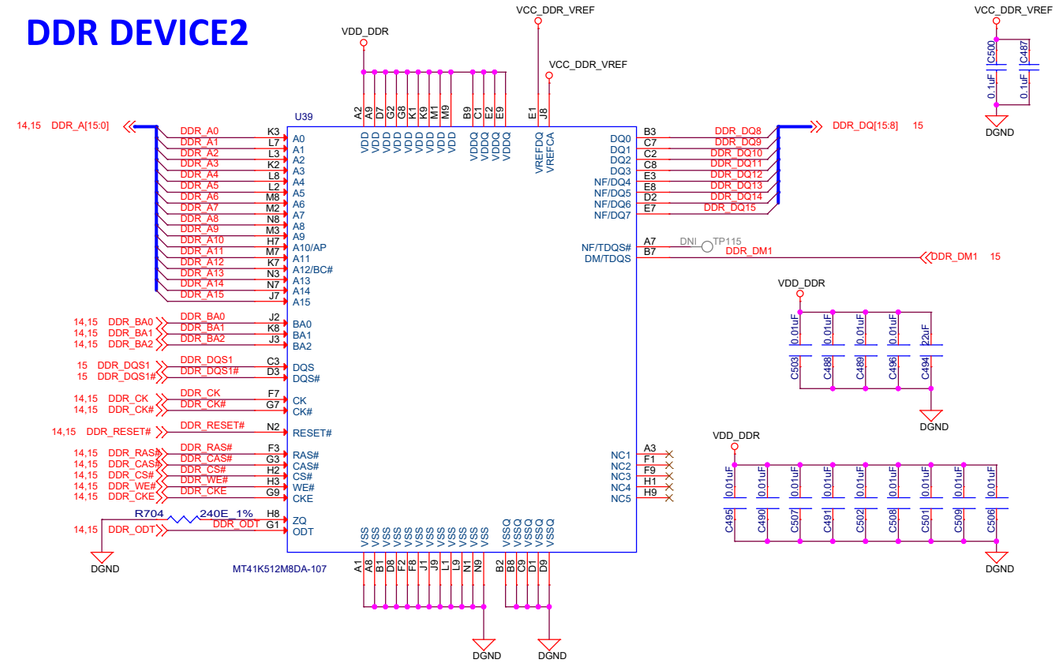
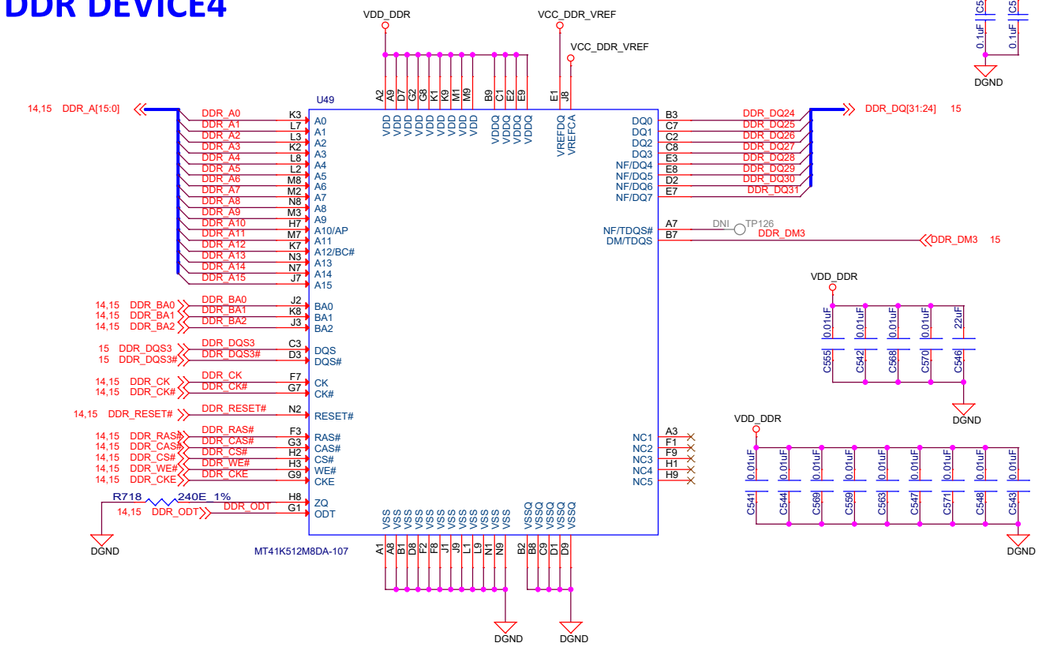
DDR DEVICE1



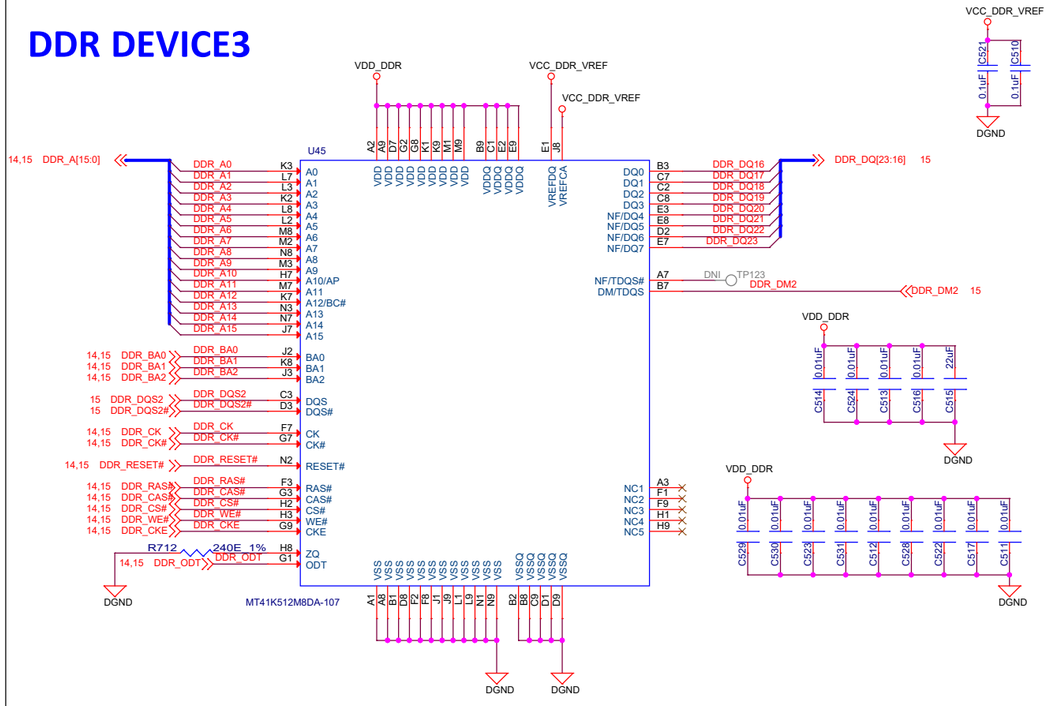
DDR DEVICE2



DDR DEVICE4



DDR DEVICE3



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Title	DDR3L DEVICES	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
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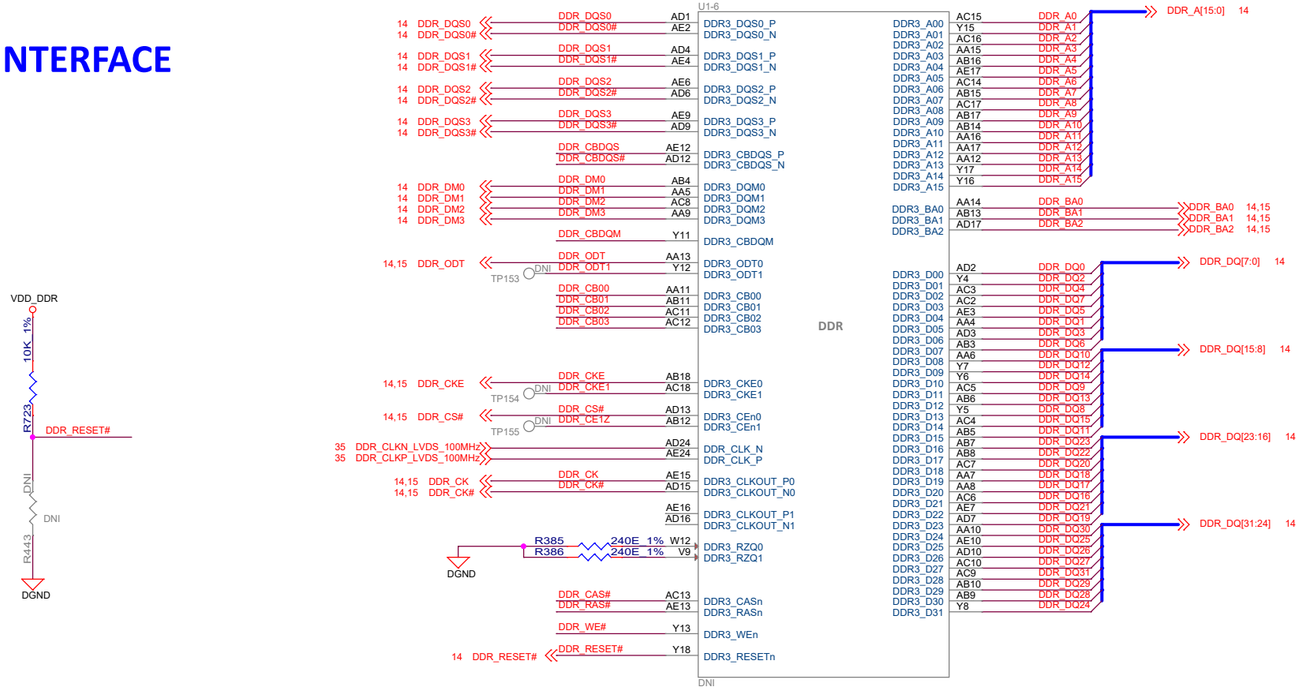
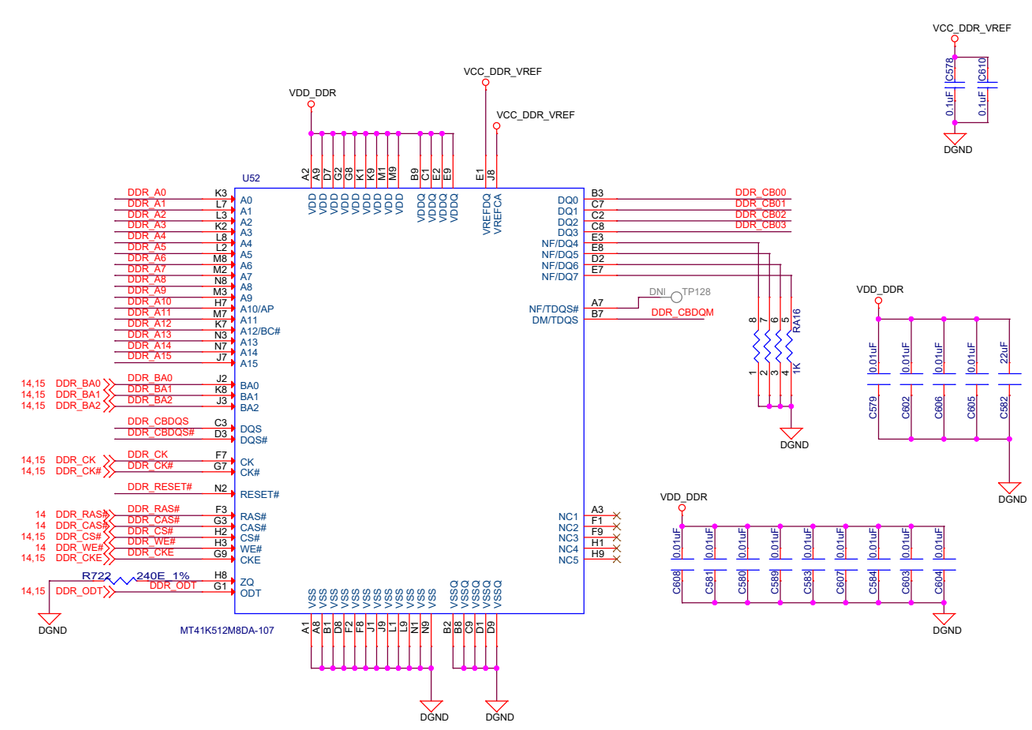
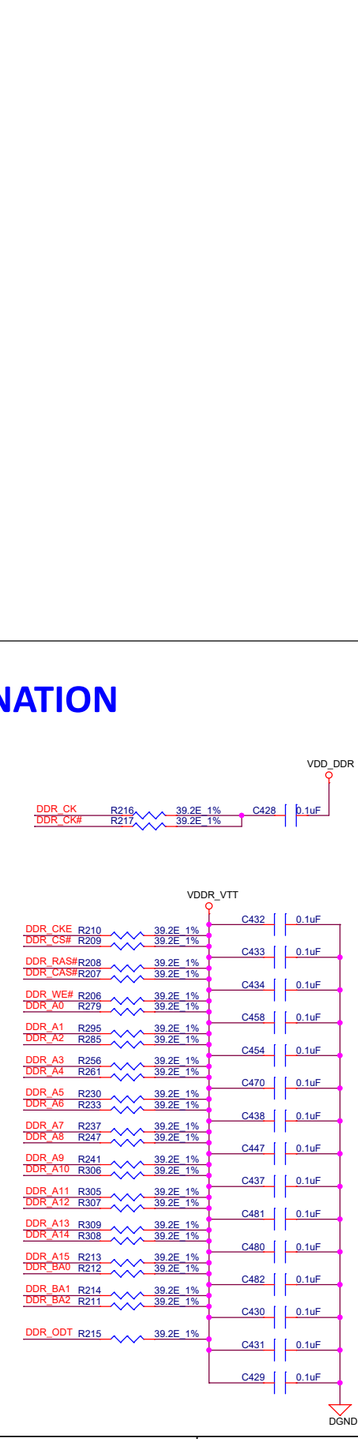
SoC DDR INTERFACE



ECC DEVICE



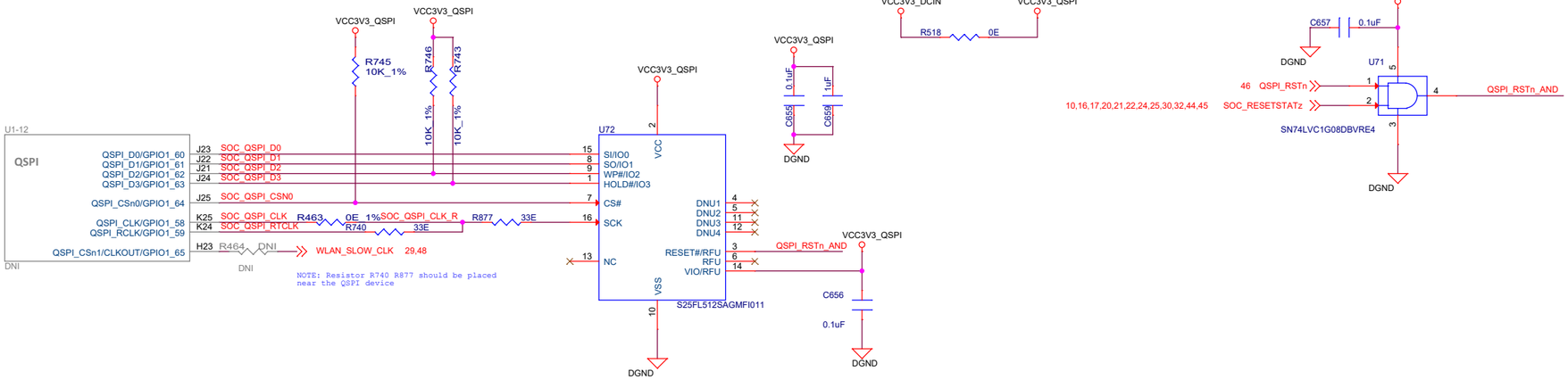
DDR TERMINATION



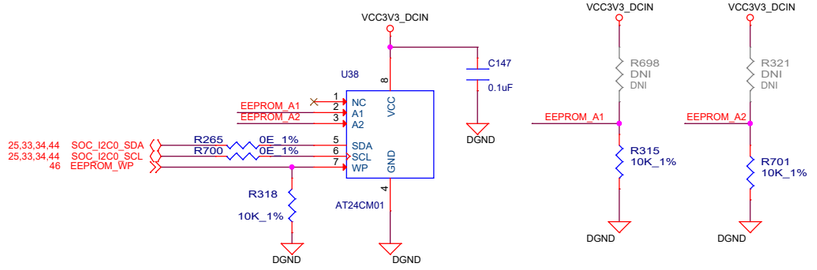
Designed for TI by Mistral Solutions Pvt Ltd

Title	SOC DDR MODULE, ECC & TERMINATION	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 15 of 51	

QSPI

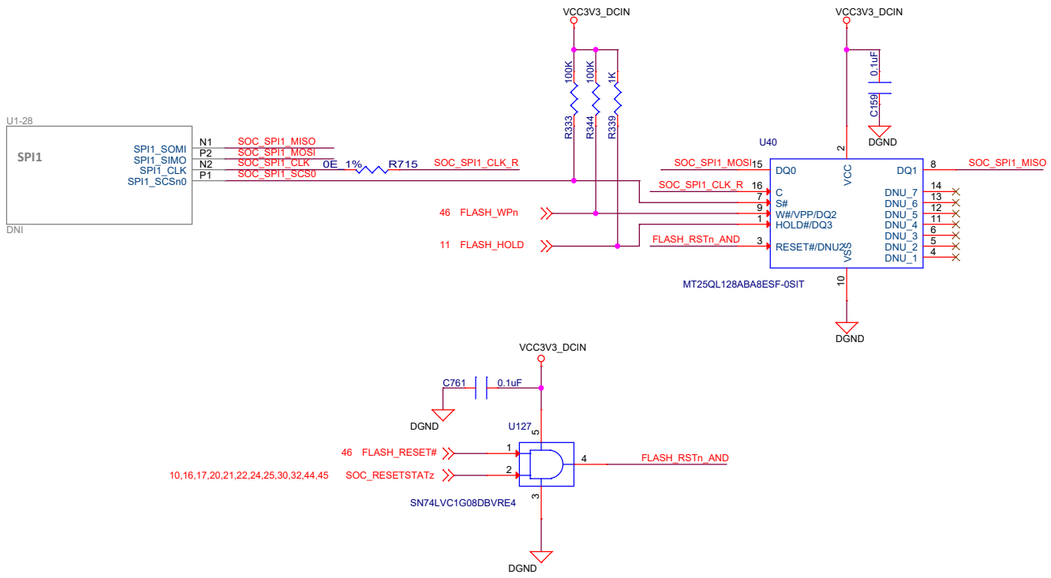


I2C EEPROM FLASH



I2C ADDRESS: 0x50 (PAGE 0), 0x51 (PAGE 1)

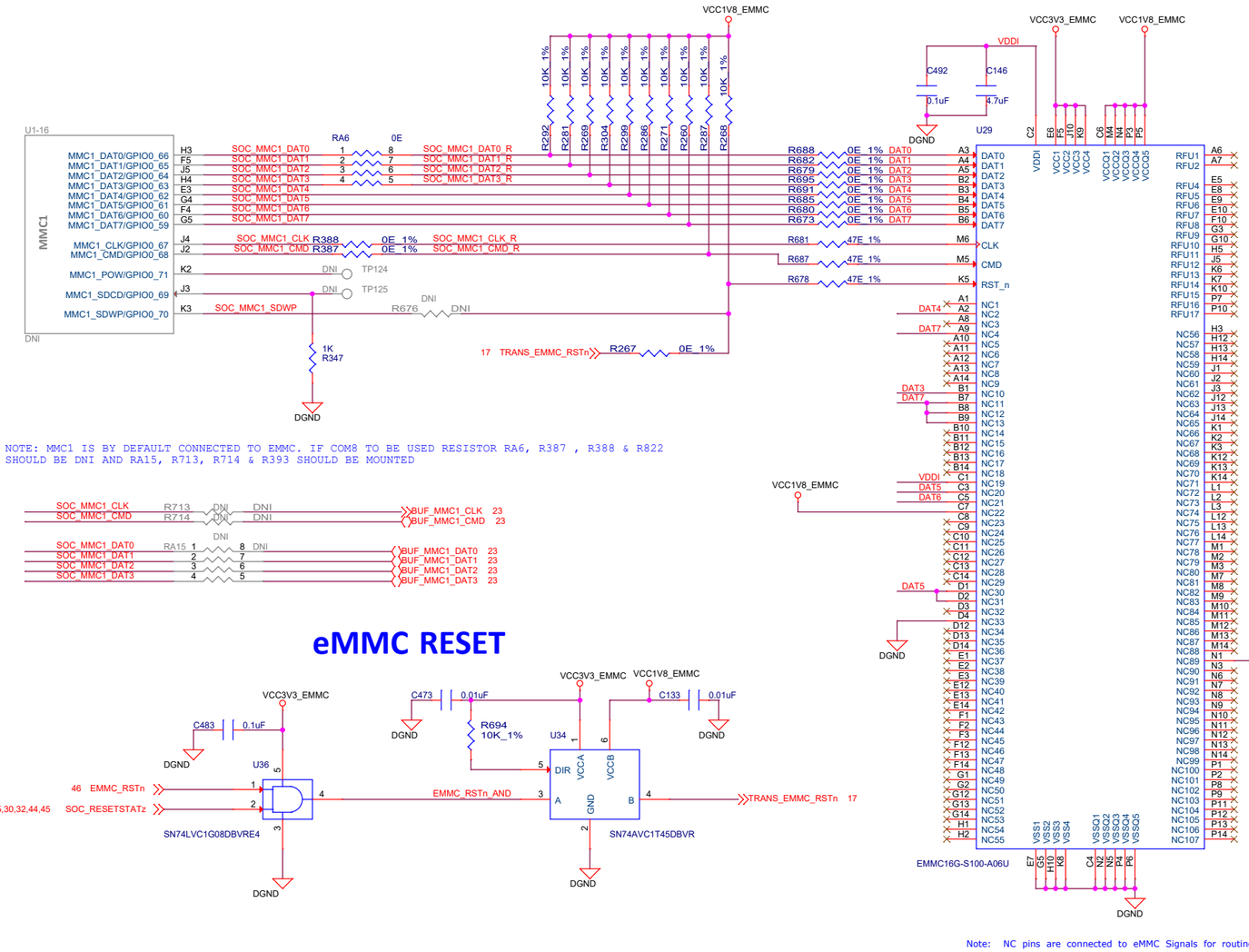
SPI NORFLASH



Designed for TI by Mistral Solutions Pvt Ltd

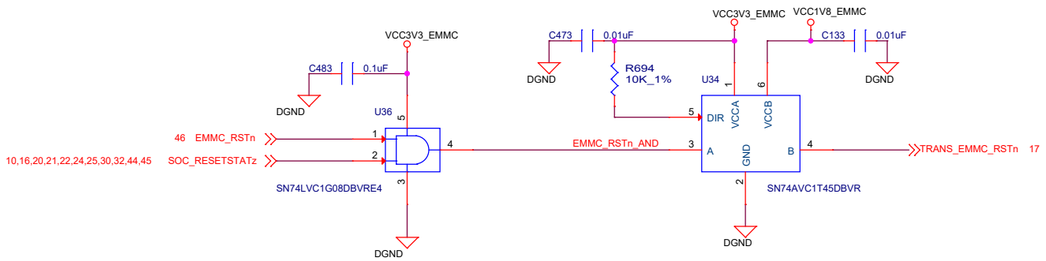
Title	QSPI, I2C & SPI EEPROM FLASH	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 16 of 51	

eMMC FLASH

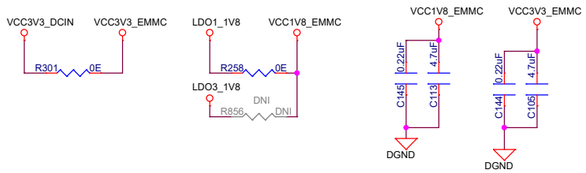


NOTE: MMC1 IS BY DEFAULT CONNECTED TO EMMC. IF COM8 TO BE USED RESISTOR RA6, R387 , R388 & R822 SHOULD BE DNI AND RA15, R713, R714 & R393 SHOULD BE MOUNTED

eMMC RESET



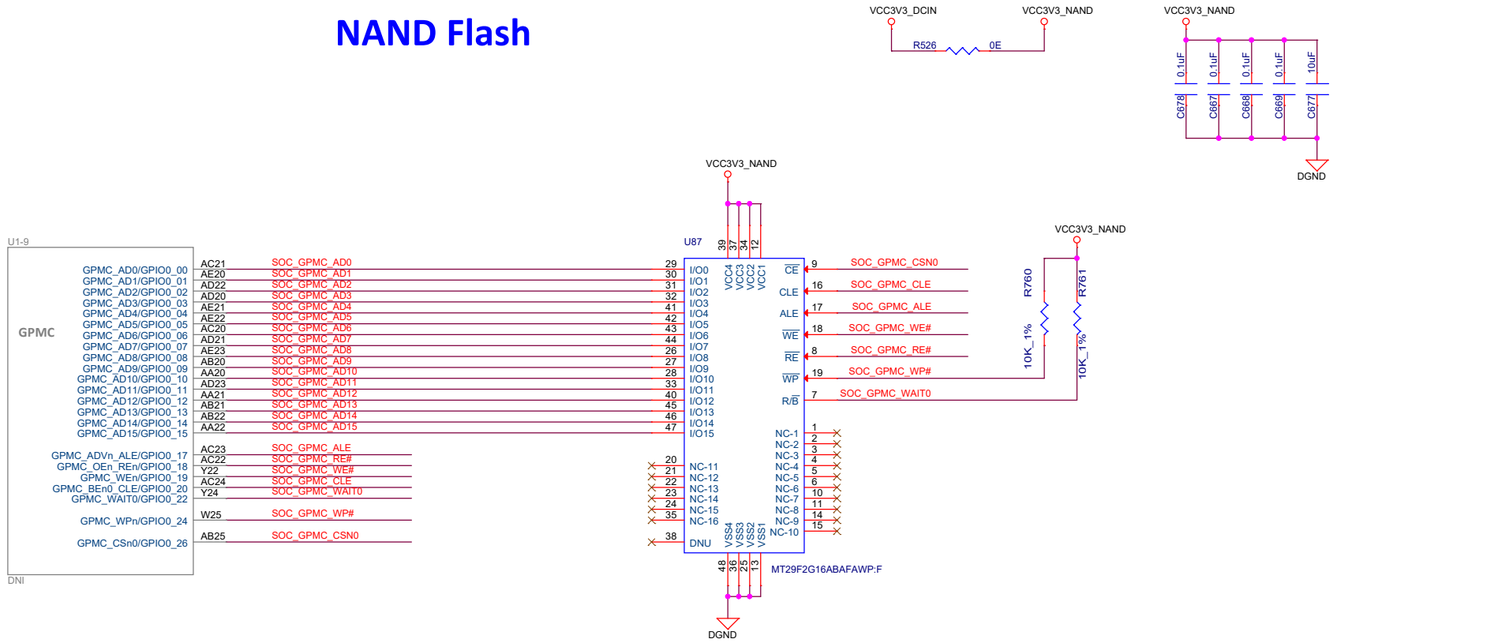
Note: NC pins are connected to eMMC Signals for routing



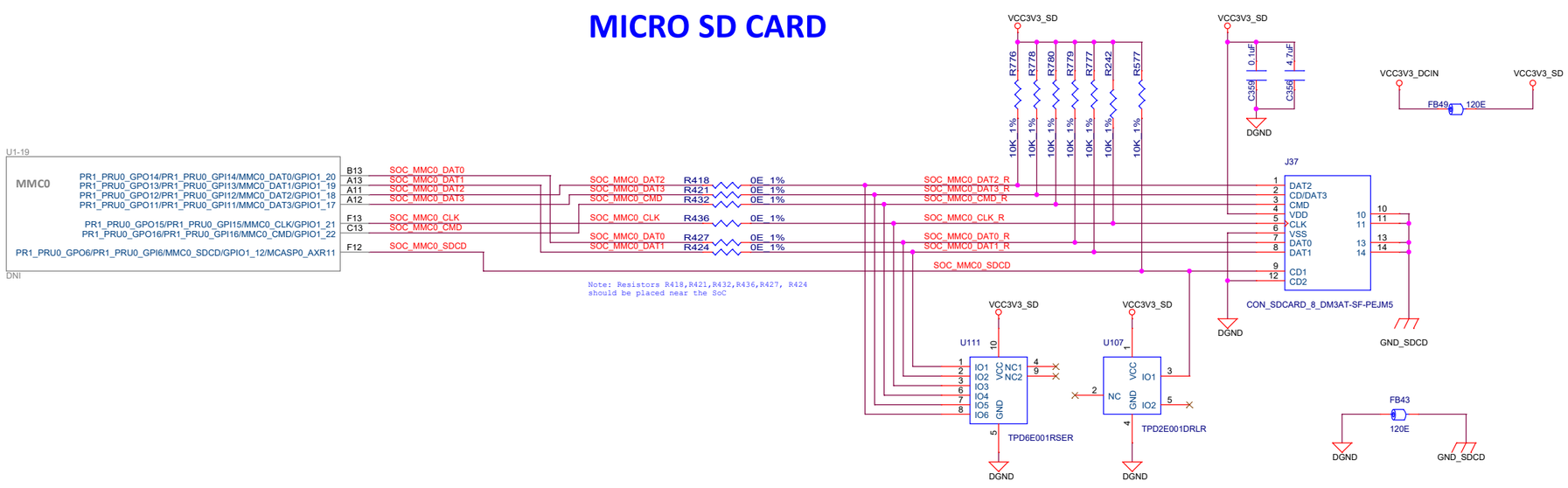
Designed for TI by Mistral Solutions Pvt Ltd

Title	EMMC FLASH		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
Date:	Friday, February 02, 2018	Sheet 17 of 51	

NAND Flash



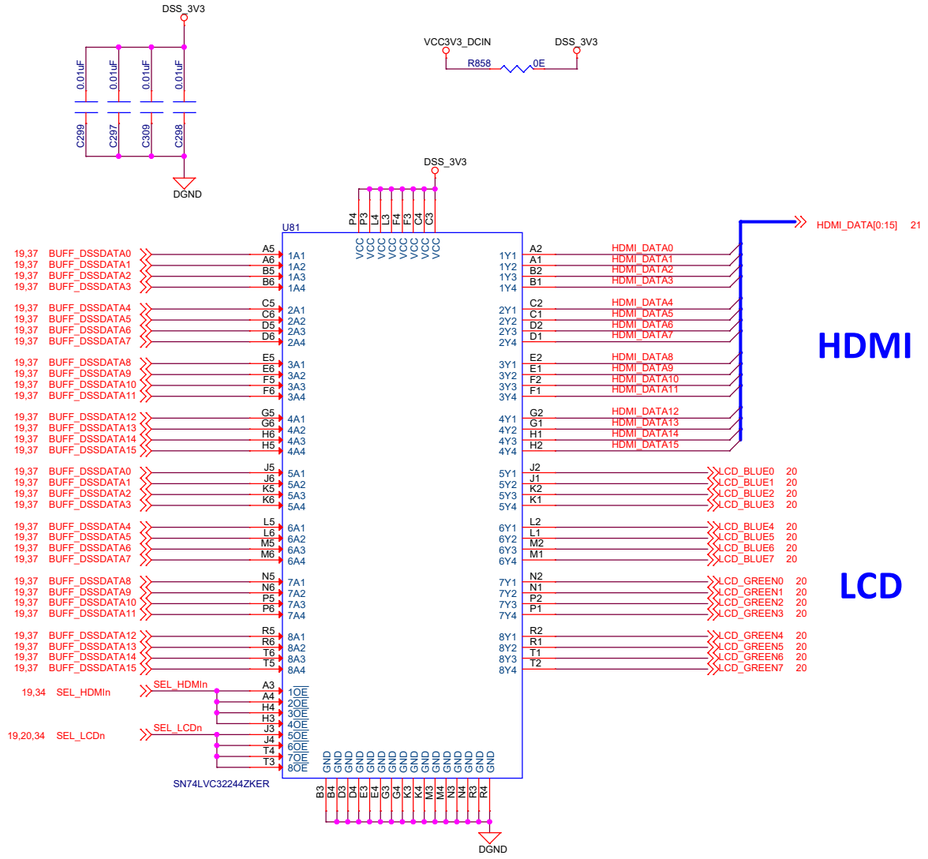
MICRO SD CARD



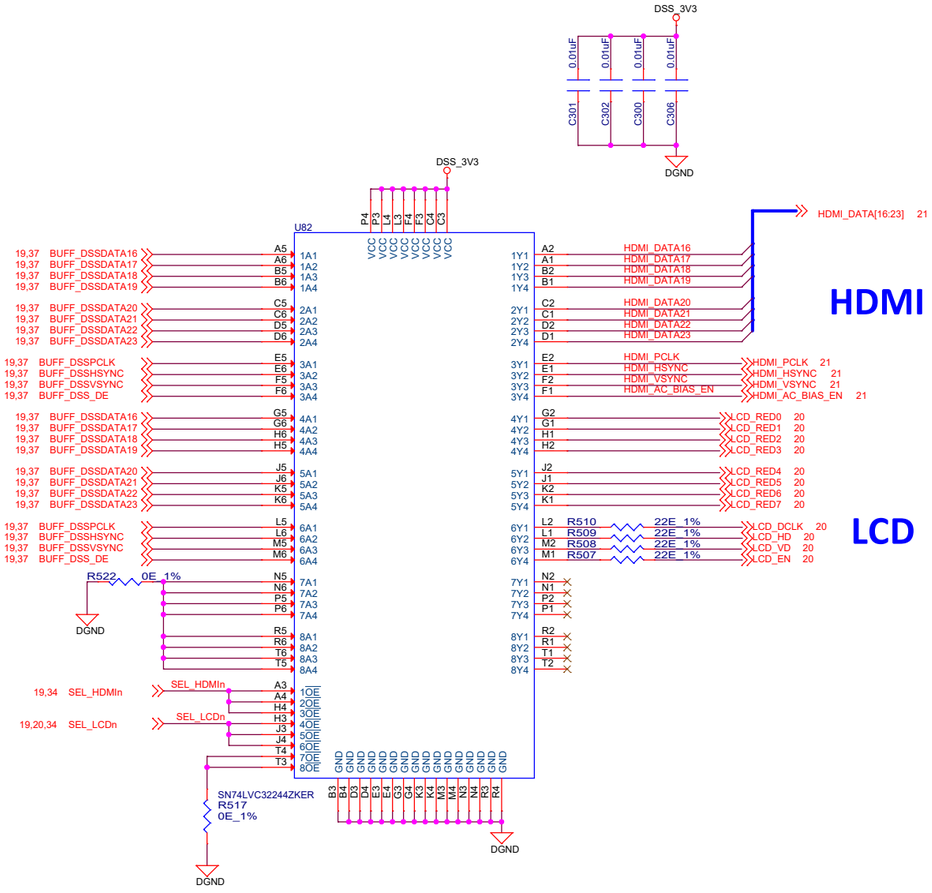
Designed for TI by Mistral Solutions Pvt Ltd

Title	NAND FLASH & SD CARD	
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev
C		A
Date:	Friday, February 02, 2018	Sheet 18 of 51

DSS BUFFER 1



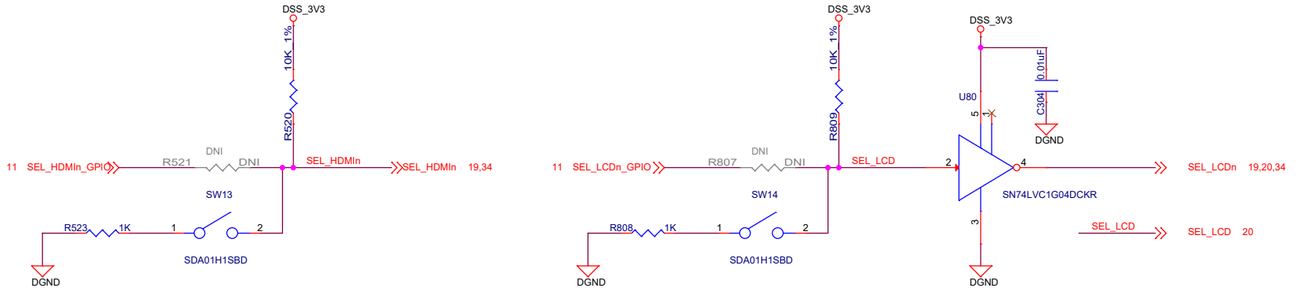
DSS BUFFER 2



SELECTION LCD AND HDMI

HDMI/LCD SWITCH SELECTION

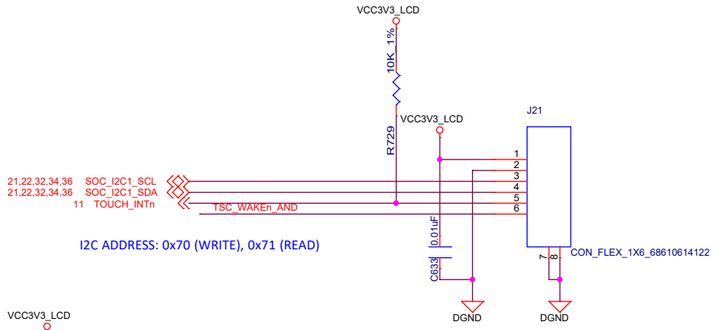
SW14	SW13	DEVICE
ON	ON	HDMI ON
OFF	OFF	LCD ON
OFF	ON	NOT VALID
ON	OFF	NOT VALID

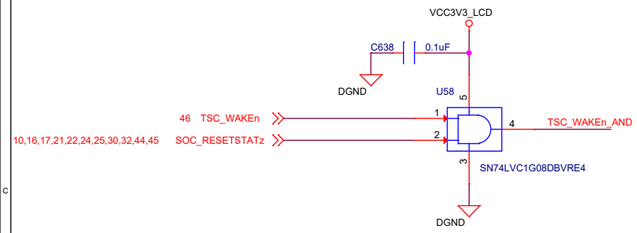


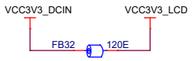
Designed for TI by Mistral Solutions Pvt Ltd

Title	DSS BUFFER SECTION		
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev A
Date: Friday, February 02, 2018		Sheet 19 of 51	

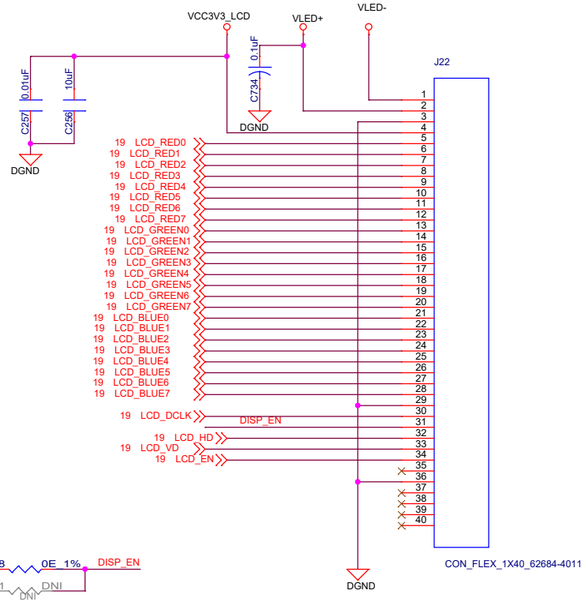
TOUCH SCREEN CONNECTOR



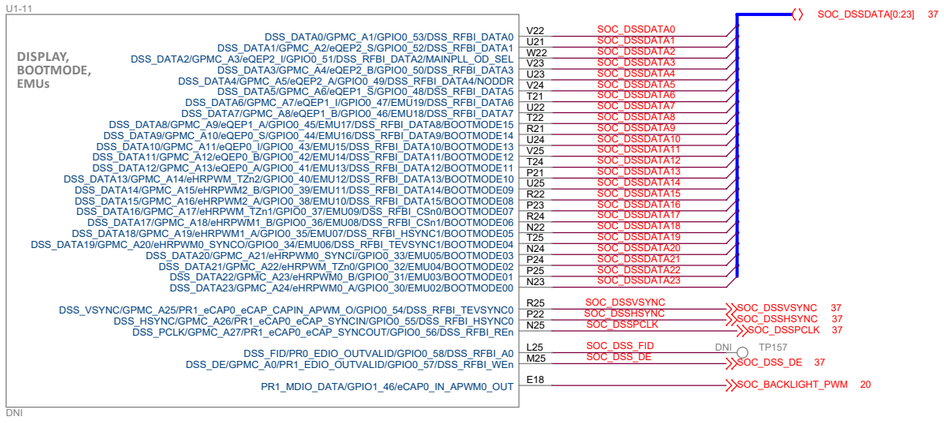




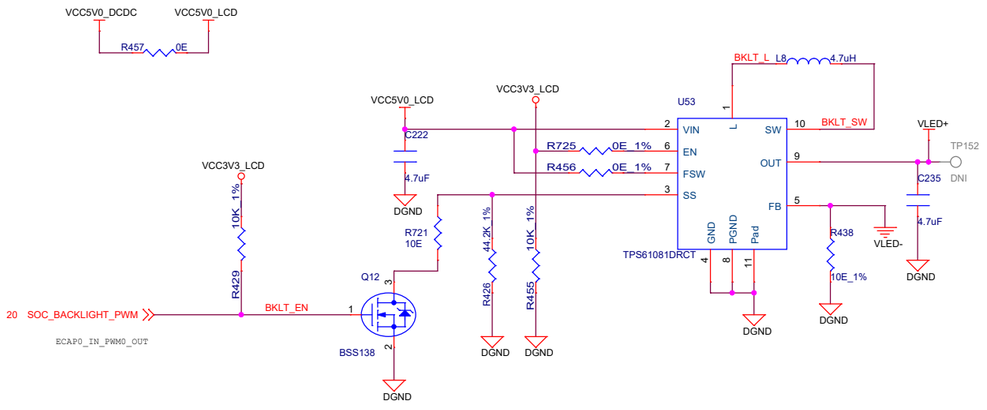
LCD CONNECTOR



SOC DSS



BACKLIGHT POWER



Designed for TI by Mistral Solutions Pvt Ltd

Title	LCD & TOUCH SCREEN	
Size: C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev: A
Date: Friday, February 02, 2018	Sheet 20 of 51	

HDMI TRANSMITTER

HDMI Audio Line select i on bet ween McASP0 & McASP2

DC_BRD_DET	AUDIO LINE SELECTED
L	McASP2
H	McASP0

I2C ADDRESS: 0x3B, 0x3F, 0x62

HDMI 1.2V GENERATION

HDMI ESD DEVICE

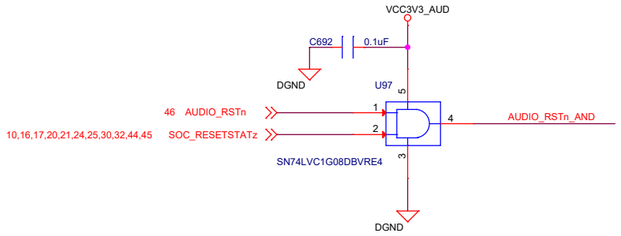
NOTE:
TPD12S016PWR has integrated pullup or pulldown resistors on the I2C and HPD lines hence no external pullup or pulldown required.

HDMI CONNECTOR

Designed for TI by Mistral Solutions Pvt Ltd

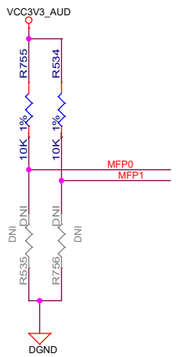
Title	HDMI TRASMITTER	
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev
C		A
Date:	Friday, February 02, 2018	Sheet 21 of 51

AUDIO CODEC RESET

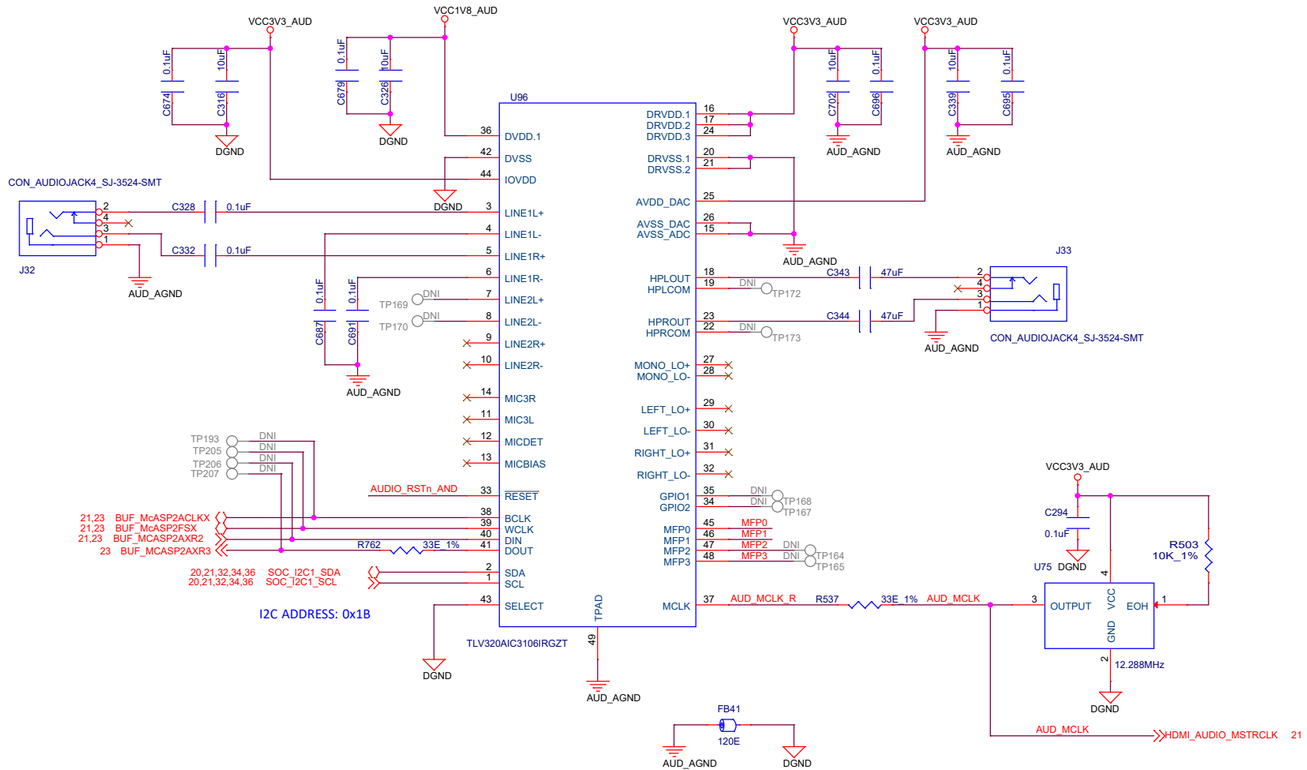


CODEC I2C ADDRESS SELECTION

MFP0	MFP1	ADDRESS
L	L	1A
L	H	1B
H	L	1C
H	H	1D



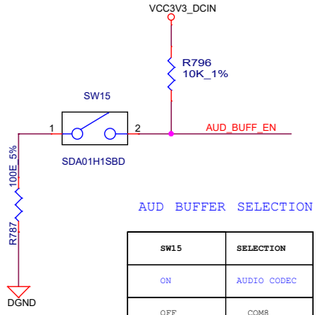
AUDIO CODEC



Designed for TI by Mistral Solutions Pvt Ltd

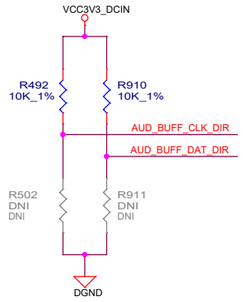
Title	AUDIO CODEC		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
Date:	Friday, February 02, 2018	Sheet 22 of 51	

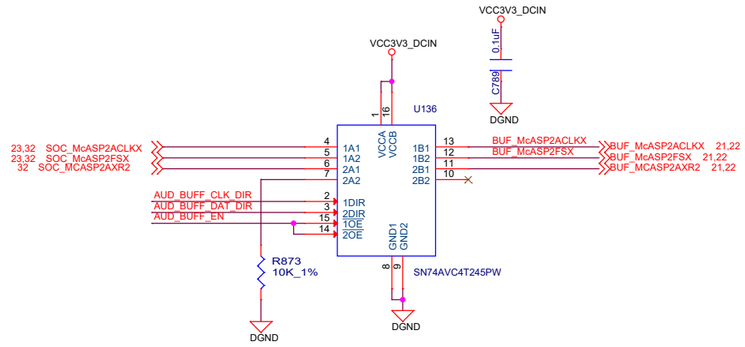
AUDIO BUFFER

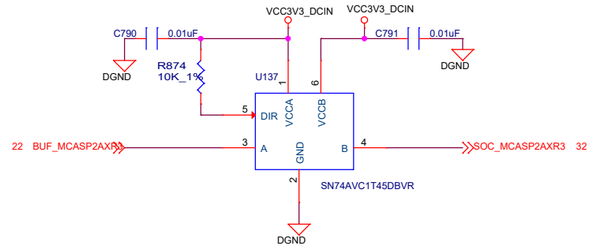


AUD BUFFER SELECTION

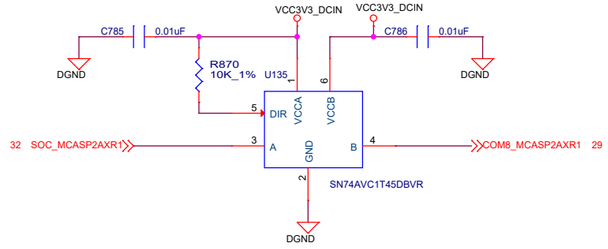
SW15	SELECTION
ON	AUDIO CODEC
OFF	COM8

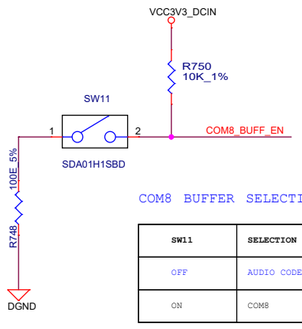






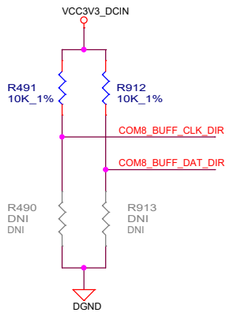
COM8 BUFFER

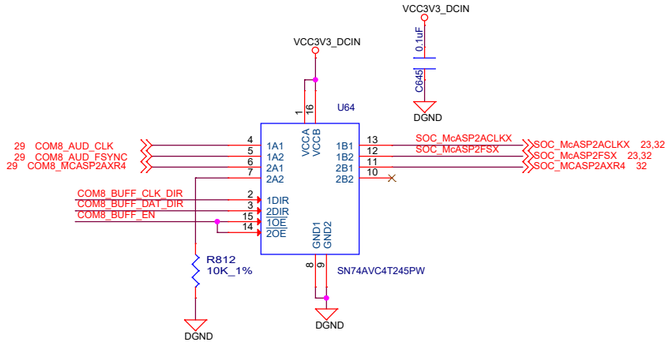




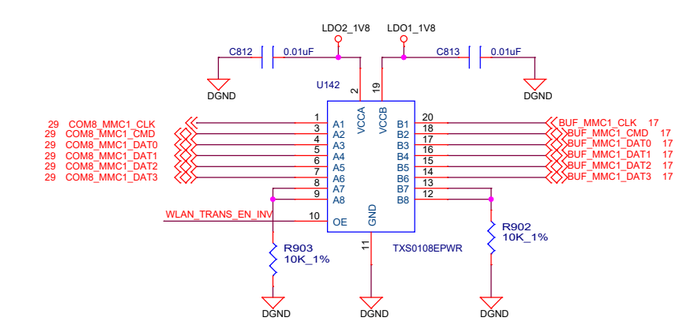
COM8 BUFFER SELECTION

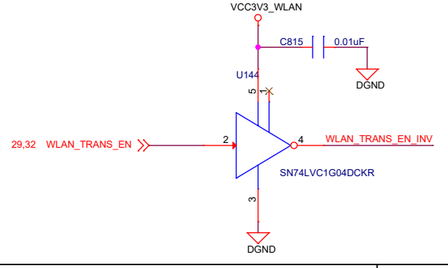
SW11	SELECTION
OFF	AUDIO CODEC
ON	COM8





COM8 SDIO BUFFER





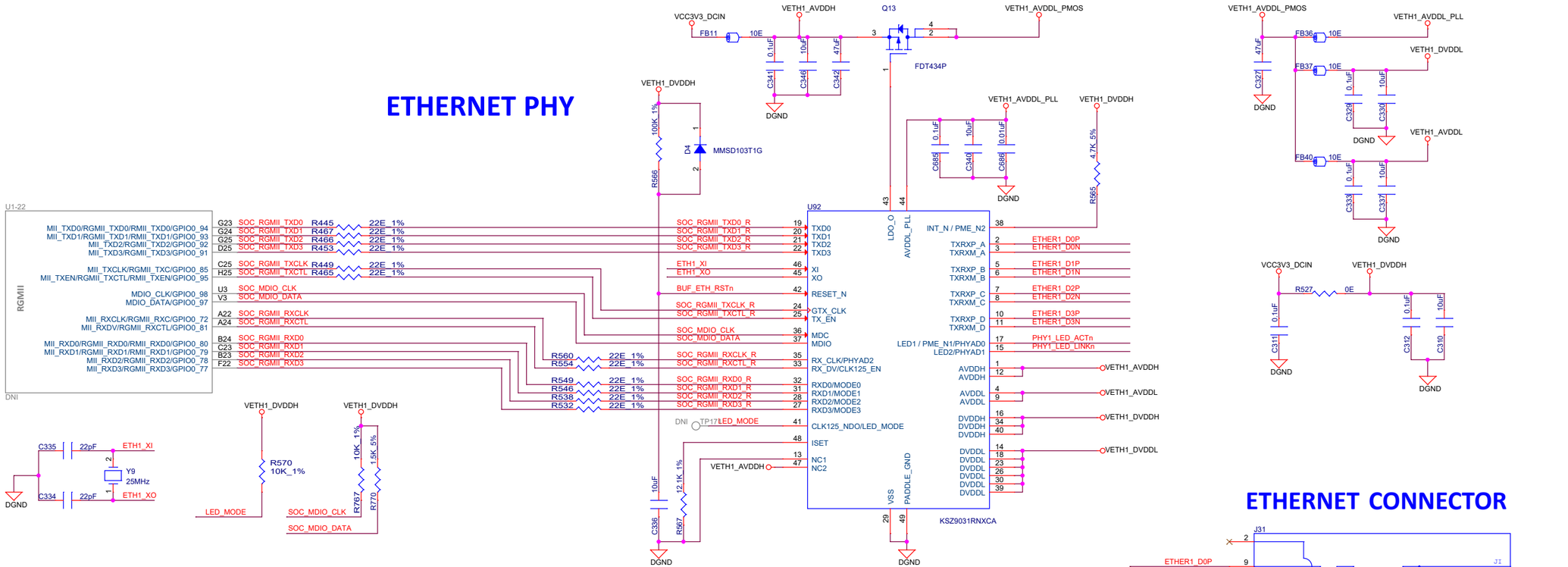
Designed for TI by Mistral Solutions Pvt Ltd

TEXAS INSTRUMENTS

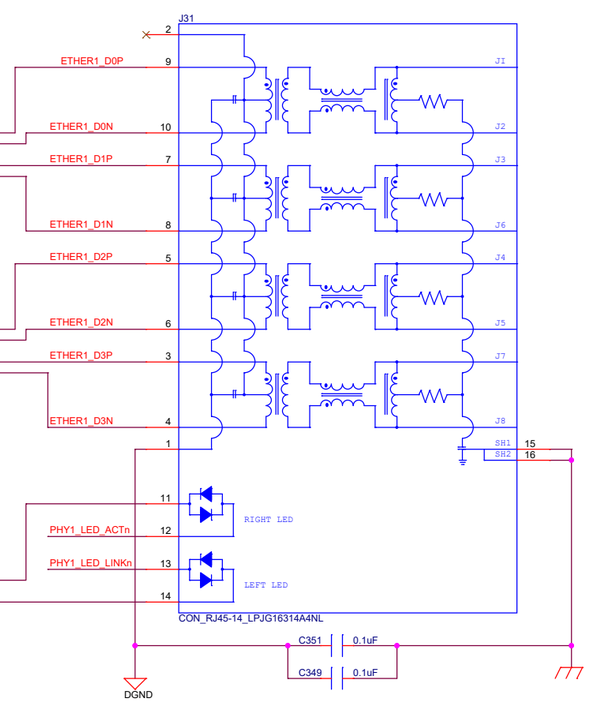
MISTRAL

Title	MCASP2 BUFFERS FOR COM8 , HDMI& AUDIO CODEC	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 23 of 51	

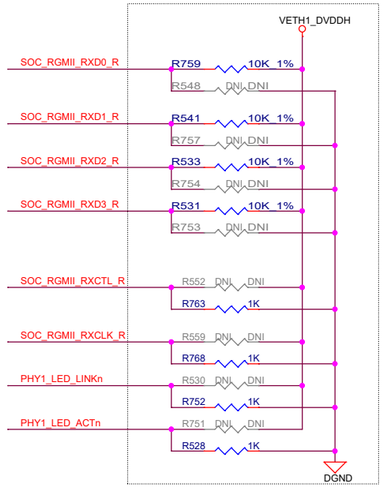
ETHERNET PHY



ETHERNET CONNECTOR

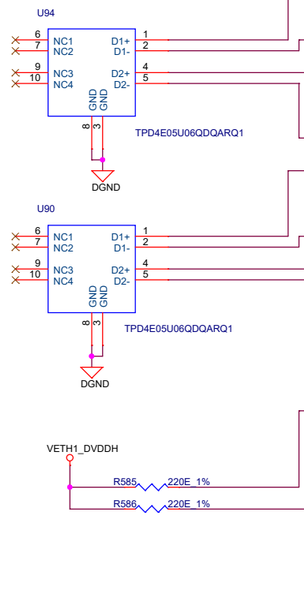


ETHERNET MODE CONFIGURATION

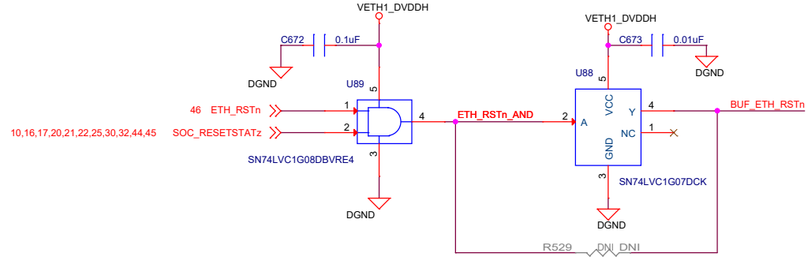


RXD[3:0]	MODE
0100	NAND TREE
0111	CHIP POWER DOWN
1100	RGMII MODE - ADVERTISE 1000 BASE-T FULL DUPLEX ONLY
1101	RGMII MODE- ADVERTISE 1000 BASE-T FULL & HALF DUPLEX ONLY
1110	RGMII MODE- ADVERTISE ALL CAPABILITIES (10/100/1000 SPEED HALF / FULL DUPLEX), EXCEPT 1000 BASE-T HALF DUPLEX
1111	RGMII MODE- ADVERTISE ALL CAPABILITIES (10/100/1000 SPEED HALF / FULL DUPLEX)

ETHERNET ESD DEVICES



ETHERNET RESET



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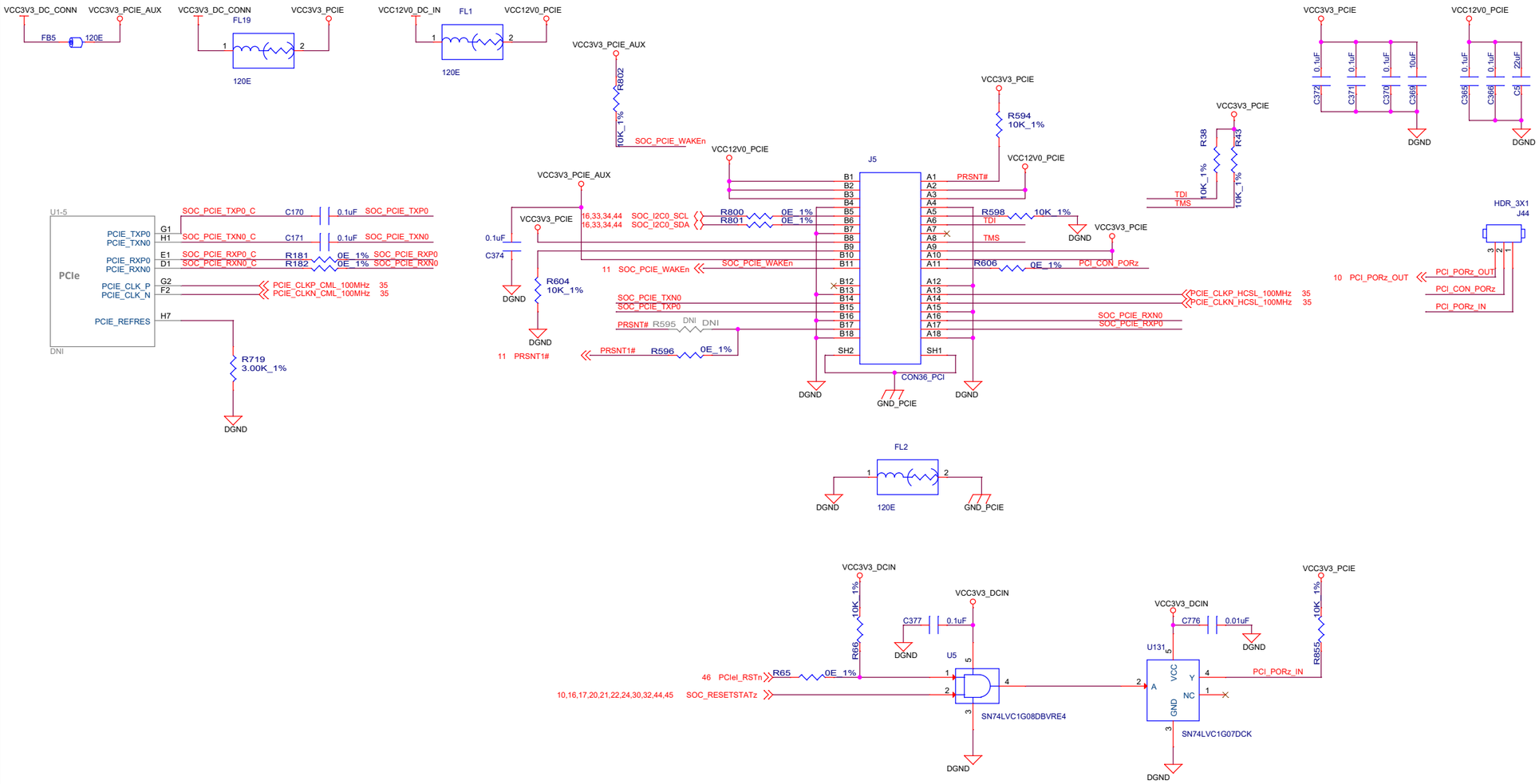
Title	ETHERNET PHY		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
Date:	Friday, February 02, 2018	Sheet 24 of 51	

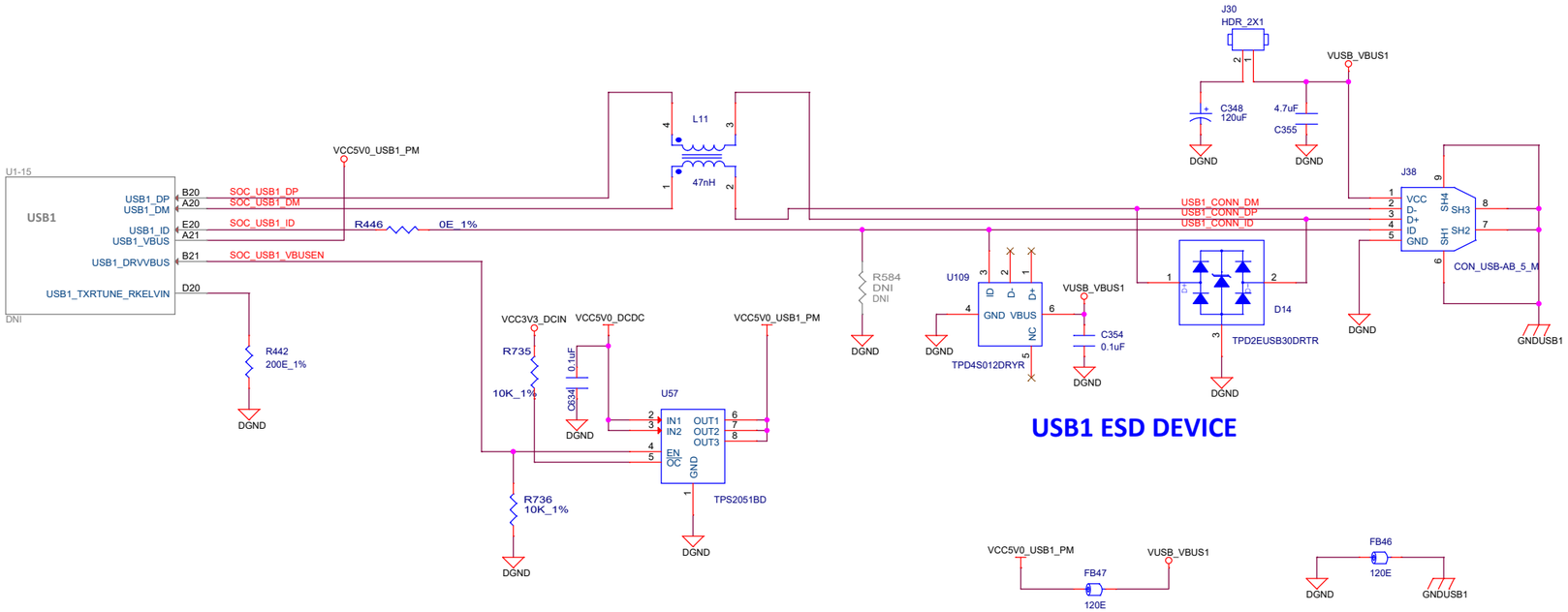
PCI EXPRESS x1 CONNECTOR



Designed for TI by Mistral Solutions Pvt Ltd

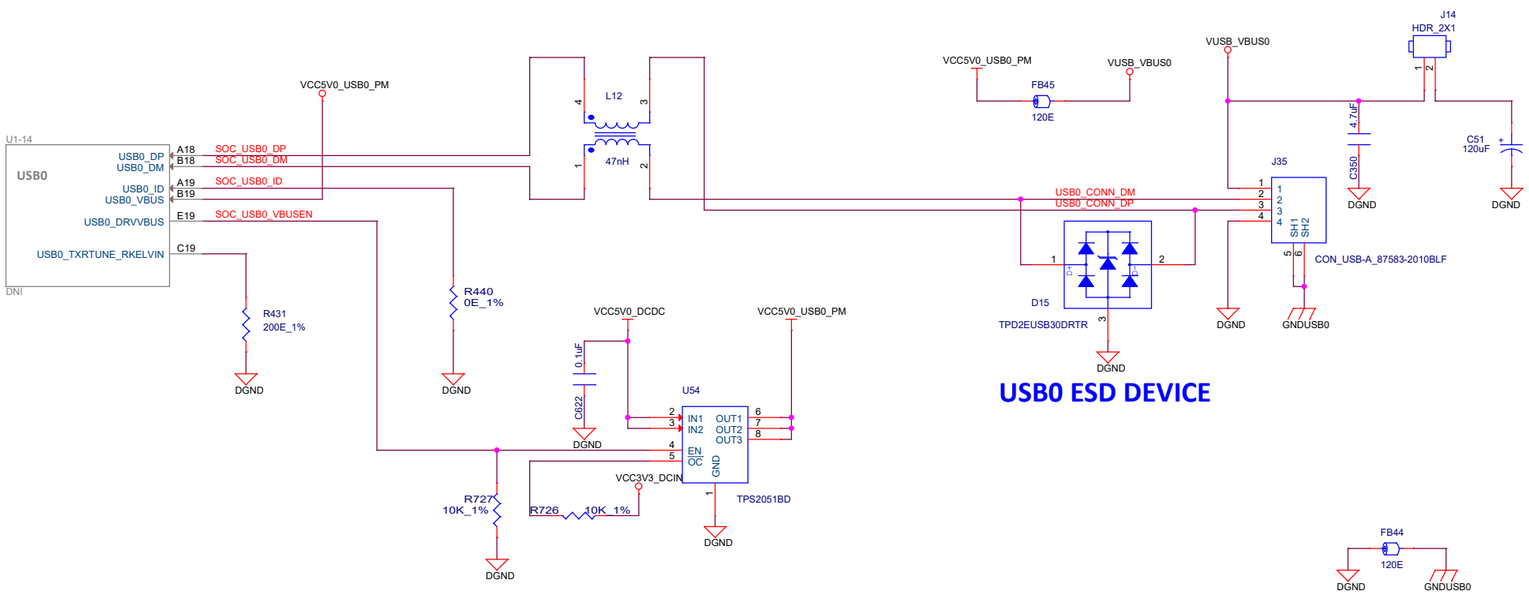
Title	PCIe x1 CONNECTOR	
Size: C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev: A
Date: Friday, February 02, 2018	Sheet 25 of 51	

USB1 DUAL ROLE



USB1 ESD DEVICE

USB0 HOST

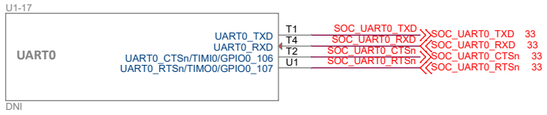


USB0 ESD DEVICE

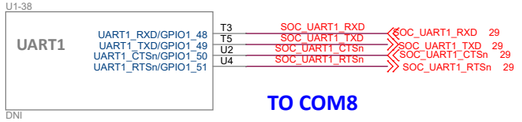
Designed for TI by Mistral Solutions Pvt Ltd

Title	USB DUAL ROLE & HOST	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 26 of 51	

SoC UART0



SoC UART1

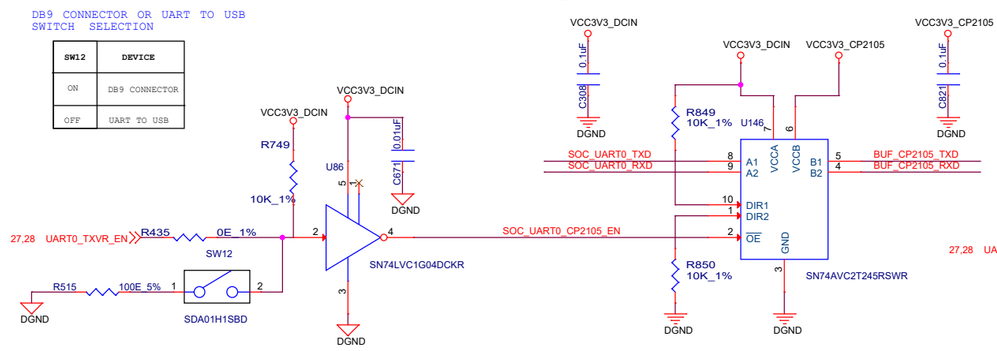


TO COM8

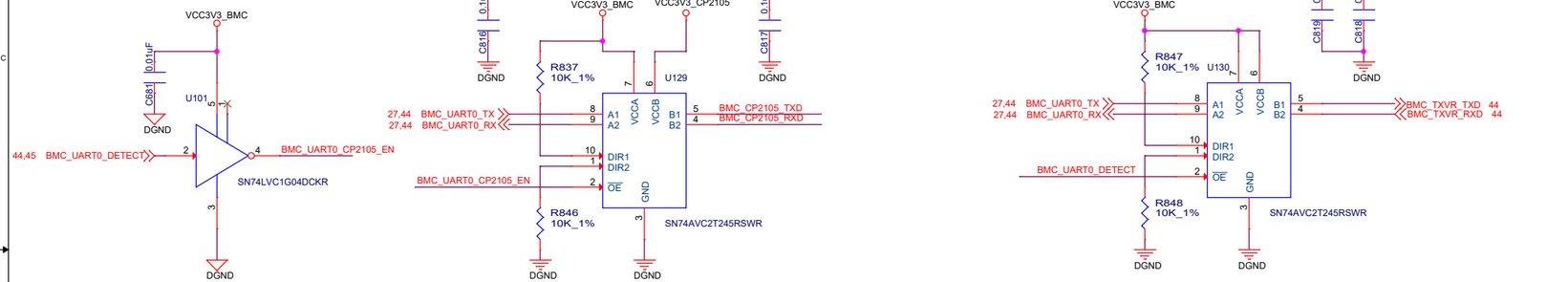
SoC_UART0 MUX: CP2105 or DB9

DB9 CONNECTOR OR UART TO USB SWITCH SELECTION

SW12	DEVICE
ON	DB9 CONNECTOR
OFF	UART TO USB



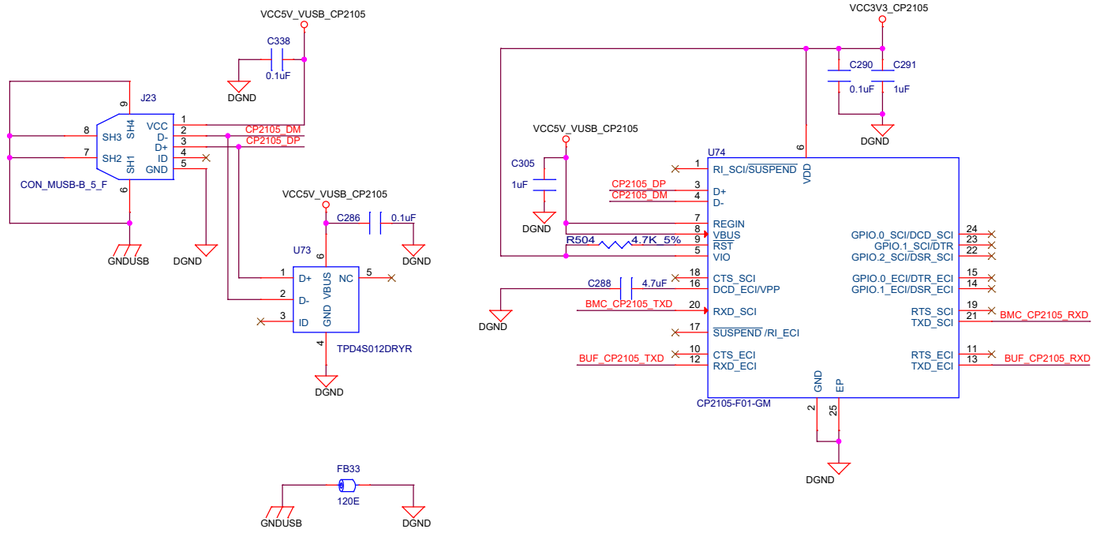
MUX BETWEEN BMC UART0 TO USB & 4 PIN HDR



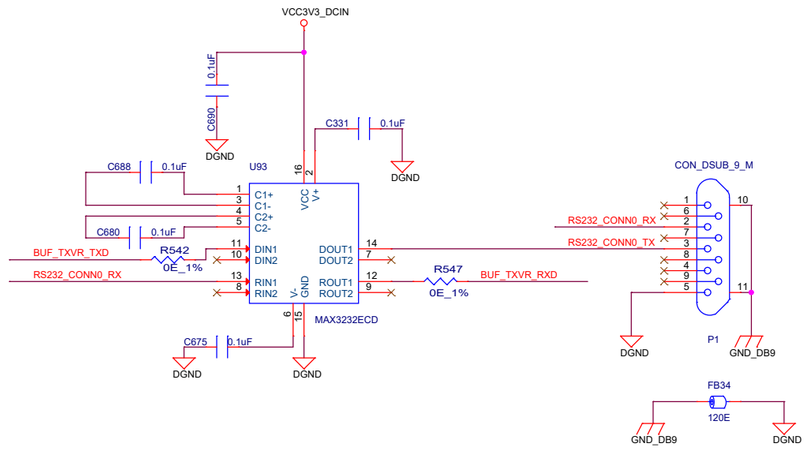
SoC_UART0 MUX SELECTION

SOC_UART0_CP2105_EN	SoC_UART0 Connected to
L	CP2105 (USB-to-UART)
H	UART TRANSCIEVER DB9 CONNECTOR

SoC UART0 & BMC CONSOLE OVER USB



UART0 DB9 CON



Designed for TI by Mistral Solutions Pvt Ltd

Title	UART 0,1,& 2	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
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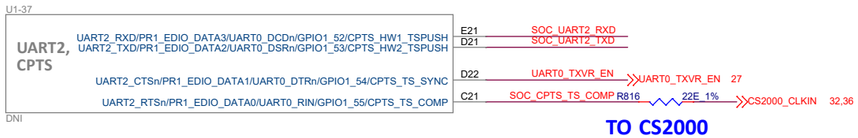
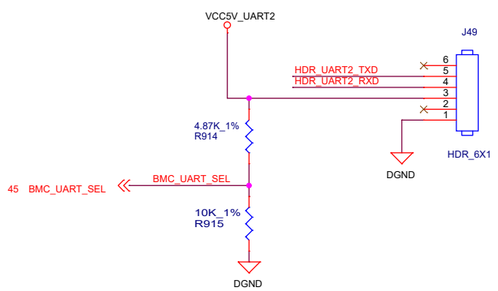
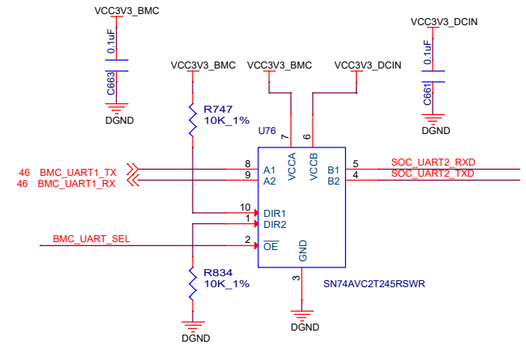
SoC UART2

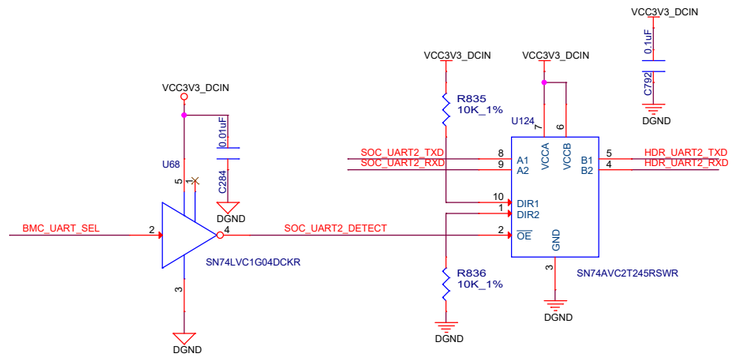


UART2 HEADER



SoC UART2 MUX - BMC or UART2 Header





SoC_UART2 MUX SELECTION

BMC_UART_SEL	SoC_UART2 Connected to
L	BMC
H	UART2 HEADER (J49)

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Title	UART2		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
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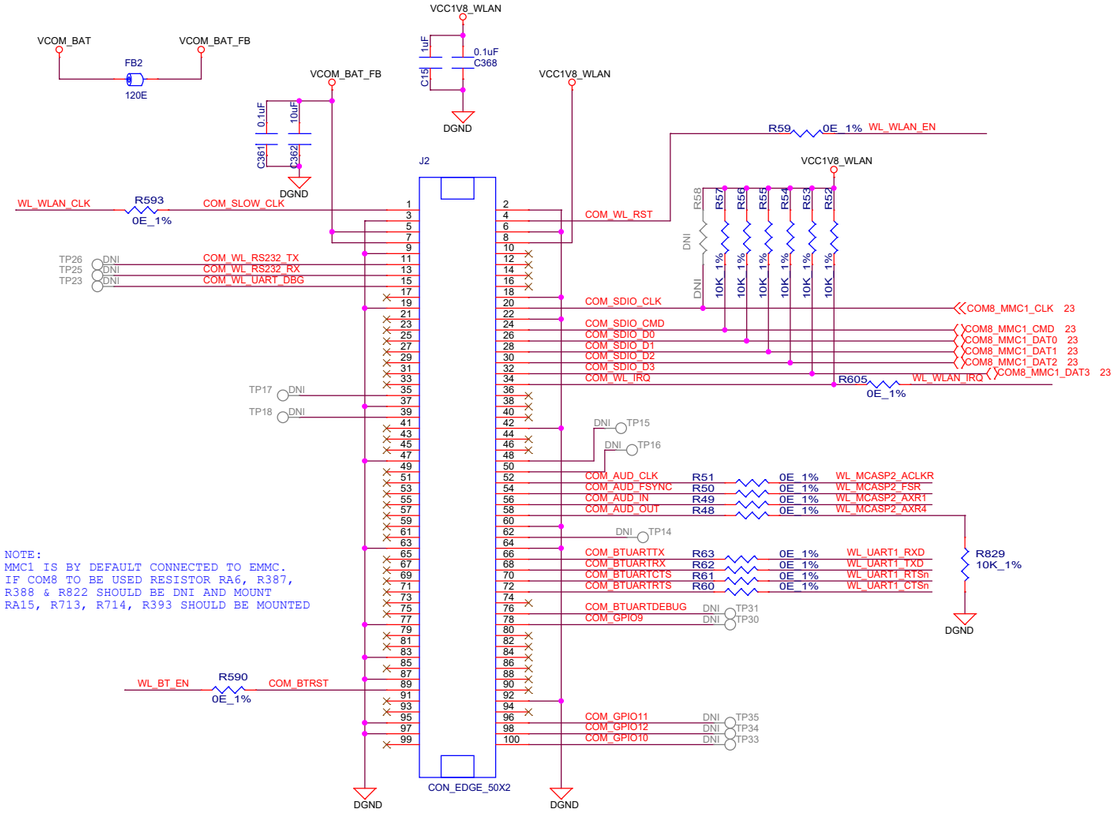
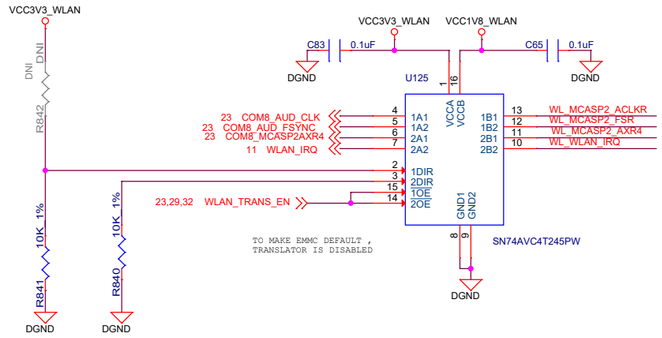
COM8 CONNECTOR



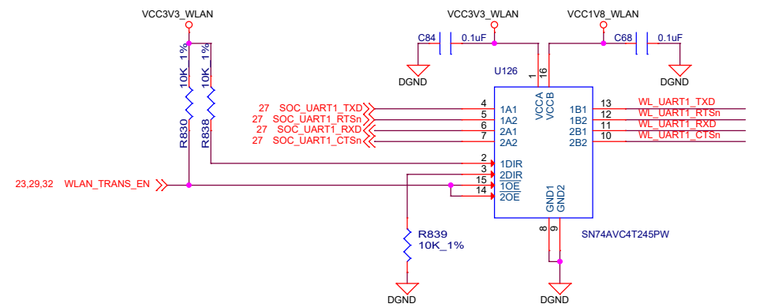
NOTE:
MMC1 IS BY DEFAULT CONNECTED TO EMMC.
IF COM8 TO BE USED RESISTOR RA6, R387,
R388 & R822 SHOULD BE DNI AND MOUNT
RA15, R713, R714, R393 SHOULD BE MOUNTED

CONNECTION ON COM8 IS FOLLOWED AS PER
AM437x GP EVM. THESE ARE GPIOS ARE NOT USED
IN AM437x GP EVM, SO WE PROVIDED AS TEST POINTS

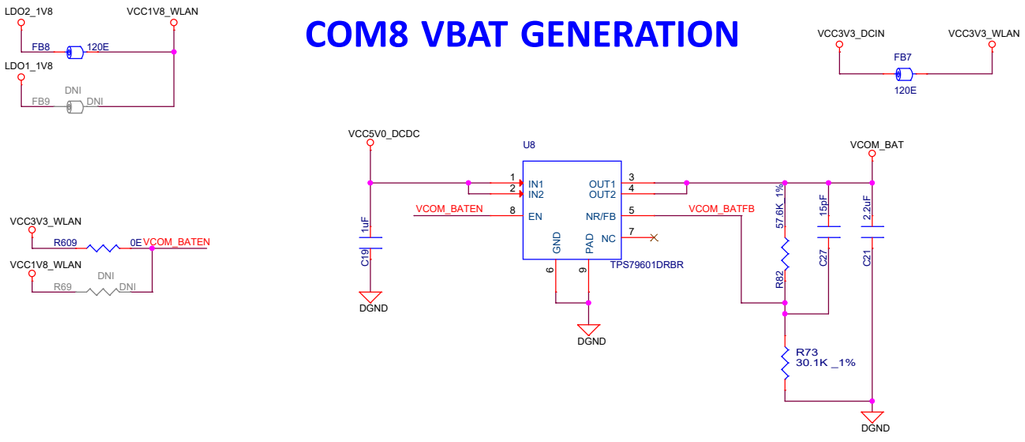
COM8 LEVEL TRANSLATOR1



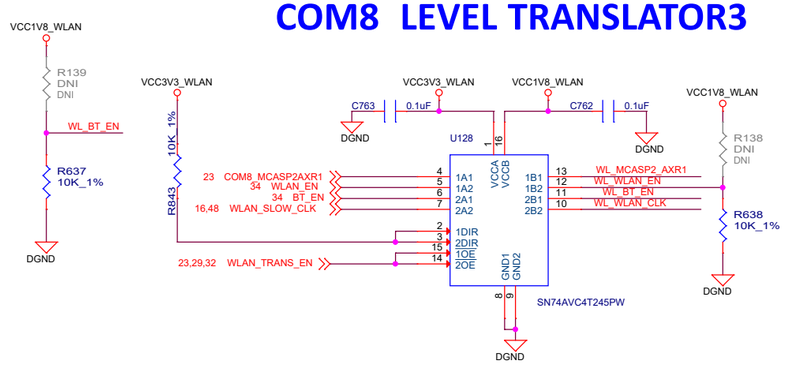
COM8 LEVEL TRANSLATOR2



COM8 VBAT GENERATION



COM8 LEVEL TRANSLATOR3



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Title	COM8 CONNECTOR		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
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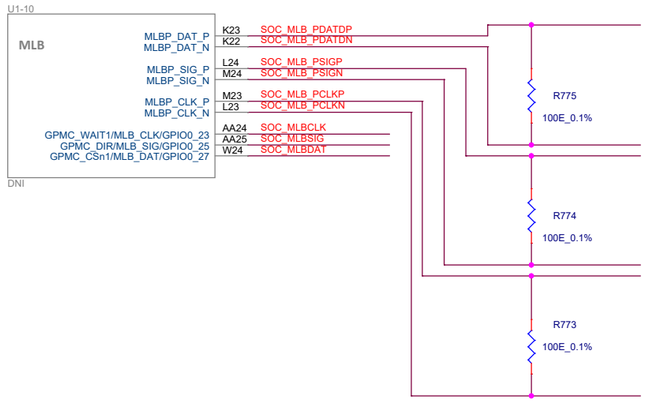
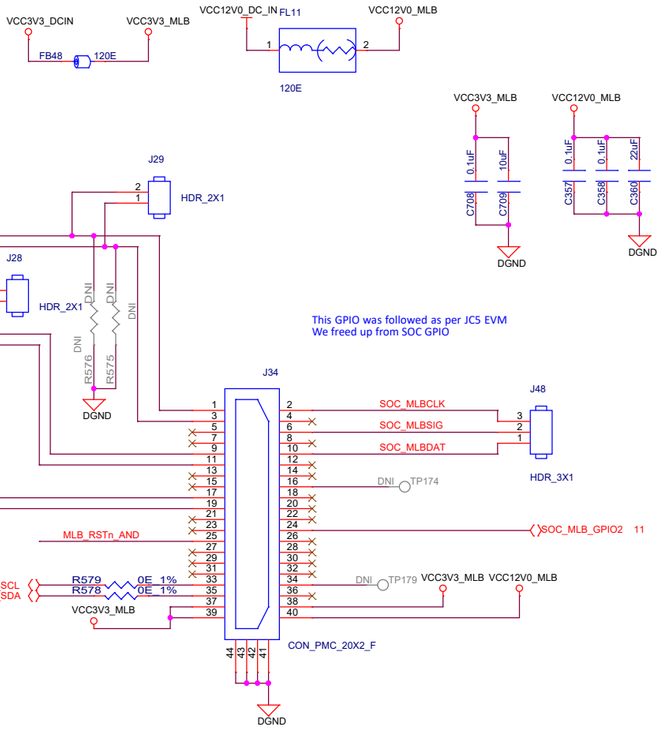
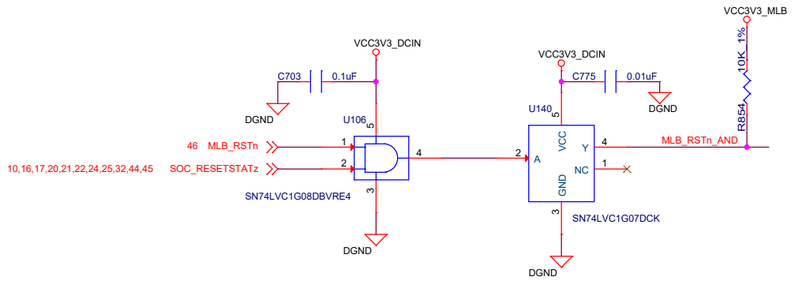
MLB CONNECTOR







Designed for TI by Mistral Solutions Pvt Ltd

Title	MLB CONNECTOR	
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev
C		A
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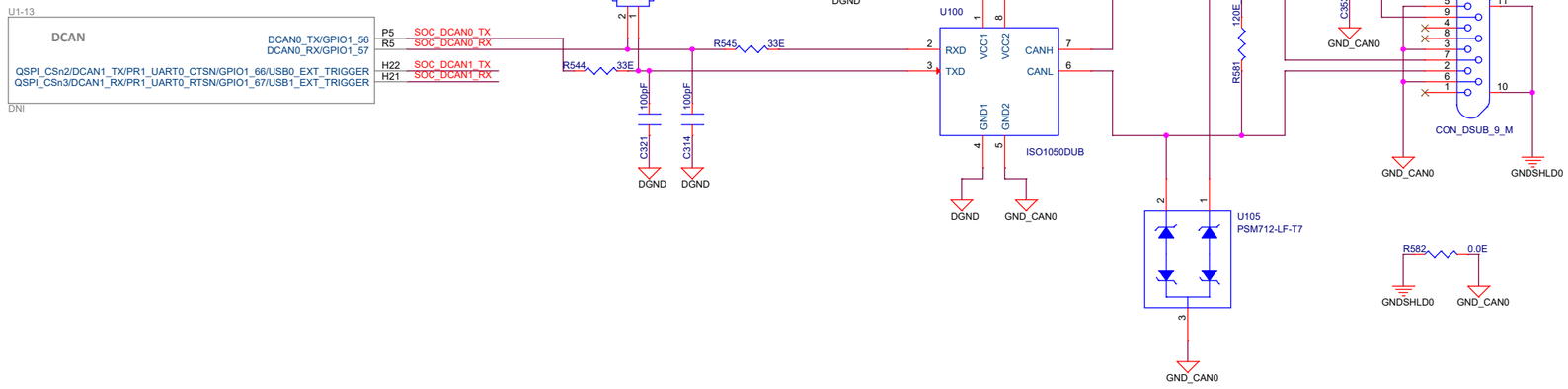
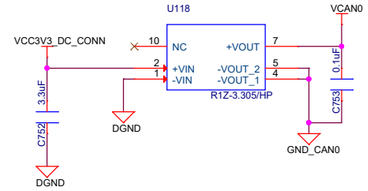
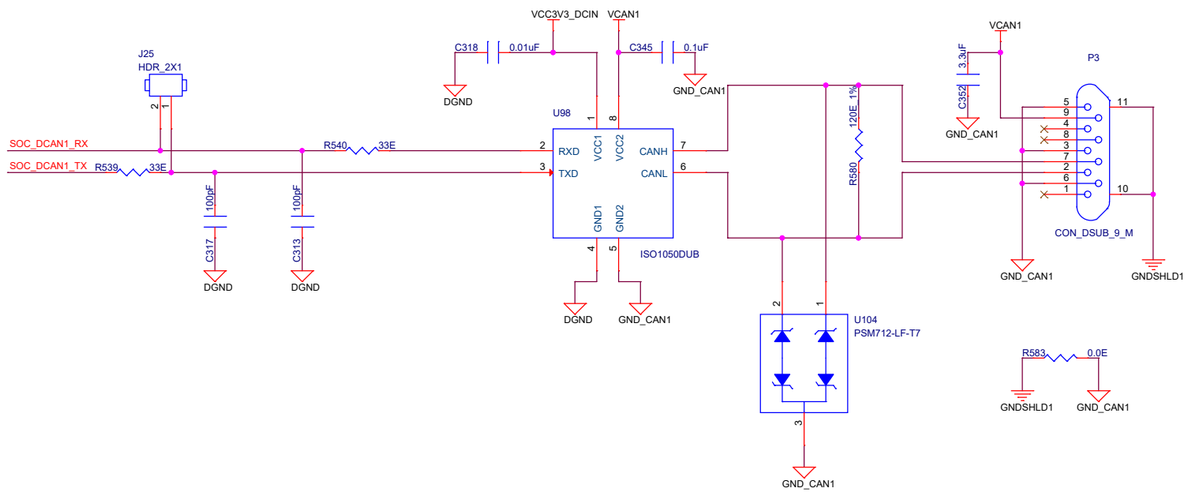
DCAN0



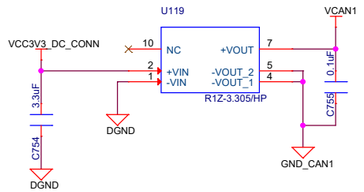
DCAN0 POWER



DCAN1



DCAN1 POWER



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Title	DCAN CONNECTOR	
Size: C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev: A
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SOC MCASP

U1-20

McASP0

PR0_PRU1_GPO17/PR0_PRU1_GPI17/PR1_UART0_TXD/GPIO1_01/MCASP0_AXR0	B9	R482	0E 1%	SOC_MCASP0AXR0	SOC_MCASP0AXR0	21
PR0_PRU1_GPO18/PR0_PRU1_GPI18/PR0_EDC_LATCH1_IN/GPIO1_02/MCASP0_AXR1	A9	R454	0E 1%	SOC_MCASP0AXR1	SOC_MCASP0AXR1	21
PR0_PRU1_GPO19/PR0_PRU1_GPI19/PR0_EDC_SYNC1_OUT/GPIO1_03/MCASP0_AXR2	B10	R451	0E 1%	SOC_MCASP0AXR2	SOC_MCASP0AXR2	21
PR0_MDIO_DATA/GPIO1_04/MCASP0_AXR3	A10	R447	0E 1%	SOC_MCASP0AXR3	SOC_MCASP0AXR3	21
PR0_MDIO_MDCLK/GPIO1_05/MCASP0_AXR4	C10	R480	0E 1%	SOC_MCASP0AXR4		
PR1_PRU0_GPO0/PR1_PRU0_GPI0/GPIO1_06/MCASP0_AXR5	E10	R476	0E 1%	SOC_MCASP0AXR5		
PR1_PRU0_GPO1/PR1_PRU0_GPI1/GPIO1_07/MCASP0_AXR6	D10	R472	0E 1%	SOC_MCASP0AXR6		
PR1_PRU0_GPO2/PR1_PRU0_GPI2/GPIO1_08/MCASP0_AXR7	F10	R469	0E 1%	SOC_MCASP0AXR7		
PR1_PRU0_GPO3/PR1_PRU0_GPI3/GPIO1_09/MCASP0_AXR8	C11	R441	0E 1%	SOC_MCASP0AXR8		
PR1_PRU0_GPO4/PR1_PRU0_GPI4/MMC0_POW/GPIO1_10/MCASP0_AXR9	D11	R437	0E 1%	SOC_MCASP0AXR9		
PR1_PRU0_GPO5/PR1_PRU0_GPI5/MMC0_SDWP/GPIO1_11/MCASP0_AXR10	E11			SOC_MCASP0AXR10	R462 0E 1%	SoC_LED1 34
PR1_PRU0_GPO7/PR1_PRU0_GPI7/MMC0_DAT7/GPIO1_13/MCASP0_AXR12	E12	R459	0E 1%	SOC_MCASP0AXR12		
PR1_PRU0_GPO8/PR1_PRU0_GPI8/MMC0_DAT6/GPIO1_14/MCASP0_AXR13	C12	R481	0E 1%	SOC_MCASP0AXR13		
PR1_PRU0_GPO9/PR1_PRU0_GPI9/MMC0_DAT5/GPIO1_15/MCASP0_AXR14	B11	R460	0E 1%	SOC_MCASP0AXR14		
PR1_PRU0_GPO10/PR1_PRU0_GPI10/MMC0_DAT4/GPIO1_16/MCASP0_AXR15	B12	R477	0E 1%	SOC_MCASP0AXR15		
PR0_PRU1_GPO13/PR0_PRU1_GPI13/GPIO0_141/MCASP0_AHCLKR	B8	R473	0E 1%	SOC_MCASP0AHCLKR		
PR0_PRU1_GPO11/PR0_PRU1_GPI11/GPIO0_139/MCASP0_ACLKR	E9	R470	0E 1%	SOC_MCASP0ACLKR		
PR0_PRU1_GPO12/PR0_PRU1_GPI12/GPIO0_140/MCASP0_AFSR	A8	R468	0E 1%	SOC_MCASP0FSR		
PR0_PRU1_GPO16/PR0_PRU1_GPI16/GPIO1_00/MCASP0_AHCLKX	C9	R458	50E	SOC_MCASP0AHCLKX		
PR0_PRU1_GPO14/PR0_PRU1_GPI14/GPIO0_142/MCASP0_ACLKX	D9	R452	22E 1%	SOC_MCASP0ACLKX	SOC_MCASP0ACLKX	21
PR0_PRU1_GPO15/PR0_PRU1_GPI15/GPIO0_143/MCASP0_AFSX	C8	R448	22E 1%	SOC_MCASP0FSX	SOC_MCASP0FSX	21
PR0_PRU1_GPO10/PR0_PRU1_GPI10/GPIO0_138/MCASP0_AMUTE	C7	R444	0E 1%	SOC_MCASP0AMUTE		

DNI

SOC_MCASP0AXR10 R820 DNI DNI SOC_MCASP0AXR10_R
SOC_MCASP0AHCLKX SOC_MCASP1AHCLKX

U1-7

McASP1

PR0_PRU1_GPO0/PR0_PRU1_GPI0/GPIO0_128/MCASP1_AXR0	B5	R439	0E 1%	SOC_MCASP1AXR0		
PR0_PRU1_GPO1/PR0_PRU1_GPI1/GPIO0_129/MCASP1_AXR1	B6	R434	0E 1%	SOC_MCASP1AXR1		
PR0_PRU1_GPO2/PR0_PRU1_GPI2/GPIO0_130/MCASP1_AXR2	D7	R430	0E 1%	SOC_MCASP1AXR2		
PR0_PRU1_GPO3/PR0_PRU1_GPI3/GPIO0_131/MCASP1_AXR3	A6	R425	0E 1%	SOC_MCASP1AXR3		
PR0_PRU1_GPO4/PR0_PRU1_GPI4/GPIO0_132/MCASP1_AXR4	C6	R422	0E 1%	SOC_MCASP1AXR4		
PR0_PRU1_GPO5/PR0_PRU1_GPI5/GPIO0_133/MCASP1_AXR5	E8	R419	0E 1%	SOC_MCASP1AXR5		
PR0_PRU1_GPO6/PR0_PRU1_GPI6/GPIO0_134/MCASP1_AXR6	A7	R416	0E 1%	SOC_MCASP1AXR6		
PR0_PRU1_GPO7/PR0_PRU1_GPI7/GPIO0_135/MCASP1_AXR7	D8	R414	0E 1%	SOC_MCASP1AXR7		
PR0_PRU1_GPO8/PR0_PRU1_GPI8/GPIO0_136/MCASP1_AXR8	F9	R412	0E 1%	SOC_MCASP1AXR8		
PR0_PRU1_GPO9/PR0_PRU1_GPI9/GPIO0_137/MCASP1_AXR9	B7	R410	0E 1%	SOC_MCASP1AXR9		
PR0_PRU0_GPO18/PR0_PRU0_GPI18/PR0_EDC_LATCH0_IN/GPIO0_126/MCASP1_AHCLKX	C5	R408	22E 1%	SOC_MCASP1AHCLKX		
PR0_PRU0_GPO16/PR0_PRU0_GPI16/GPIO0_124/MCASP1_ACLKX	D6	R405	0E 1%	SOC_MCASP1ACLKX		
PR0_PRU0_GPO17/PR0_PRU0_GPI17/PR1_UART0_RXD/GPIO0_125/MCASP1_AFSX	C4	R404	0E 1%	SOC_MCASP1FSX		
PR0_PRU0_GPO15/PR0_PRU0_GPI15/GPIO0_123/MCASP1_AHCLKR	E7	R402	50E	SOC_MCASP1AHCLKR		
PR0_PRU0_GPO13/PR0_PRU0_GPI13/GPIO0_121/MCASP1_ACLKR	B4	R398	33E 1%	SOC_MCASP1ACLKR		
PR0_PRU0_GPO14/PR0_PRU0_GPI14/GPIO0_122/MCASP1_AFSR	A4	R394	0E 1%	SOC_MCASP1FSR		
PR0_PRU0_GPO19/PR0_PRU0_GPI19/PR0_EDC_SYNC0_OUT/GPIO0_127/MCASP1_AMUTE	A5	SOC_MCASP1AMUTE	R817 DNI	DNI		CS2000_CLKIN 28,36

DNI

SOC_MCASP1AMUTE R823 0E 1% SOC_MCASP1AMUTE_R

U1-8

McASP2

PR0_PRU0_GPO0/PR0_PRU0_GPI0/GPIO0_108/MCASP2_AXR0	D3	SOC_MCASP2AXR0	R428 0E 1%	SOC_MCASP2AXR1		
PR0_PRU0_GPO1/PR0_PRU0_GPI1/GPIO0_109/MCASP2_AXR1	A2	R423	0E 1%	SOC_MCASP2AXR2	SoC_LED0	34
PR0_PRU0_GPO2/PR0_PRU0_GPI2/GPIO0_110/MCASP2_AXR2	E4	R433	0E 1%	SOC_MCASP2AXR3	SOC_MCASP2AXR1	23
PR0_PRU0_GPO3/PR0_PRU0_GPI3/GPIO0_111/MCASP2_AXR3	B1	R415	33E 1%	SOC_MCASP2AXR4	SOC_MCASP2AXR2	23
PR0_PRU0_GPO4/PR0_PRU0_GPI4/GPIO0_112/MCASP2_AXR4	A3	R396	0E 1%	SOC_MCASP2AXR5	SOC_MCASP2AXR3	23
PR0_PRU0_GPO5/PR0_PRU0_GPI5/GPIO0_113/MCASP2_AXR5	E5	R420	0E 1%		SOC_MCASP2AXR4	23
PR0_PRU0_GPO11/PR0_PRU0_GPI11/GPIO0_119/MCASP2_AHCLKX	D5	R417	0E 1%	SOC_McASP2AHCLKX		
PR0_PRU0_GPO12/PR0_PRU0_GPI12/GPIO0_120/MCASP2_ACLKX	B3	R413	50E	SOC_MCASP2ACLKX	SOC_McASP2ACLKX	23
PR0_PRU0_GPO10/PR0_PRU0_GPI10/GPIO0_118/MCASP2_AFSX	C3	R411	0E 1%	SOC_MCASP2FSX	SOC_McASP2FSX	23
PR0_PRU0_GPO8/PR0_PRU0_GPI8/GPIO0_116/MCASP2_AHCLKR	E6	R407	50E	SOC_McASP2AHCLKR	SOC_McASP2AHCLKR	36
PR0_PRU0_GPO6/PR0_PRU0_GPI6/GPIO0_114/MCASP2_ACLKR	B2	R401	50E	SOC_MCASP2ACLKR		
PR0_PRU0_GPO7/PR0_PRU0_GPI7/GPIO0_115/MCASP2_AFSR	D4	R409	0E 1%	SOC_MCASP2FSR		
PR0_PRU0_GPO9/PR0_PRU0_GPI9/XREFCLK/GPIO0_117/MCASP2_AMUTE	C2	SOC_MCASP2AMUTE	R393 DNI	DNI		WLAN_TRANS_EN 23,29

DNI

DNI
SOC_MCASP2AXR0 R821 DNI SOC_MCASP2AXR0_R
SOC_MCASP2AMUTE R822 DNI SOC_MCASP2AMUTE_R
DNI

AUDIO EXP CONNECTOR

VCC12V0_DC_IN — FL6 120E — VCC12V0_AUD_EXP
C166 22uF, C167 0.1uF, DGND

VCC3V3_DCIN — FB52 120E — VCC3V3_AUD_EXP
C173 0.1uF, C169 10uF, DGND

JP1 — HDR_2X50

		VCC12V0_AUD_EXP	
		VCC3V3_AUD_EXP	
34 SOC_SPI2_CLK			
10,16,17,20,21,22,24,25,30,44,45 SOC_RESETSTAT2			
34 SOC_SPI2_MOSI			
34 SOC_SPI2_SCS0		SOC_MCASP2FSR	SOC_SPI2_MISO 34
SOC_MCASP2AMUTE_R		SOC_MCASP2AXR0_R	
SOC_MCASP2ACLKR		SOC_MCASP2AXR5	
SOC_MCASP1AMUTE_R		SOC_MCASP1FSX	
SOC_MCASP1AXR0		SOC_MCASP0AMUTE	
SOC_MCASP1ACLKR		SOC_MCASP1ACLKX	
SOC_MCASP1AXR8		SOC_MCASP1AXR1	
SOC_MCASP1AXR4		SOC_MCASP1AXR3	
SOC_MCASP1FSR		SOC_MCASP1AHCLKR	
SOC_MCASP0AXR5		SOC_MCASP1AXR6	
SOC_MCASP1AXR2		SOC_MCASP2AHCLKX	
SOC_MCASP1AXR9		SOC_MCASP0AXR7	
SOC_MCASP0AXR0		SOC_MCASP0AHCLKR	
SOC_MCASP0AXR4		SOC_MCASP1AXR7	
SOC_MCASP0ACLKR		SOC_MCASP0AXR2	
SOC_MCASP0AXR13		SOC_MCASP0FSR	
SOC_MCASP0AXR14		SOC_MCASP0FSX	
SOC_MCASP0AXR1		BUF_MCASP0AHCLKX	BUF_MCASP0AHCLKX 21
SOC_MCASP0AXR12		SOC_MCASP0AXR15	
SOC_MCASP1AXR5		SOC_MCASP0AXR3	
SOC_MCASP0AXR6		SOC_MCASP0AXR10_R	
SOC_MCASP0ACLKX		SOC_MCASP0AXR8	
SOC_MCASP0AXR9			
44 BMC_GPIO3		BMC_GPIO1	44
44 BMC_GPIO0		BMC_GPIO4	44
		DC_BRD_DET	11,21,44
44 BMC_GPIO5		BMC_GPIO6	44
BUF_I2C1_SCL		BUF_I2C1_SDA	

DGND, HDR_2X50, DGND

SOC SIGNAL	GPIOs ON AUDIO CARD
SOC_MCASP0AXR8/GPIO1_09	DIR_RST#
SOC_MCASP0AXR9/GPIO1_10	PCM1690_RST#
SOC_McASP0_AMUTE	DAC0_AMUTEI#, DAC1_AMUTEI#
SOC_MCASP1AXR4/GPIO0_132	McASP_CLK_SEL
SOC_MCASP1AXR6/GPIO0_134	DIR_AUDIO#
SOC_MCASP1AXR7/GPIO0_135	DIR_EMPH
SOC_MCASP1AXR8/GPIO0_136	DIR_ERROR
SOC_MCASP1AXR5/GPIO0_133	DIR_CLKST
SOC_MCASP1ACLKX/GPIO0_124	DIR_FSOUT0
SOC_MCASP1FSX / GPIO0_125	DIR_FSOUT1
SOC_SPI2_MOSI / GPIO0_105	I2SHDR_GPIO0
SOC_SPI2_MISO / GPIO0_104	I2SHDR_GPIO1

McASP0AHCLKX_BUFFER

VCC3V3_DCIN — C800 0.01uF — DGND
U139 SN74CB3T1G125DBVR (A, OE, VCC, GND, Y)
SoC_MCASP0AHCLKX, MCASP0AHCLKX_EN, BUF_MCASP0AHCLKX

VCC3V3_DCIN — R886 10K_1% — J45 HDR_3X1 — MCASP0AHCLKX_EN — R887 10K_1% — DGND

I2C BUFFER

VCC3V3_DCIN
C323 0.01uF, C322 0.01uF, 4.7K_5% (R560, R557)
U95 TCA9517DGKR (SCLB, SDAB, VCCB, VCCA, SCLA, SDAA, GND, EN)
20,21,22,34,36 SOC_I2C1_SCL — R553 0E 1% — BUF_I2C1_SCL — BUF_I2C1_SCL 30
20,21,22,34,36 SOC_I2C1_SDA — R558 0E 1% — BUF_I2C1_SDA — BUF_I2C1_SDA 30
R564 0E 1%, R563 DNI DNI, DGND

Designed for TI by Mistral Solutions Pvt Ltd

Title	AUDIO EXP CONNECTOR		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
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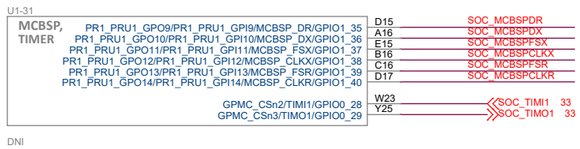
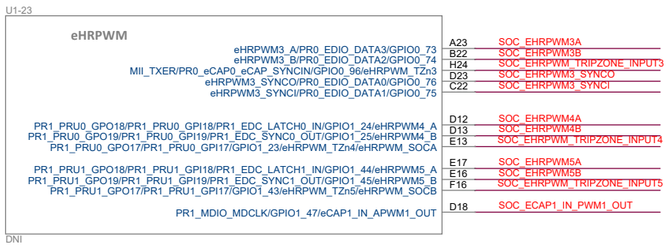
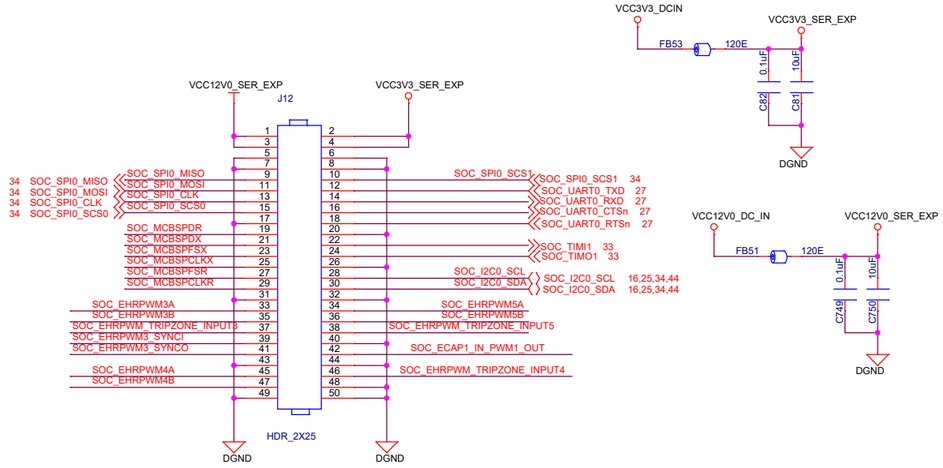
SoC McBSP



SoC PWM



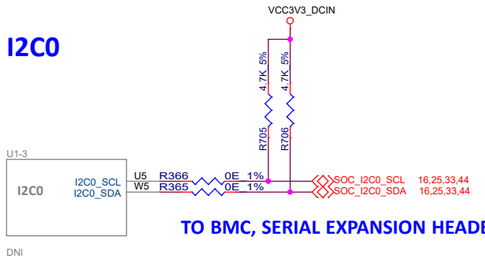
SERIAL EXPANSION CONNECTOR



Designed for TI by Mistral Solutions Pvt Ltd

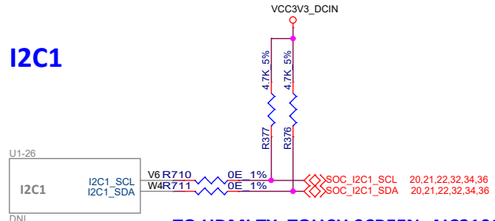
Title	SERIAL & EHRPWM EXP CONNECTOR	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 33 of 51	

I2C0



TO BMC, SERIAL EXPANSION HEADER

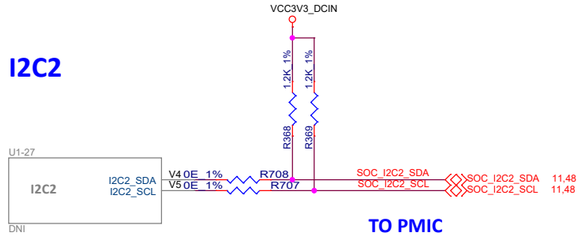
I2C1



TO HDMI TX, TOUCH SCREEN, AIC3106 ,
AUDIO & MLB EXP HEADER

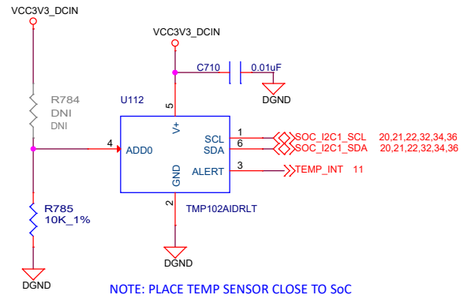
SoC I2C

I2C2



TO PMIC

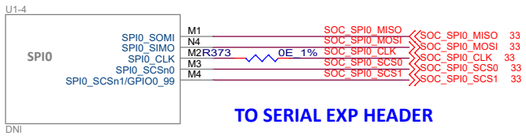
TEMPERATURE SENSOR



NOTE: PLACE TEMP SENSOR CLOSE TO SoC
I2C ADDRESS: 0x48

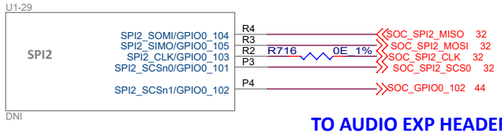
SoC SPI

SPI0



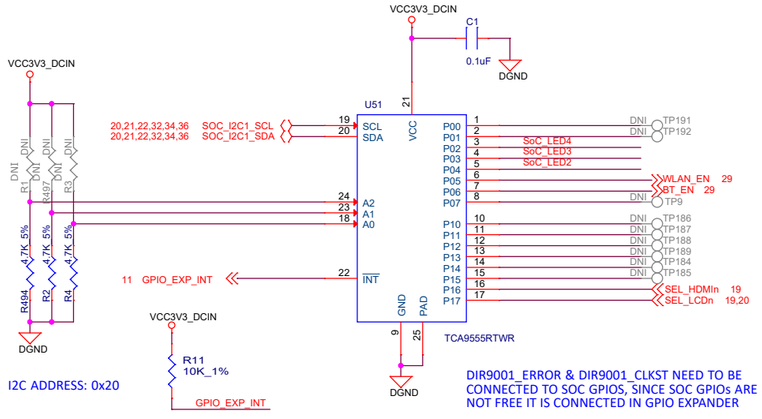
TO SERIAL EXP HEADER

SPI2



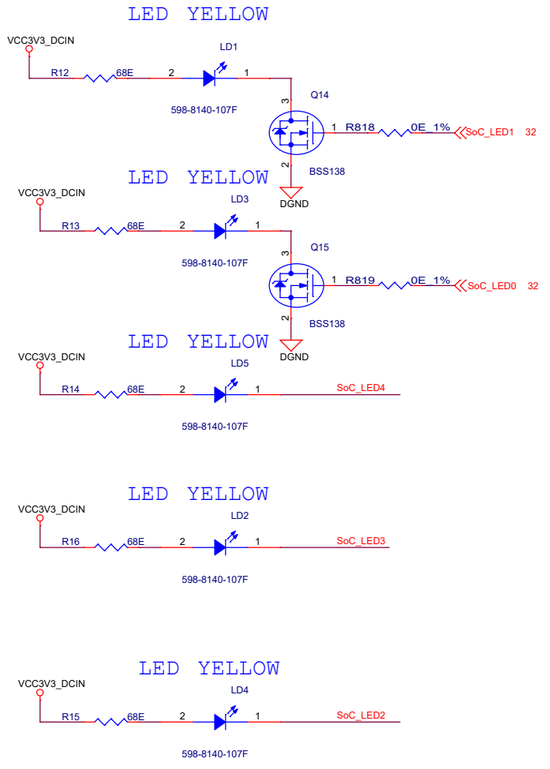
TO AUDIO EXP HEADER

GPIO EXPANDER & LEDs



I2C ADDRESS: 0x20

DIR9001_ERROR & DIR9001_CLKST NEED TO BE CONNECTED TO SOC GPIOS, SINCE SOC GPIOs ARE NOT FREE IT IS CONNECTED IN GPIO EXPANDER



Designed for TI by Mistral Solutions Pvt Ltd

Title	SERIAL CONTROL INTERFACE	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
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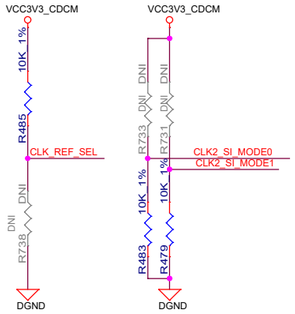
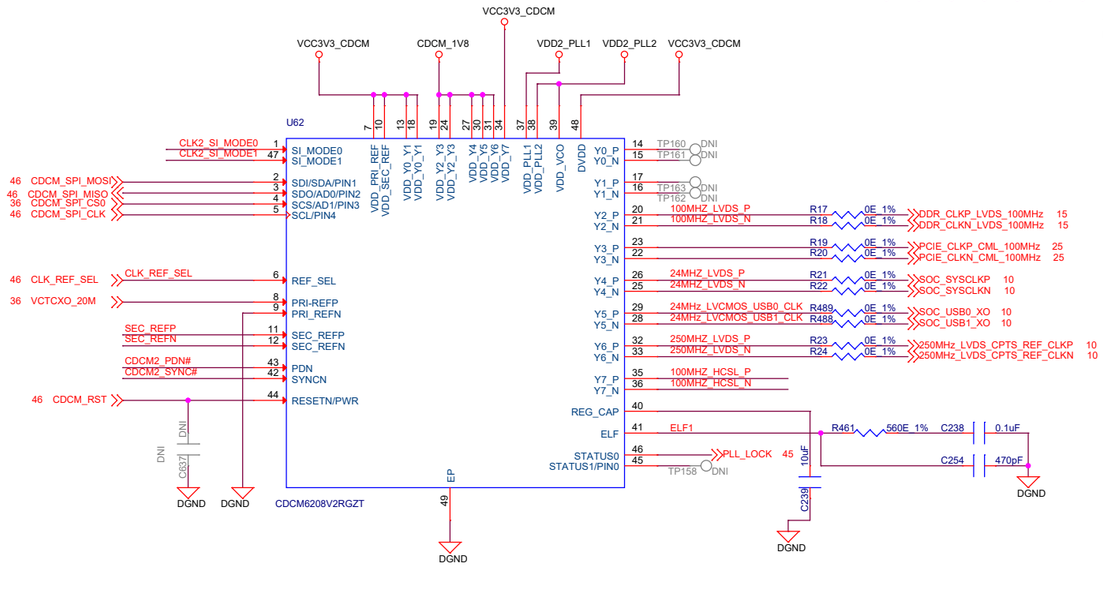
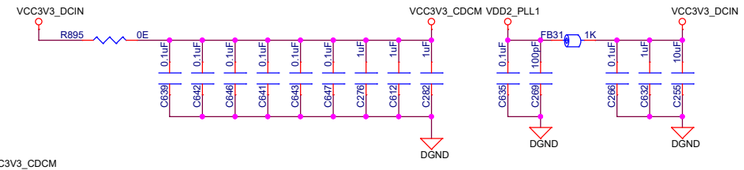
CLOCK GENERATOR

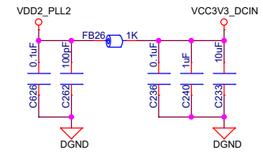


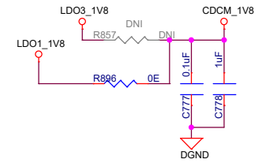
MODE SELECTION FOR CDCM DEVICE

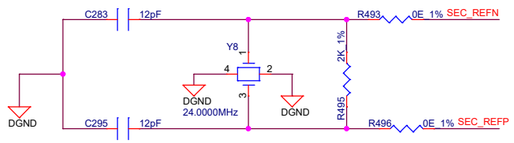
SI_MODE1	SI_MODE0	MODE SELECTED
0	0	SPI MODE
0	1	I2C MODE
1	0	PIN MODE (NO SERIAL PROGRAMMING)
1	1	RESERVED

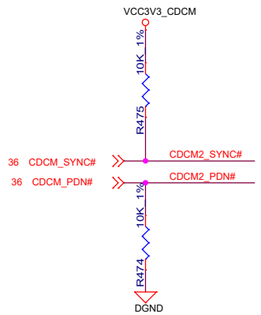


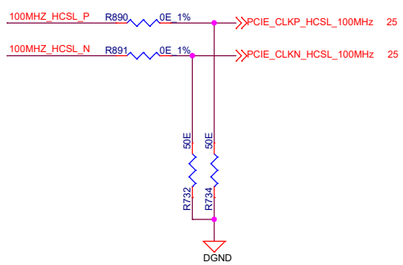








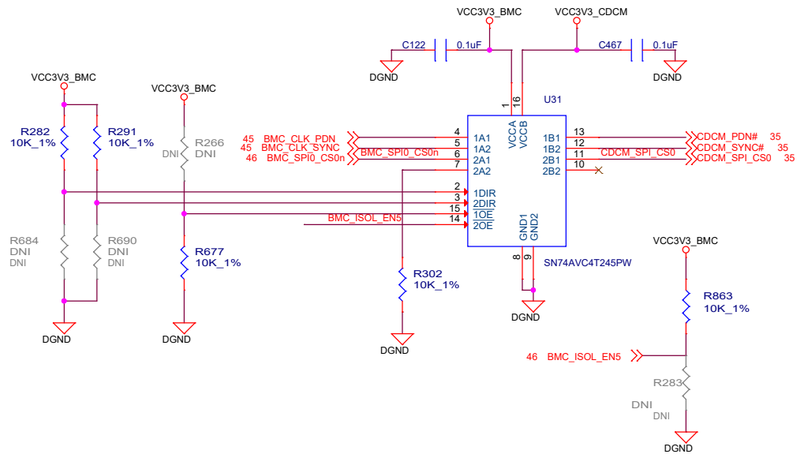




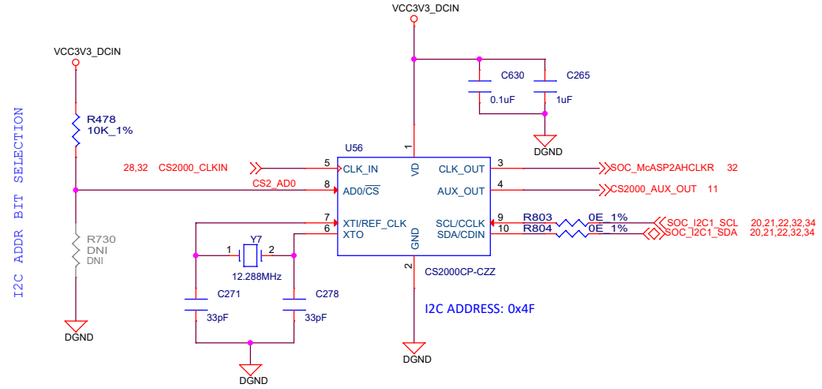
Designed for TI by Mistral Solutions Pvt Ltd

Title	CLOCK DISTRIBUTION 1		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
Date:	Friday, February 02, 2018	Sheet 35 of 51	

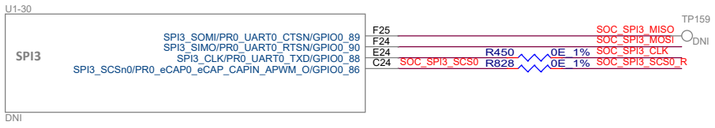
BMC ISOLATOR 5



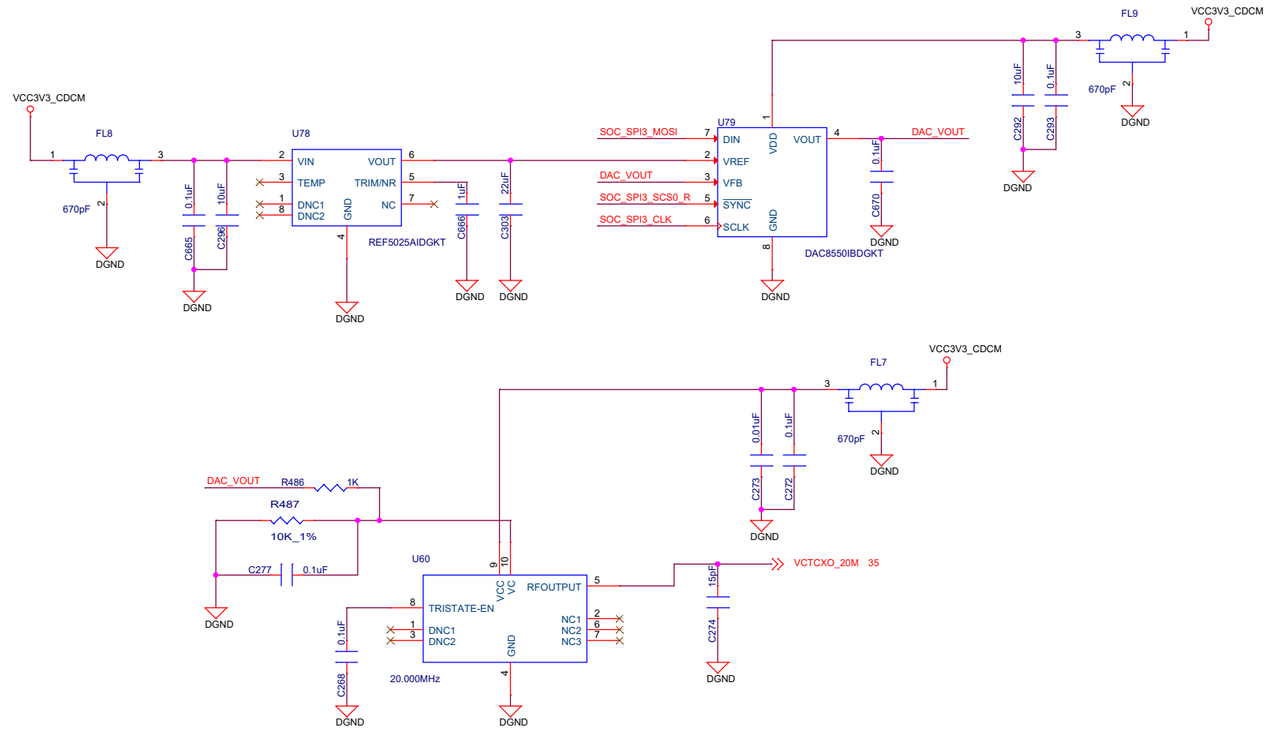
GENERATOR - AUDIO CLOCK DOMAIN



SoC SPI3



AVB CLOCK GENERATOR- 1588 CLOCK DOMAIN



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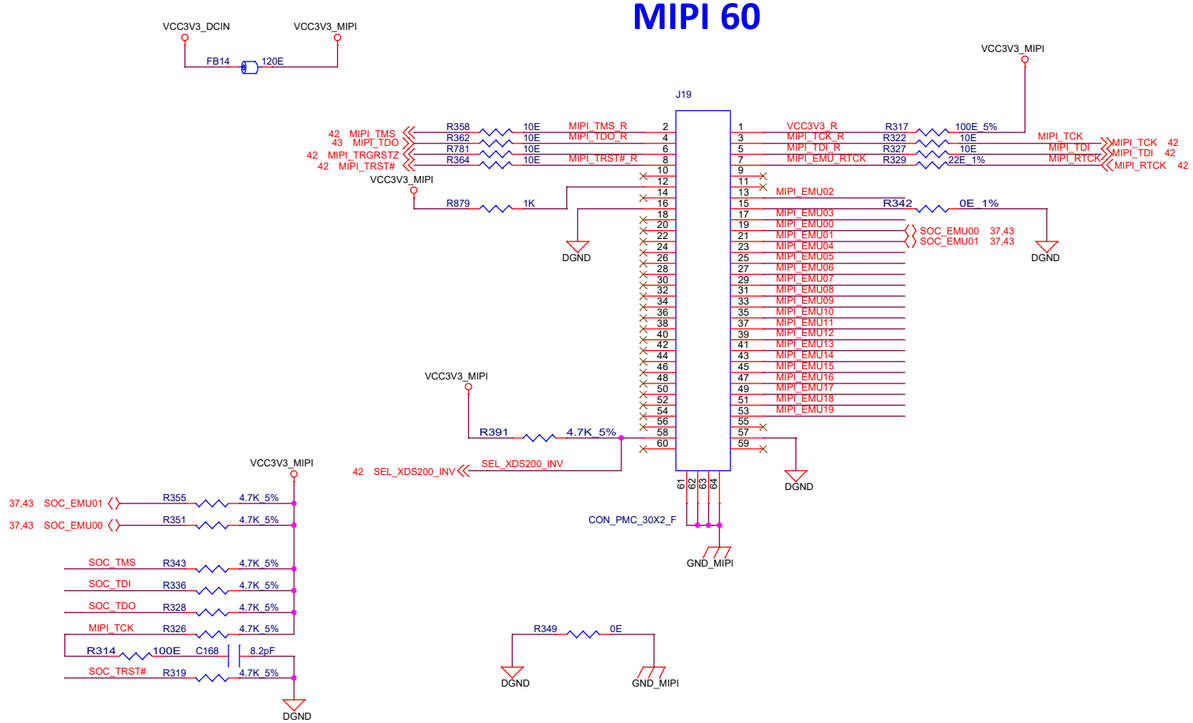
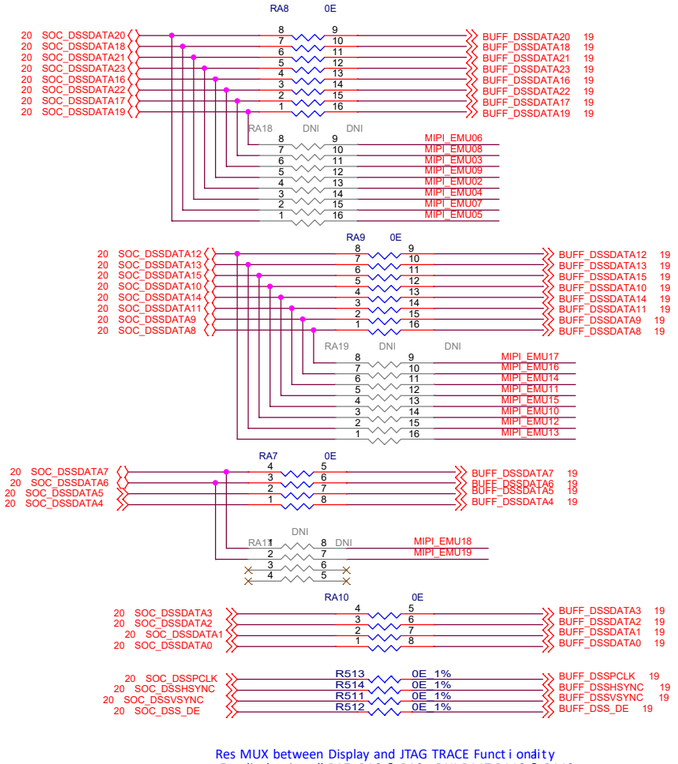
Title	CLOCK DISTRIBUTION 2	
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev
C		A
Date:	Friday, February 02, 2018	Sheet 36 of 51

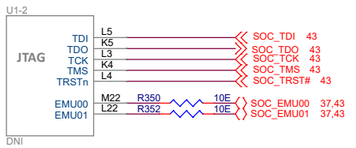
MIPI 60

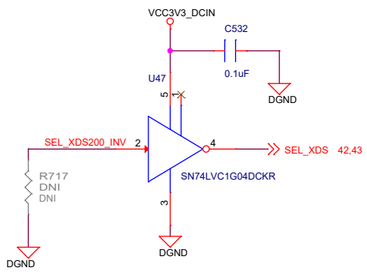


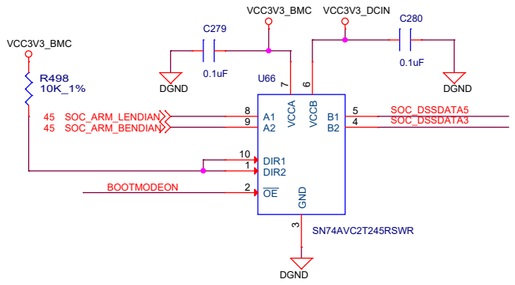


Res MUX between Display and JTAG TRACE Functi onality
-For display, install RA7, RA8 & RA9 . DNI RA17,RA18 & RA19
-For TRACE, install RA17, RA18 & RA19. DNI RA7, RA8 & RA9

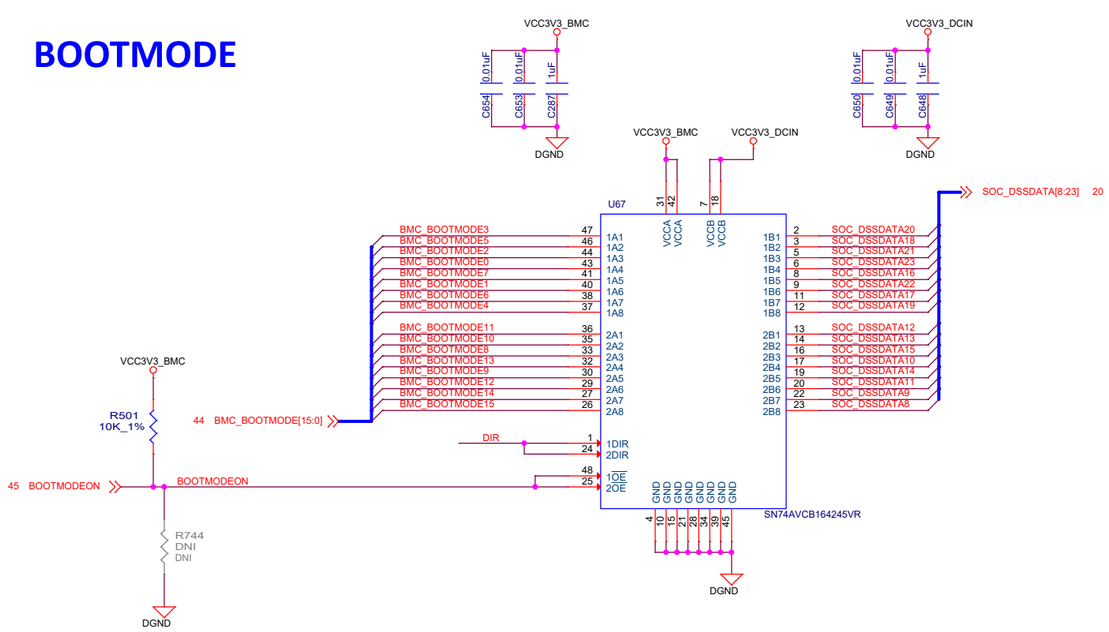
SoC JTAG







BOOTMODE



Designed for TI by Mistral Solutions Pvt Ltd

Title	EMU & MIPI60 CONNECTOR		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
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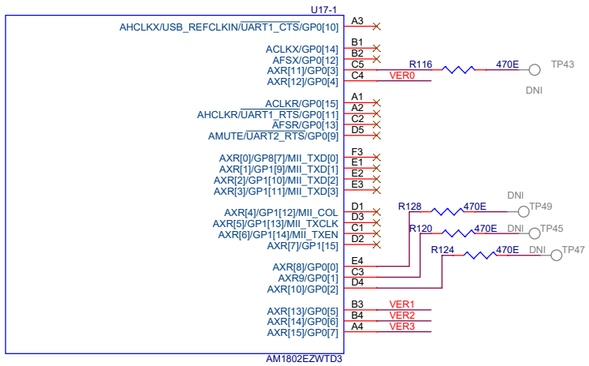
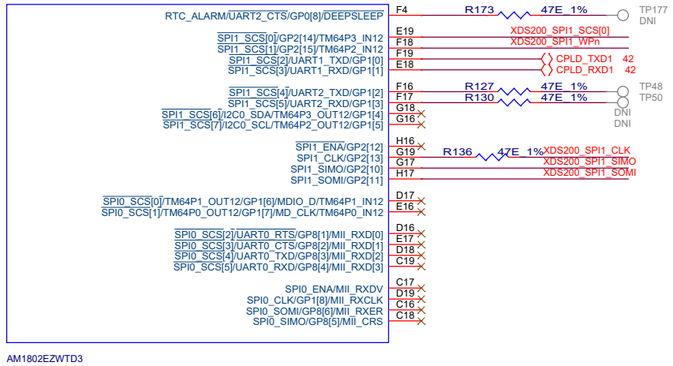
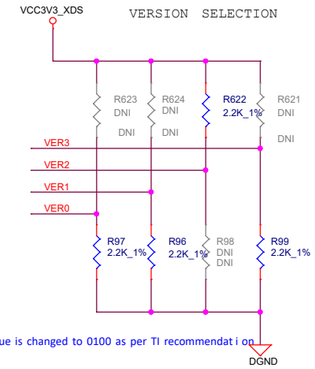
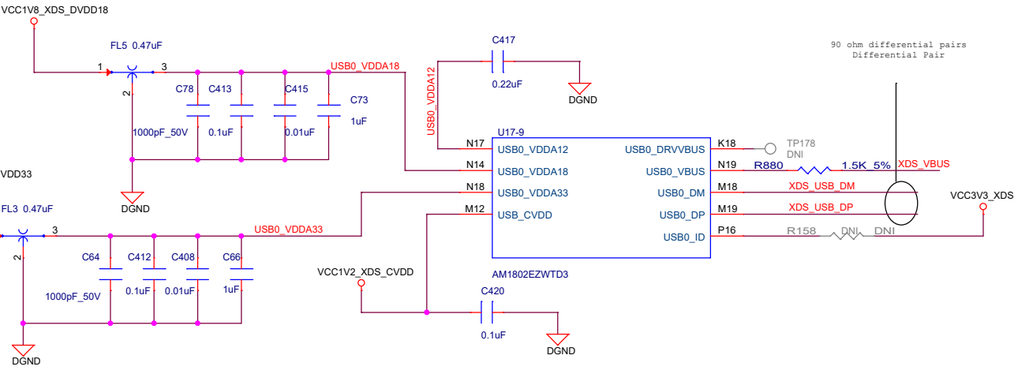
XDS200

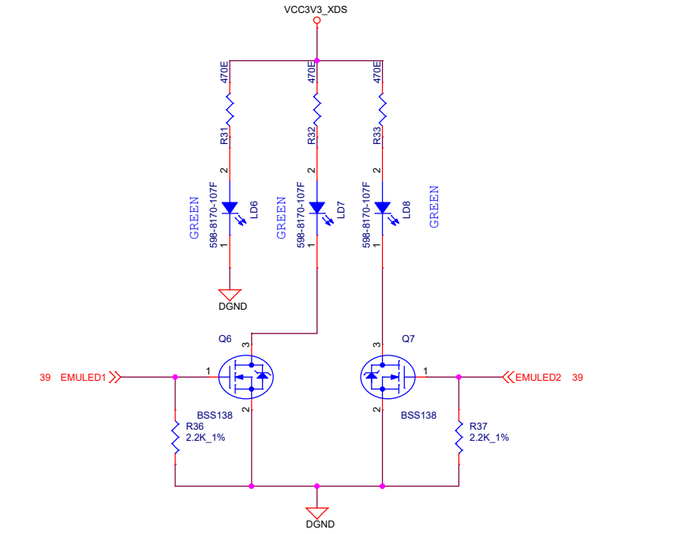


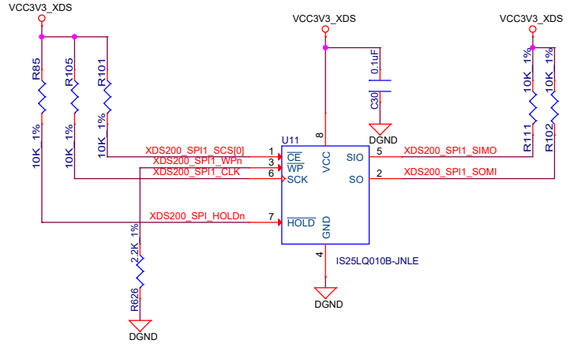




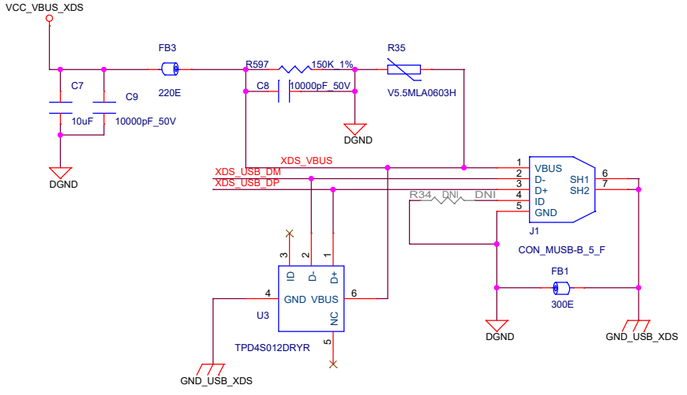
Note: Version Code default value is changed to 0100 as per TI recommendat i on







MINI USB CONN



Designed for TI by Mistral Solutions Pvt Ltd

Title	XDS200		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
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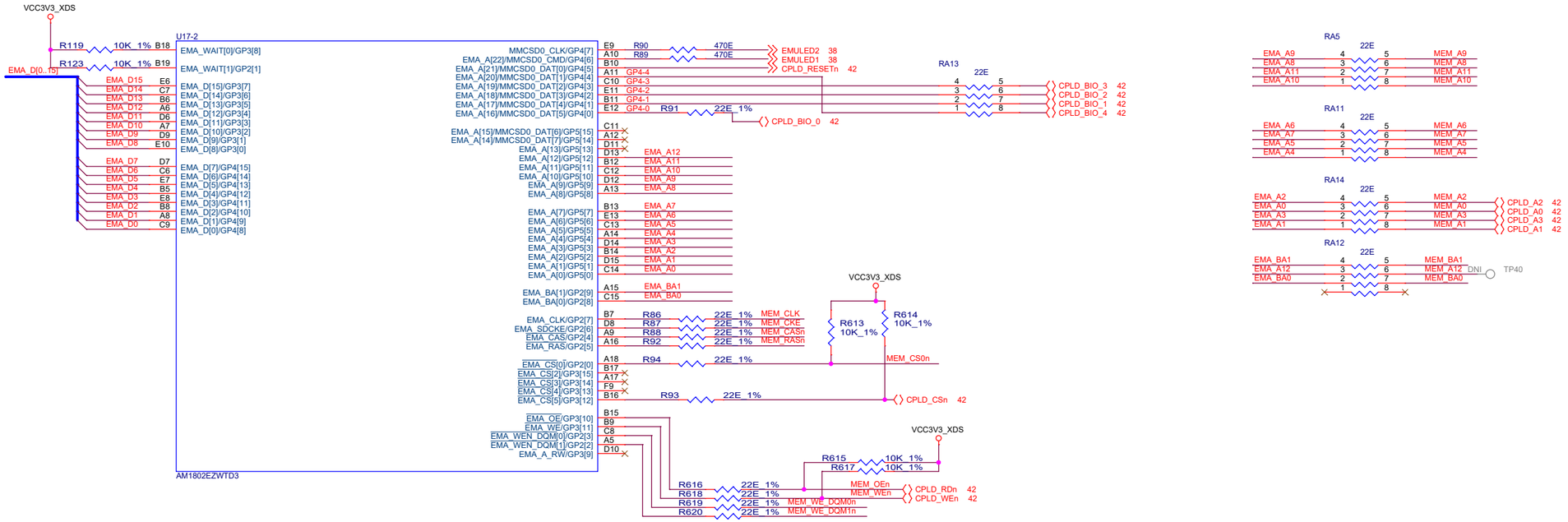
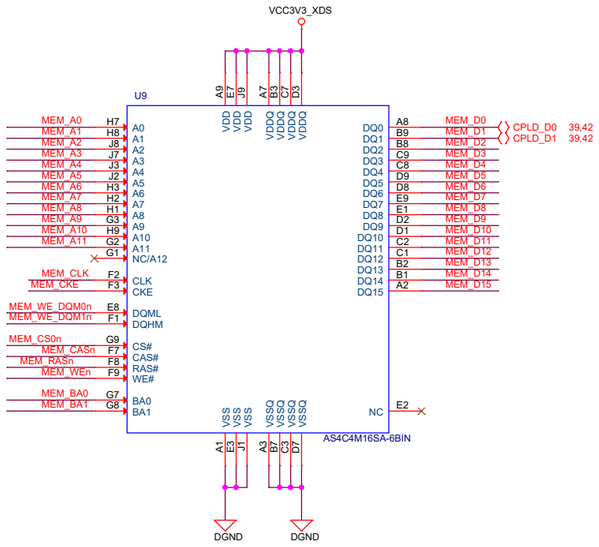
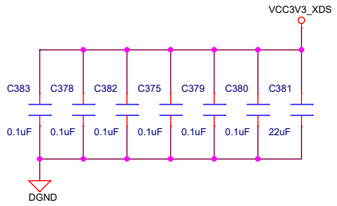
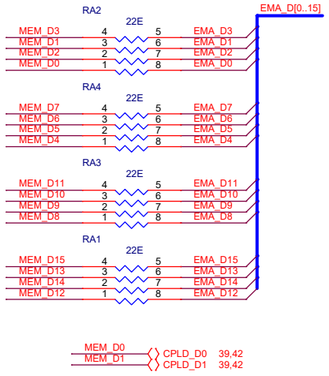
XDS200



SDRAM







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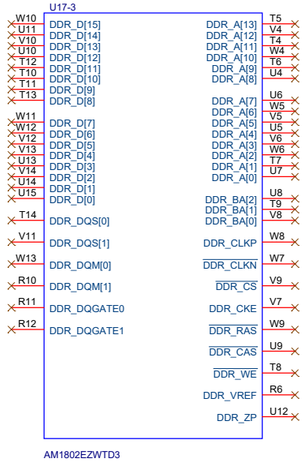
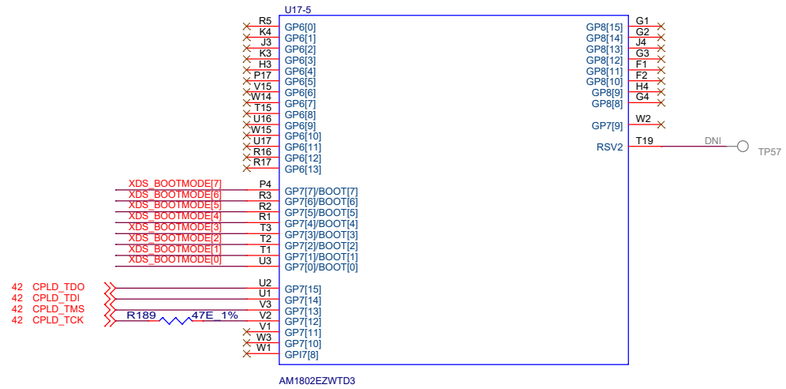
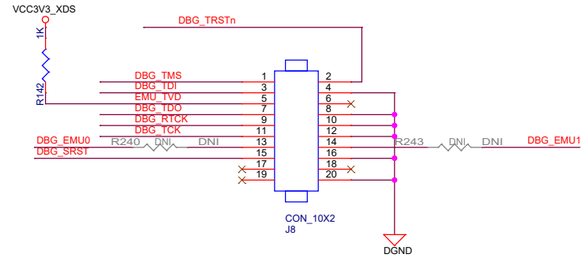
Title	JTAG_XDS200_2		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
Date:	Friday, February 02, 2018	Sheet 39 of 51	

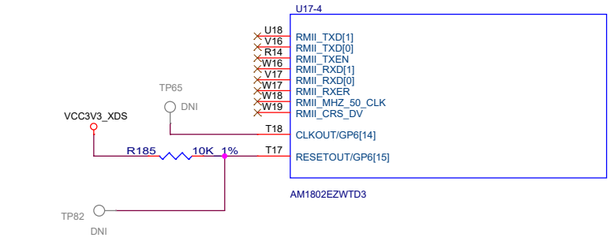
XDS200



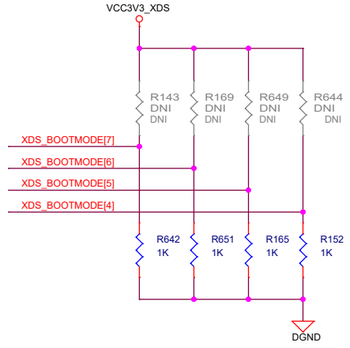


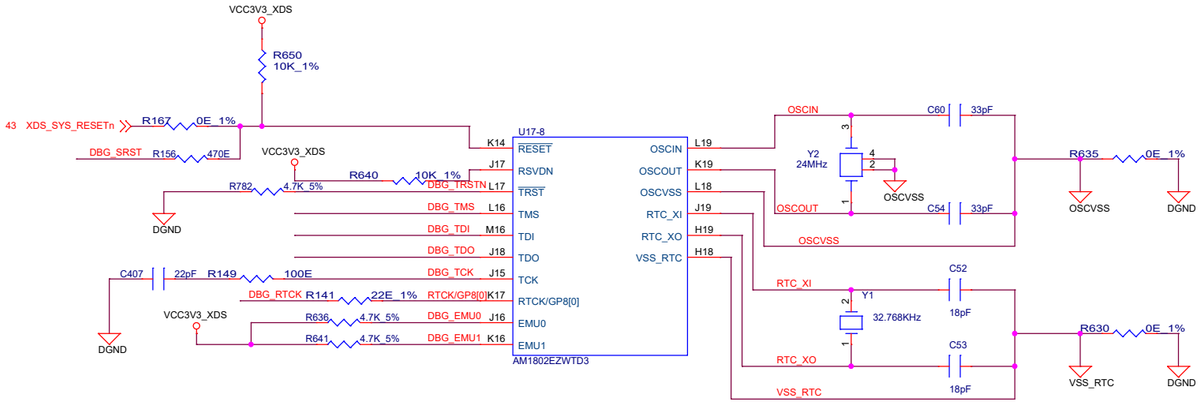
JTAG HEADER FOR AM1802



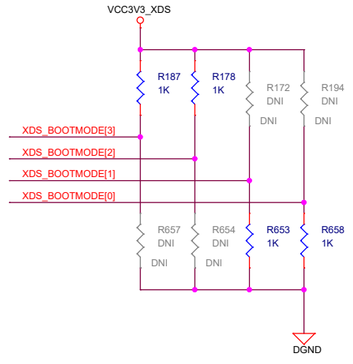


BOOTMODE: SPI1-FLASH ^B0001100





BOOT MODE



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Title	JTAG_XDS200_3	
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev
C		A
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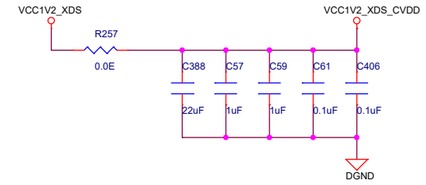
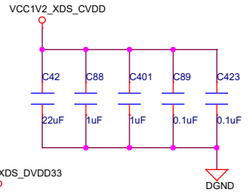
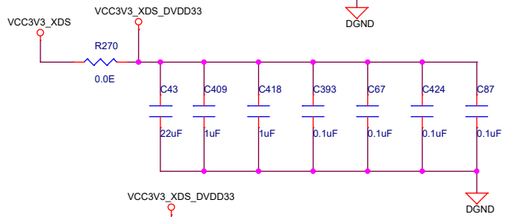
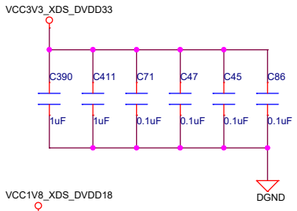
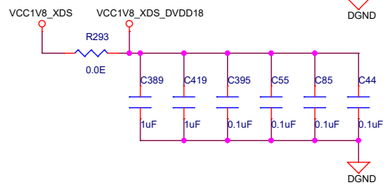
XDS200 DECAPS

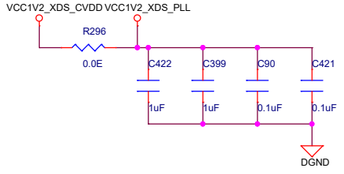




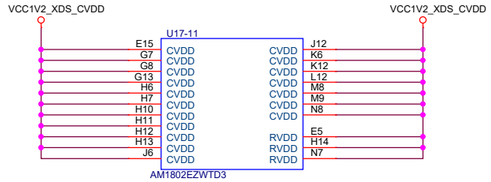


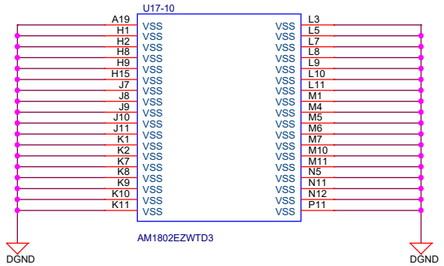


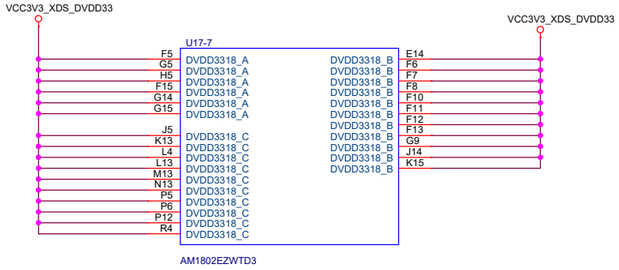


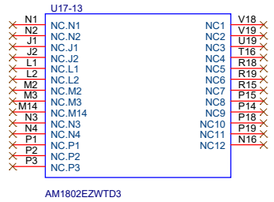


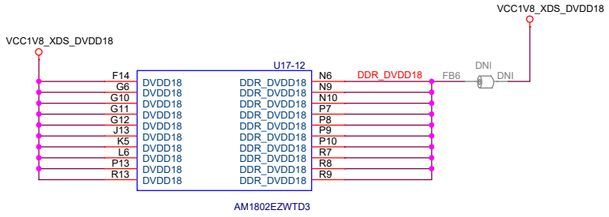
XDS200



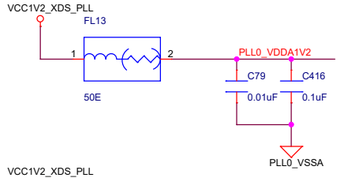


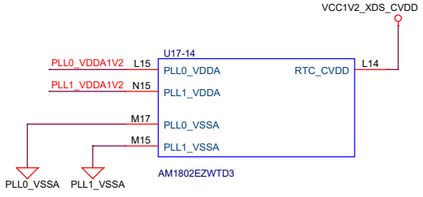


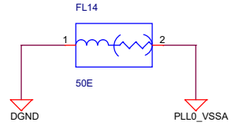


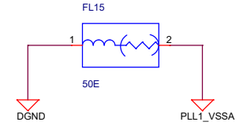


DDR PHY DOES NOT REQUIRE POWER IF NOT USED, PER DATA SHEET, BUT NOT A TYPICAL USE CASE.









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Title	JTAG_XDS200_4	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
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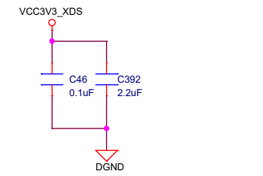
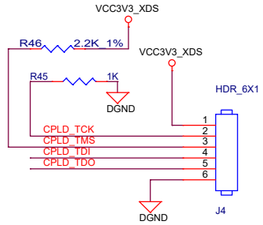
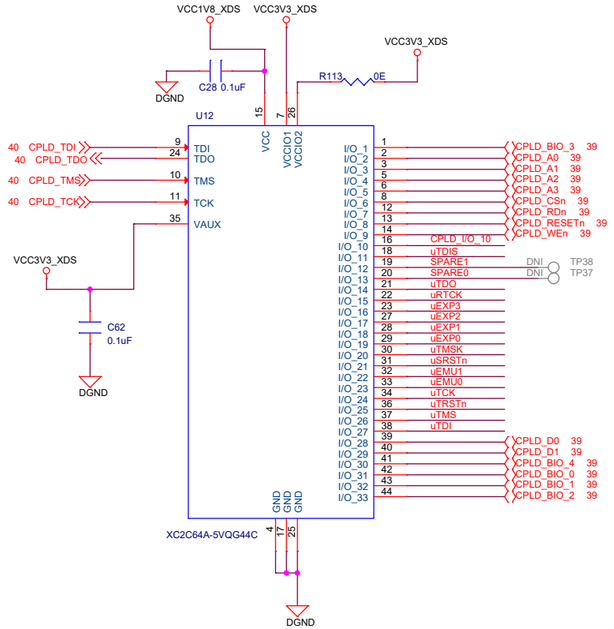
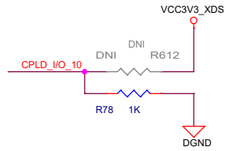
XDS200 CPLD

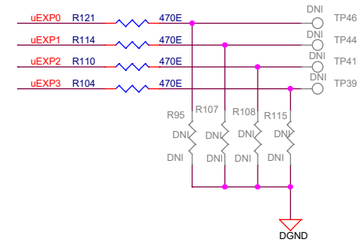


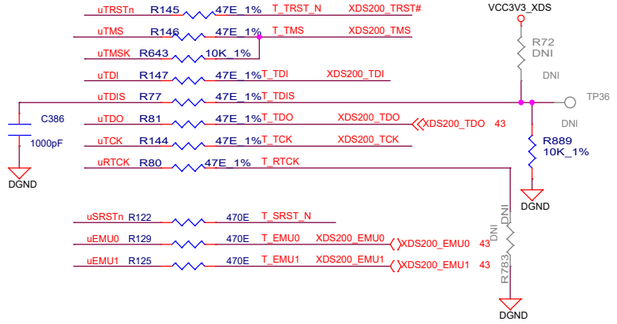


JTAG HEADER FOR CPLD

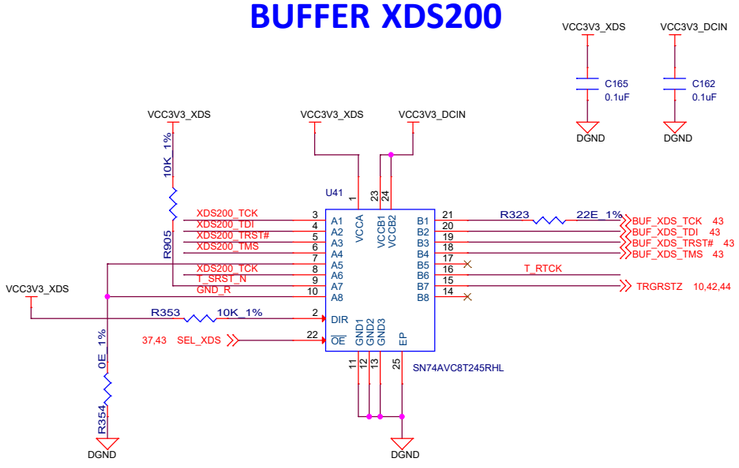




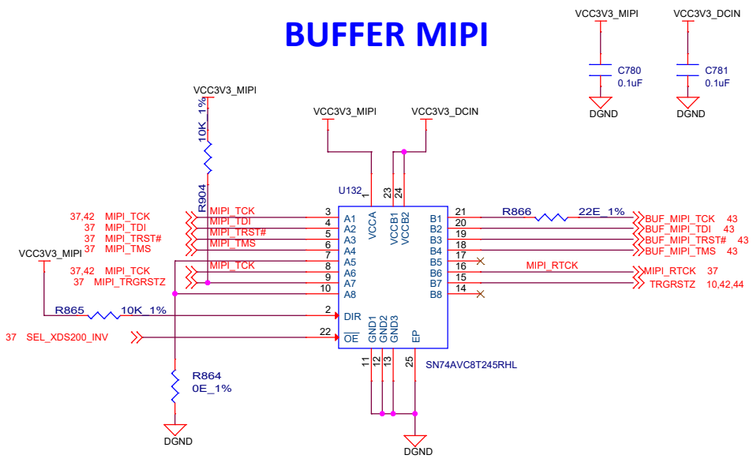




BUFFER XDS200



BUFFER MIPI



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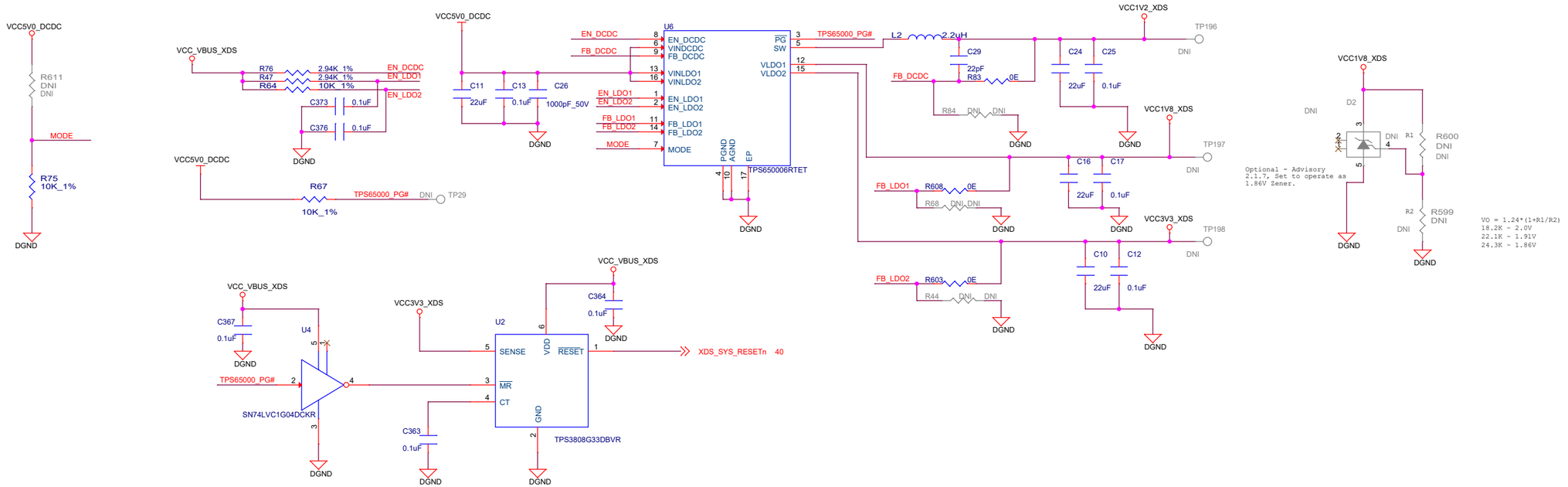
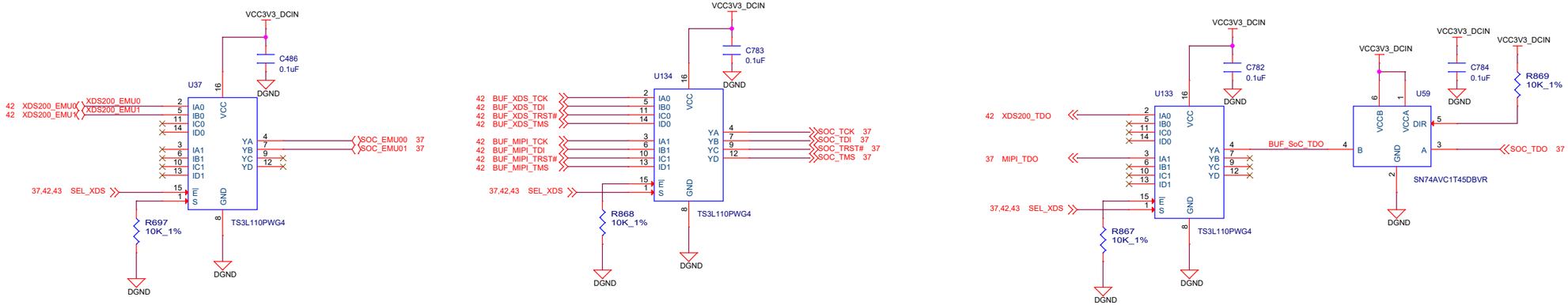
Title	JTAG_XDS200_CPLD	
Size: C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev: A
Date: Friday, February 02, 2018	Sheet 42 of 51	

XDS200 POWER



JTAG FET SWITCH



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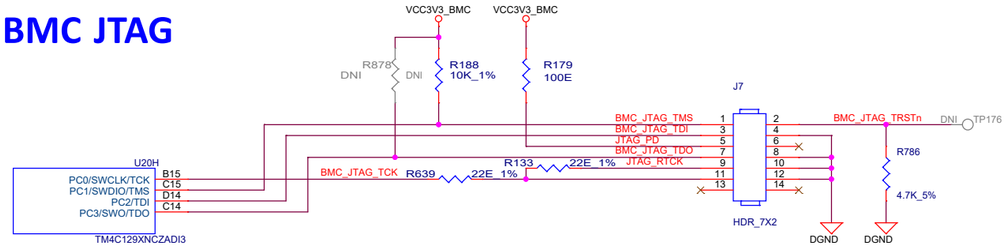
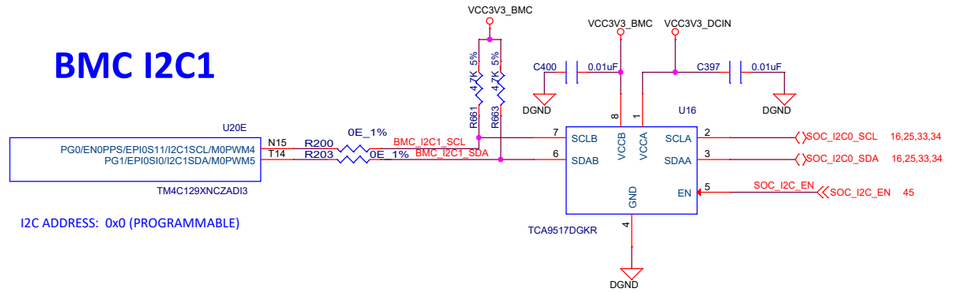
Title	XDS200 POWER	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 43 of 51	

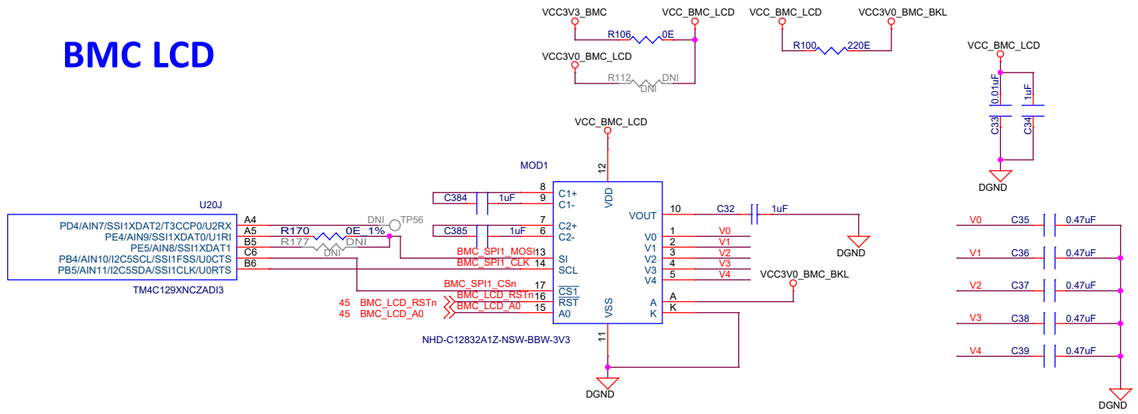
BMC JTAG



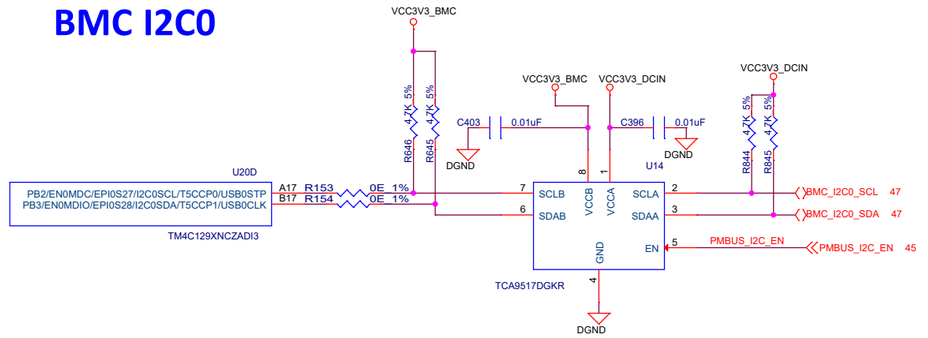
BMC I2C1



BMC LCD

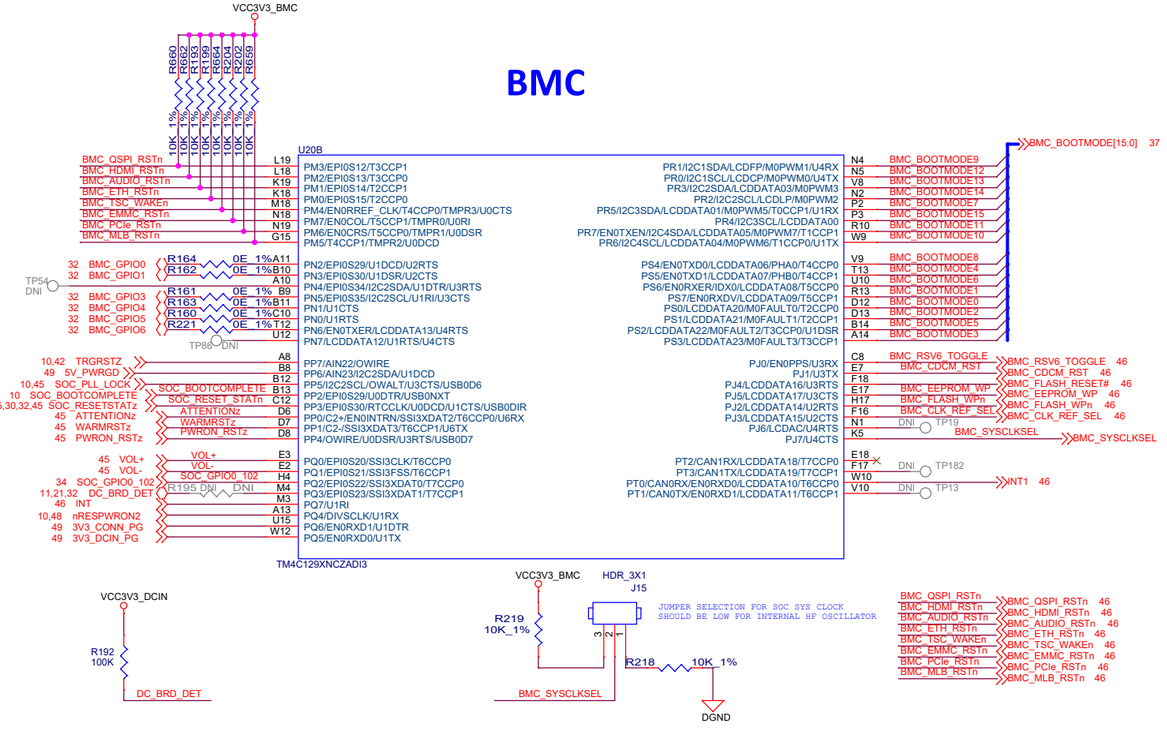


BMC I2C0

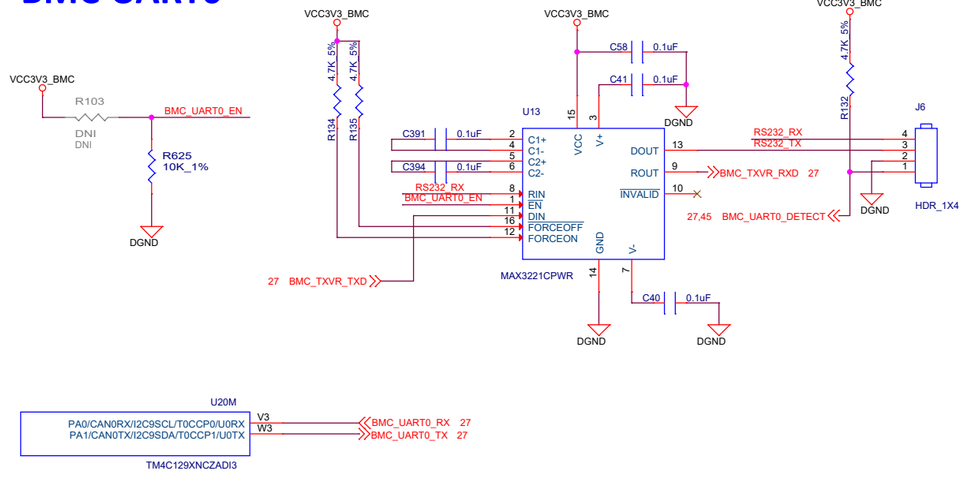


BMC TEST POINTS

BMC



BMC UART0



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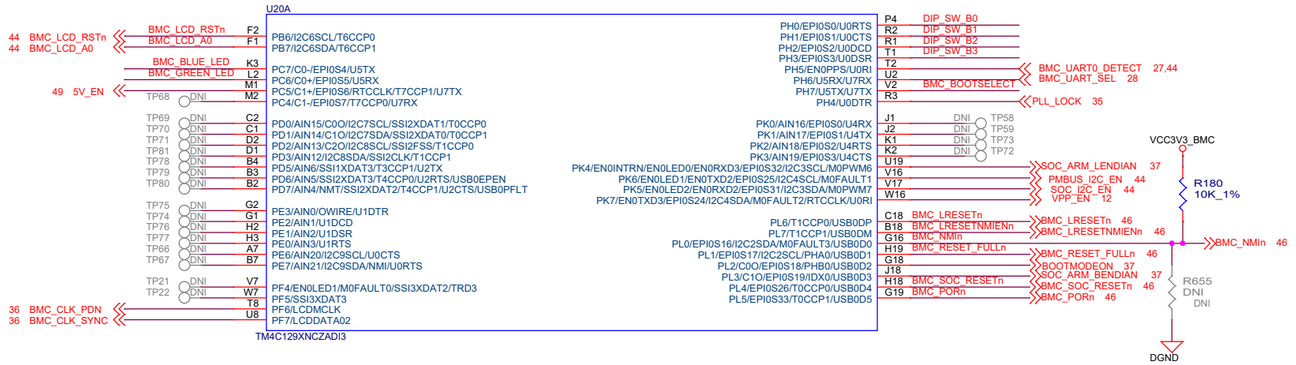
TEXAS INSTRUMENTS

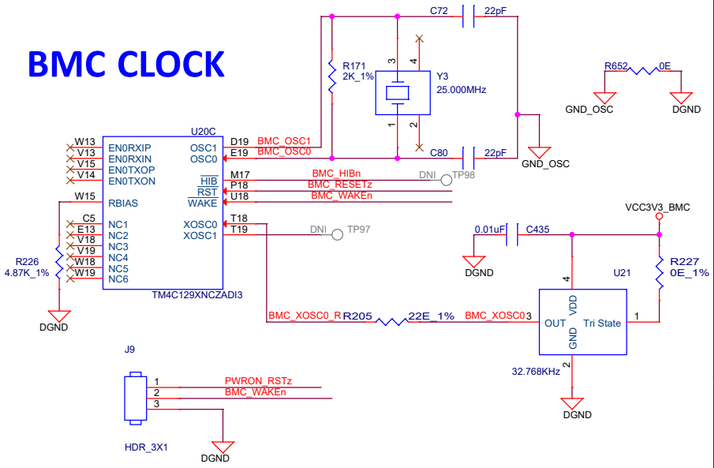
MISTRAL

Title	BOARD MANAGEMENT CONTROL_1	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 44 of 51	

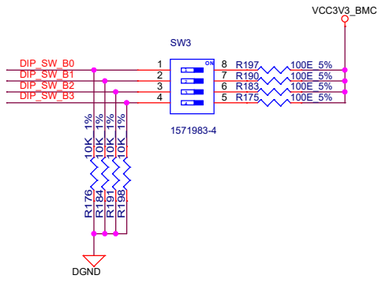
BMC



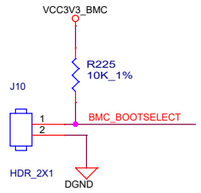
BMC CLOCK



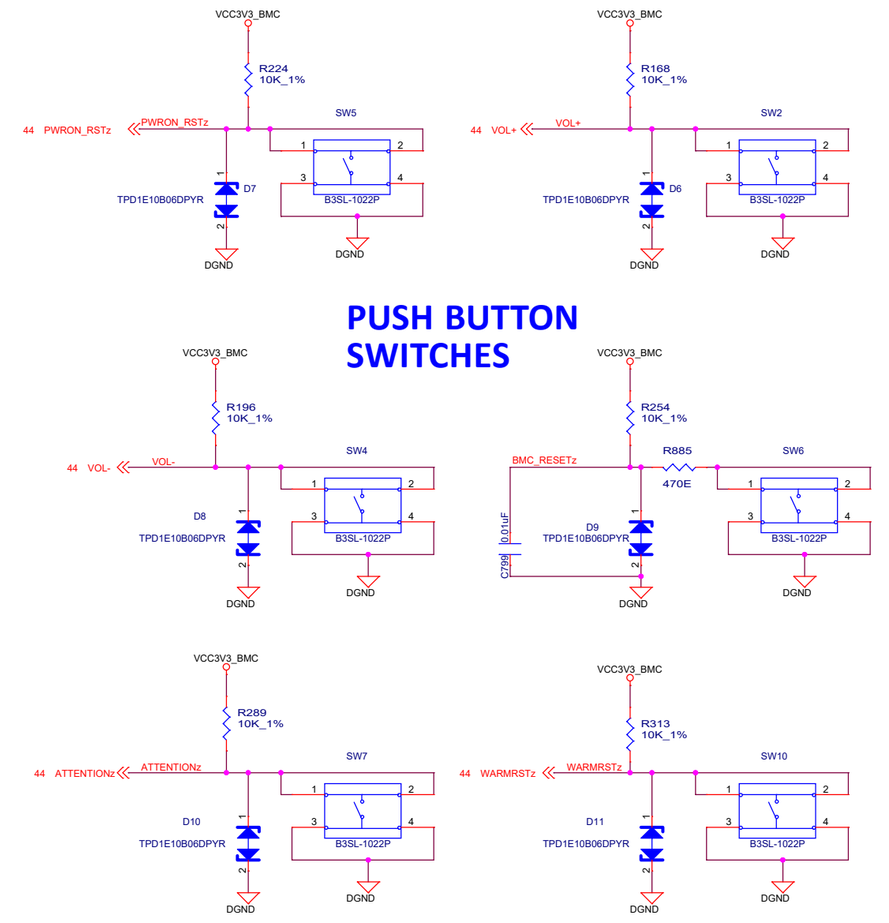
DIP SWITCH



BMC BOOT SELECT

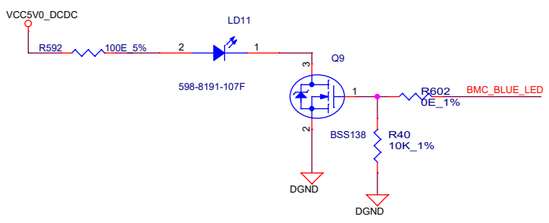


PUSH BUTTON SWITCHES

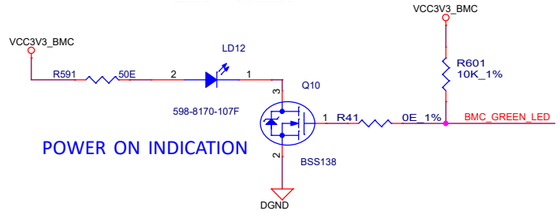


LEDS

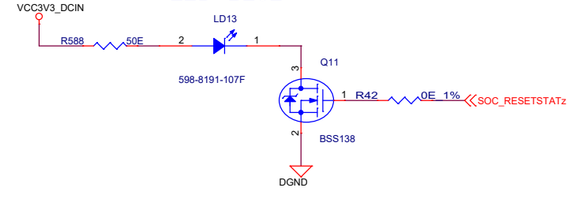
LED BLUE



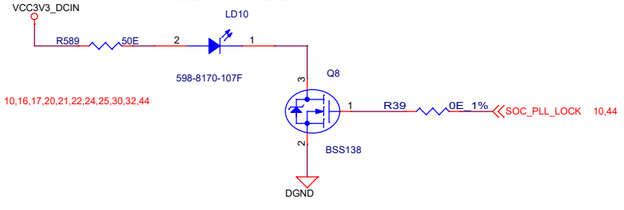
LED GREEN



LED BLUE



LED GREEN



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Title	BOARD MANAGEMENT CONTROL_2	
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev
C		A
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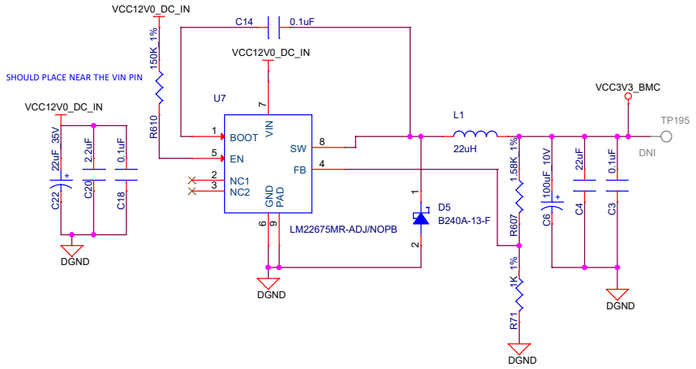
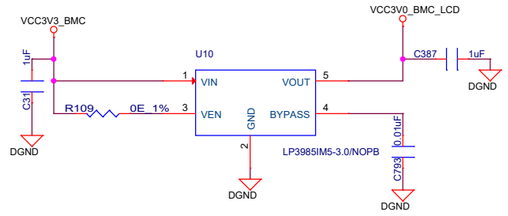
BMC 3.3V GENERATOR

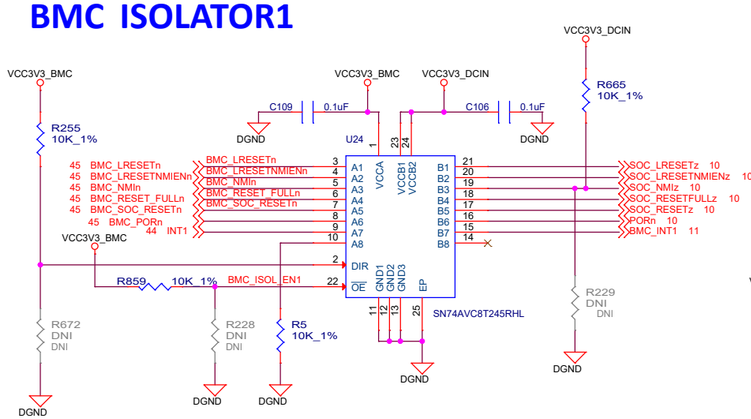


BMC LCD 3V GENERATOR

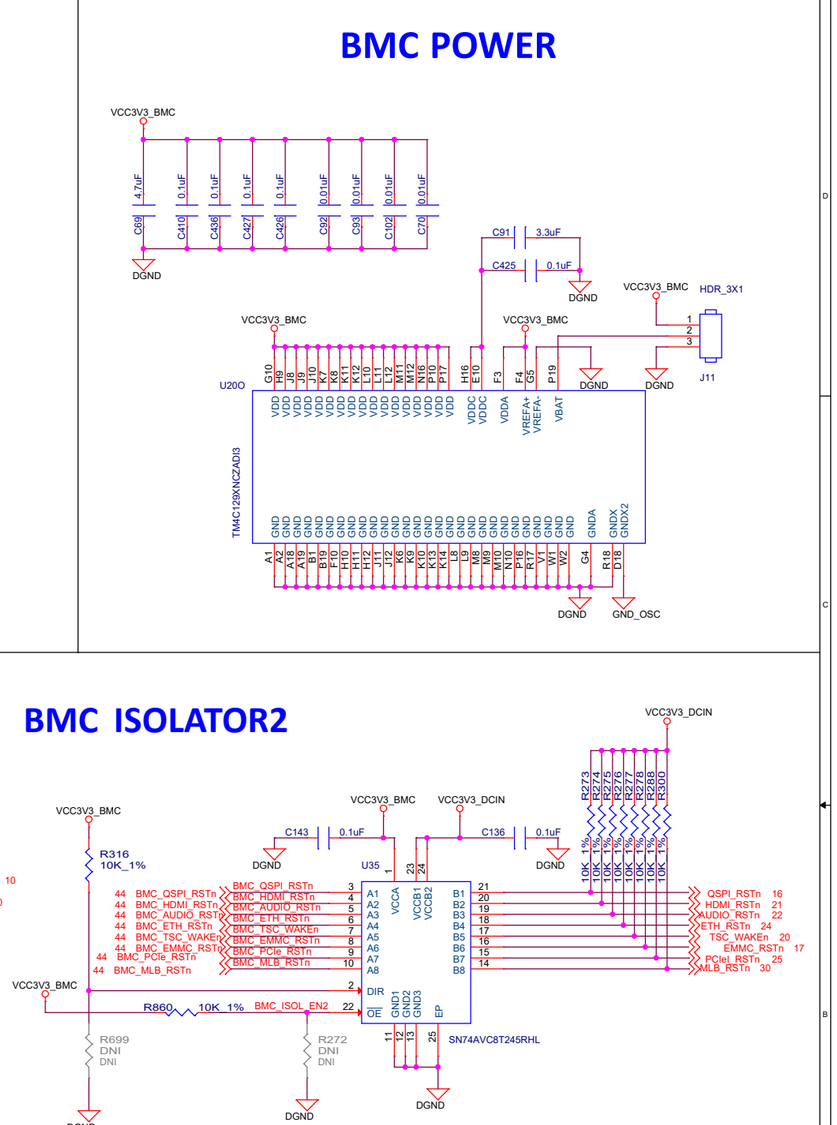


BMC POWER

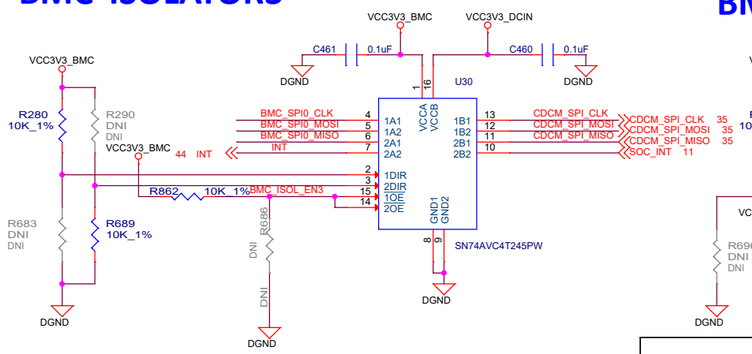
BMC ISOLATOR1



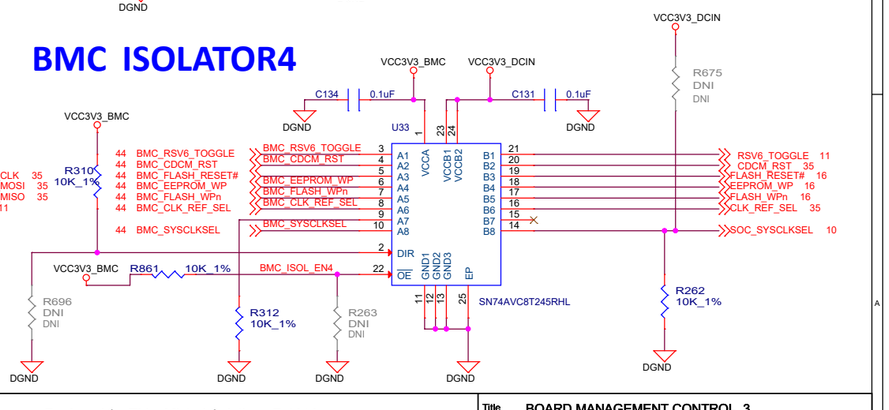
BMC ISOLATOR2



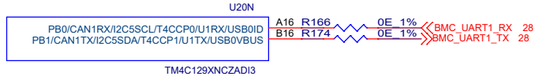
BMC ISOLATOR3



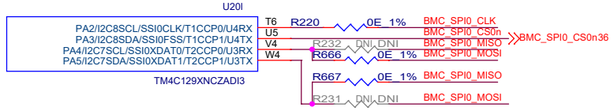
BMC ISOLATOR4



U20N

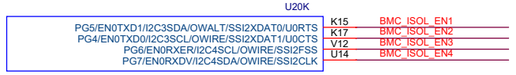


U20I



U20G

U20K



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Title	BOARD MANAGEMENT CONTROL_3		
Size	Variant Name = Proj PROC046 002 OPN# EVMK2GXS		Rev
C			A
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PMIC POWER SECTION 1

TPS65911A
BGA

U32-1

DCDC CTRL
External FET range 1.0V-6000mA

SMPS

VDD1
Output range 0.95V-1100mA

VDD2
Output range 1.35V-1500mA

VIO
3.3V-1100mA

GND

INA DEVICES

I2C ADDRESS: 0x40 : 3.3V

I2C ADDRESS: 0x41 : CVDD

I2C ADDRESS: 0x42 : CVDD1

I2C ADDRESS: 0x43 : 1.8V

I2C ADDRESS: 0x44 : DVDD_DDR

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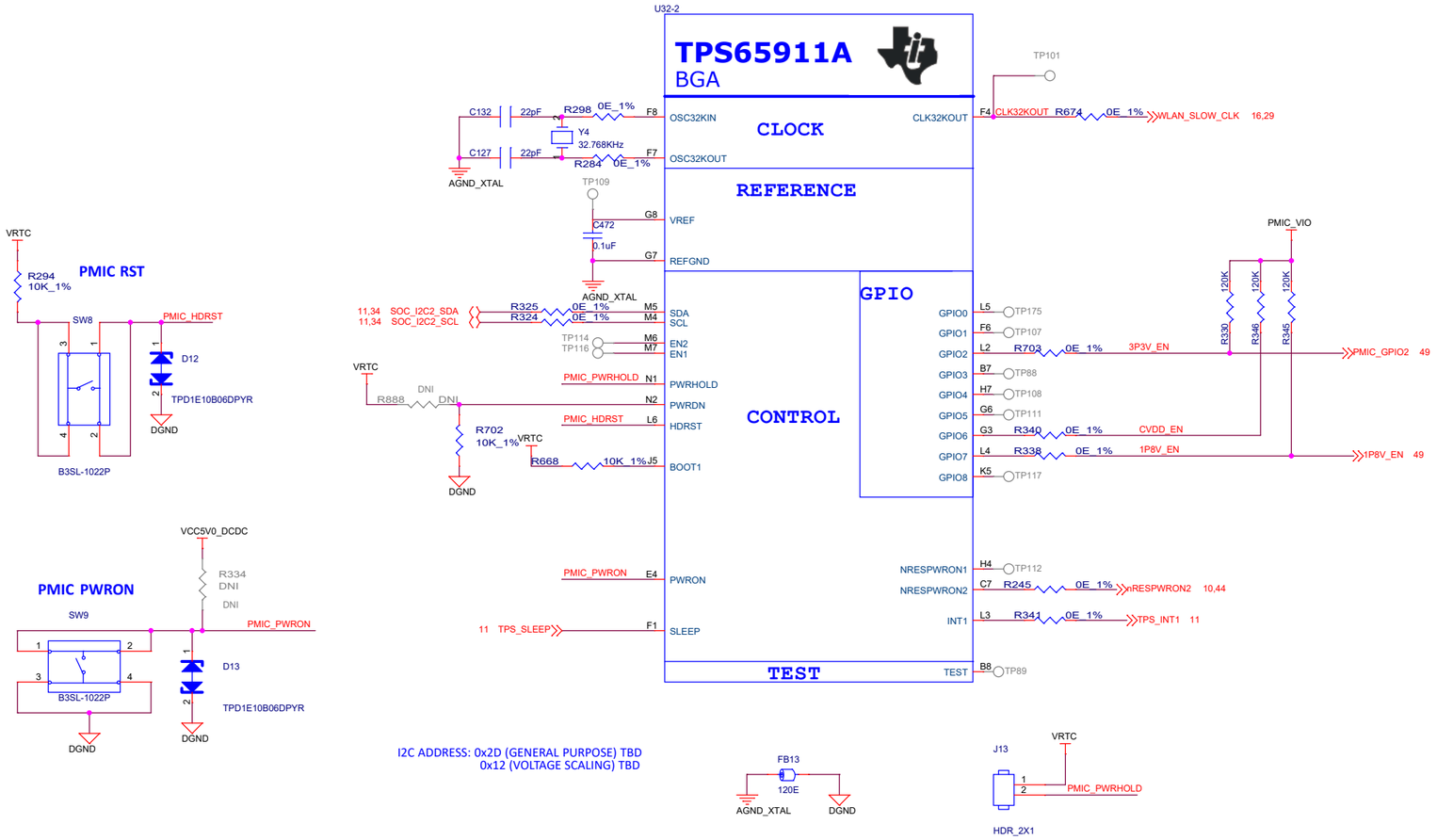
Title	PMIC POWER CIRCUIT 1	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 47 of 51	

PMIC POWER SECTION 2



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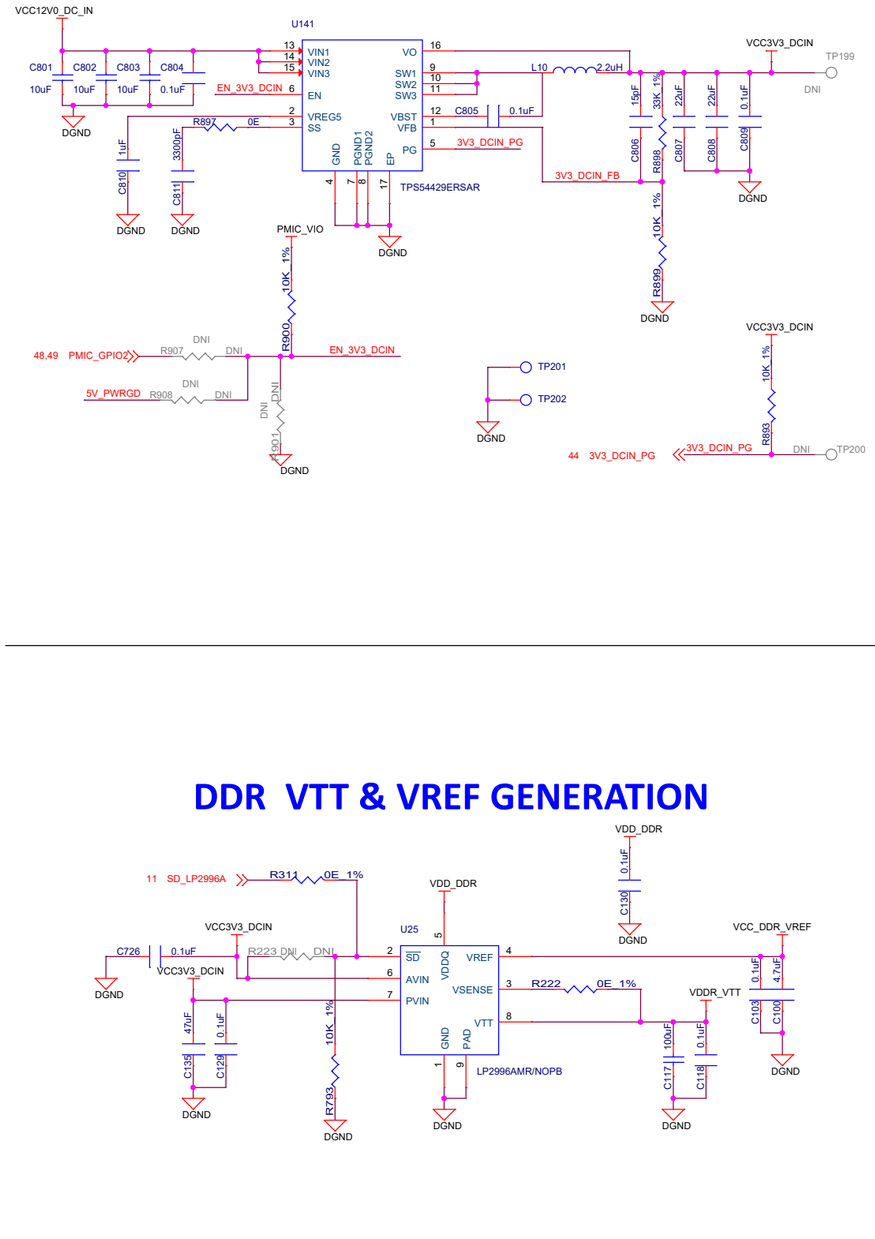
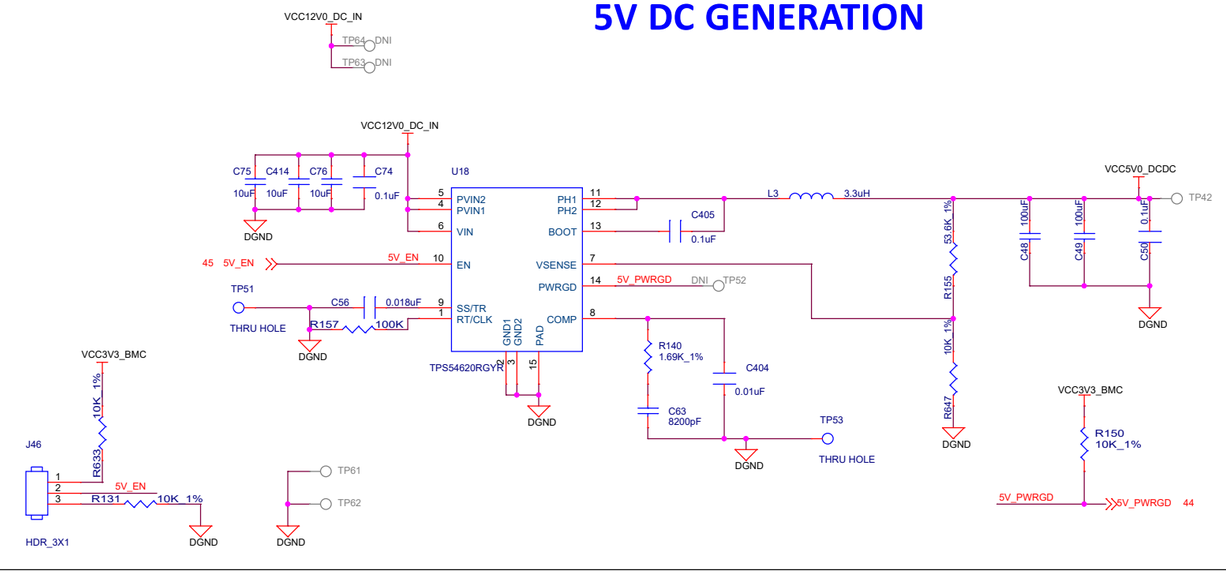
Title	PMIC POWER CIRCUIT 2	
Size: C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev: A
Date: Friday, February 02, 2018	Sheet 48 of 51	

3.3V DC

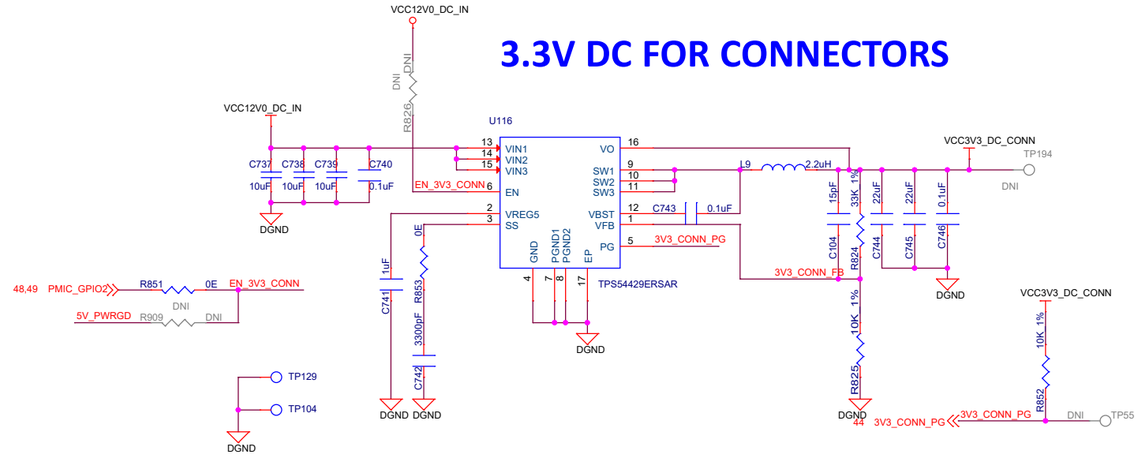


5V DC GENERATION

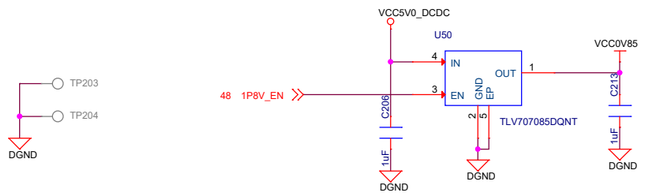


DDR VTT & VREF GENERATION

3.3V DC FOR CONNECTORS



0.85V OPTIONAL LDO FOR PCIE & USB MODULES



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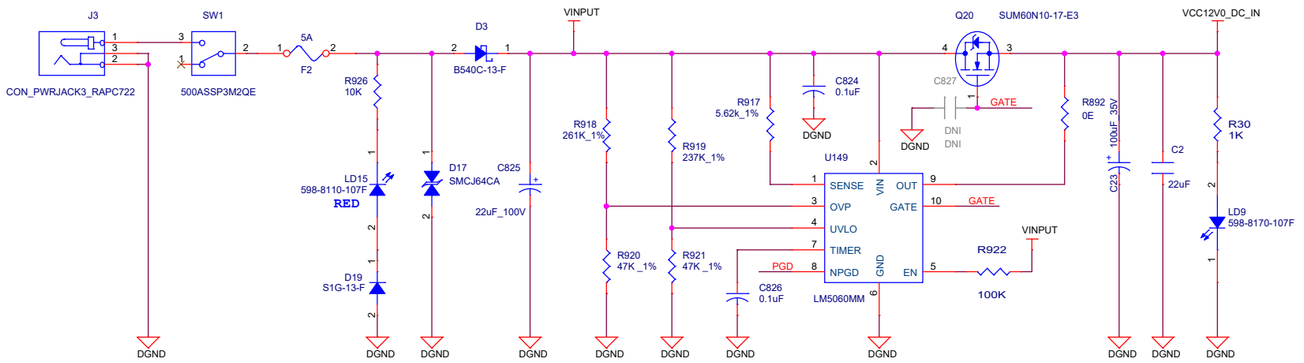
Title	DDR3L POWER	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 49 of 51	

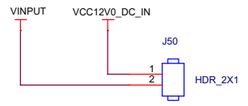
OVER VOLTAGE PROTECTION CIRCUIT



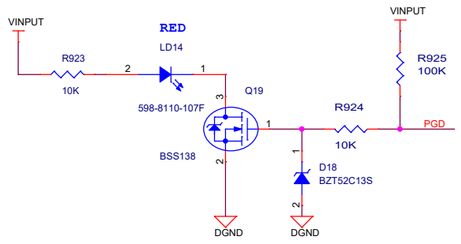
Note:-

Vinput nominal = 12V
I input max = 5A

Condition	LED Status(LD15)
Reverse Voltage	ON



Fault Indication



Condition	LED Status(LD14)
VINPUT between 11 to 13V & Current below 5A	OFF
VINPUT above 13V or below 11V & Current above 5A	ON

Note:-

When fault is indicated ,set to proper voltage and power cycle the board.

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Title	OVER VOLTAGE PROTECTION	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 50 of 51	

HARDWARE SCHEMATICS

RUBBER FEET

ACC4 720 | ACC24 720 | ACC25 720 | ACC26 720

ACC27 720 | ACC28 720 | ACC29 720 | ACC30 720

ACC31 720 | ACC32 720 | ACC33 720 | ACC34 720

SCREWS AND NUTS

MH9 PMSSS 440 0063 PH | MH11 PMSSS 440 0063 PH | MH12 PMSSS 440 0063 PH

MH13 PMSSS 440 0063 PH | MH14 PMSSS 440 0063 PH | MH15 PMSSS 440 0063 PH

MH16 PMSSS 440 0063 PH | MH17 PMSSS 440 0063 PH | MH18 PMSSS 440 0063 PH

MH19 PMSSS 440 0063 PH | MH20 PMSSS 440 0063 PH | MH21 PMSSS 440 0063 PH

MH10 PMSSS 440 0025 PH | MH22 PMSSS 440 0025 PH | MH23 PMSSS 440 0025 PH

MH24 PMSSS 440 0025 PH | MH25 PMSSS 440 0025 PH | MH26 PMSSS 440 0025 PH

MH27 PMSSS 440 0025 PH | MH28 PMSSS 440 0025 PH | MH29 PMSSS 440 0025 PH

MH30 PMSSS 440 0025 PH | MH31 PMSSS 440 0025 PH | MH32 PMSSS 440 0025 PH

ACC9 NSS-4-12-01 | ACC6 NSS-4-12-01 | ACC7 NSS-4-12-01 | ACC8 NSS-4-12-01

ACC3 HEX NUT_#4-40 | ACC21 HEX NUT_#4-40 | ACC22 HEX NUT_#4-40 | ACC23 HEX NUT_#4-40

FIDUCIALS

FID1 | FID2 | FID3 | FID4 | FID5 | FID6

STANDOFFs

ACC2 3482 | ACC10 3482 | ACC11 3482 | ACC12 3482

ACC13 3482 | ACC14 3482 | ACC15 3482 | ACC16 3482

ACC17 3482 | ACC18 3482 | ACC19 3482 | ACC20 3482

BARE PCB

PCB1 K2G EVM Bare PCB

JUMPER

ACC5 382811-8 | ACC35 382811-8 | ACC36 382811-8 | ACC37 382811-8

LABELS

Board Serial No. | ORDERABLE PART NO

LBL1 PCB LABEL SERIAL NUMBER | LBL2 PCB LABEL EVMK2GXS

LOGOS

PCB LOGO DNI | PCB LOGO DNI | PCB LOGO DNI | PCB LOGO DNI

Socket & Processor as Accessories

ACC39 C14590 | ACC40 X66AK2G12ABY100

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Title	HARDWARE SCHEMATICS	
Size C	Variant Name = Proj PROC046 002 OPN# EVMK2GXS	Rev A
Date: Friday, February 02, 2018	Sheet 51 of 51	

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