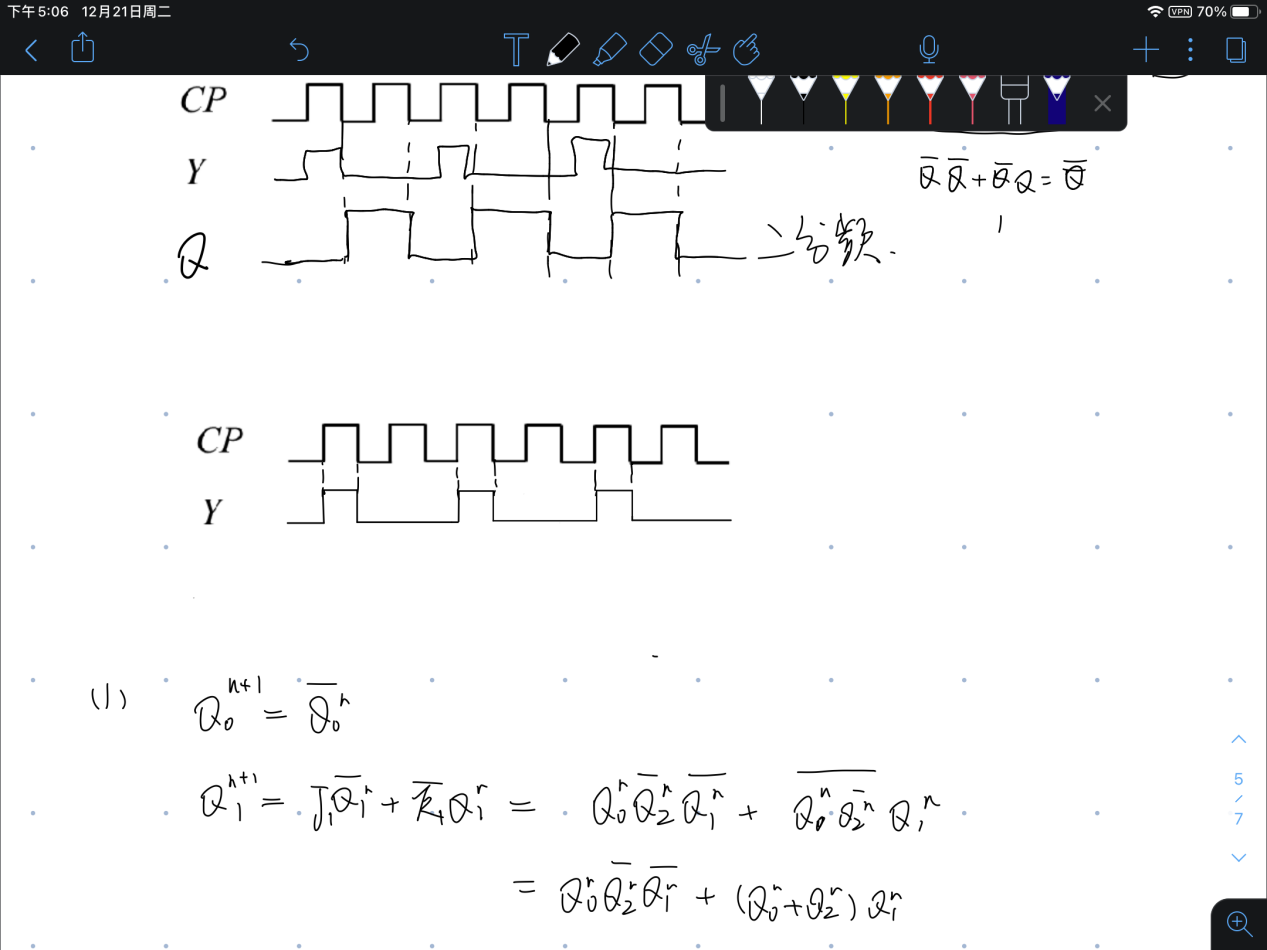
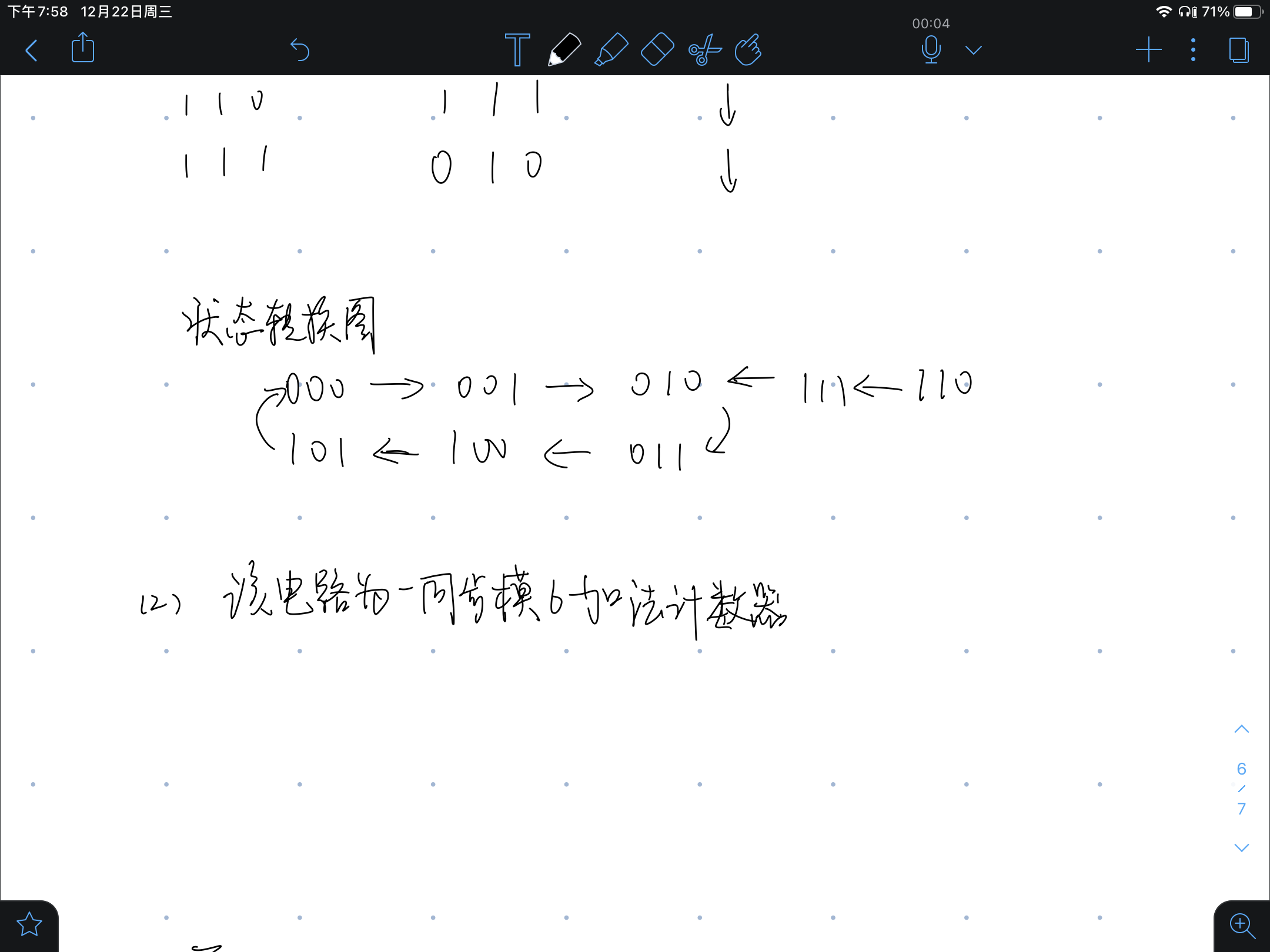
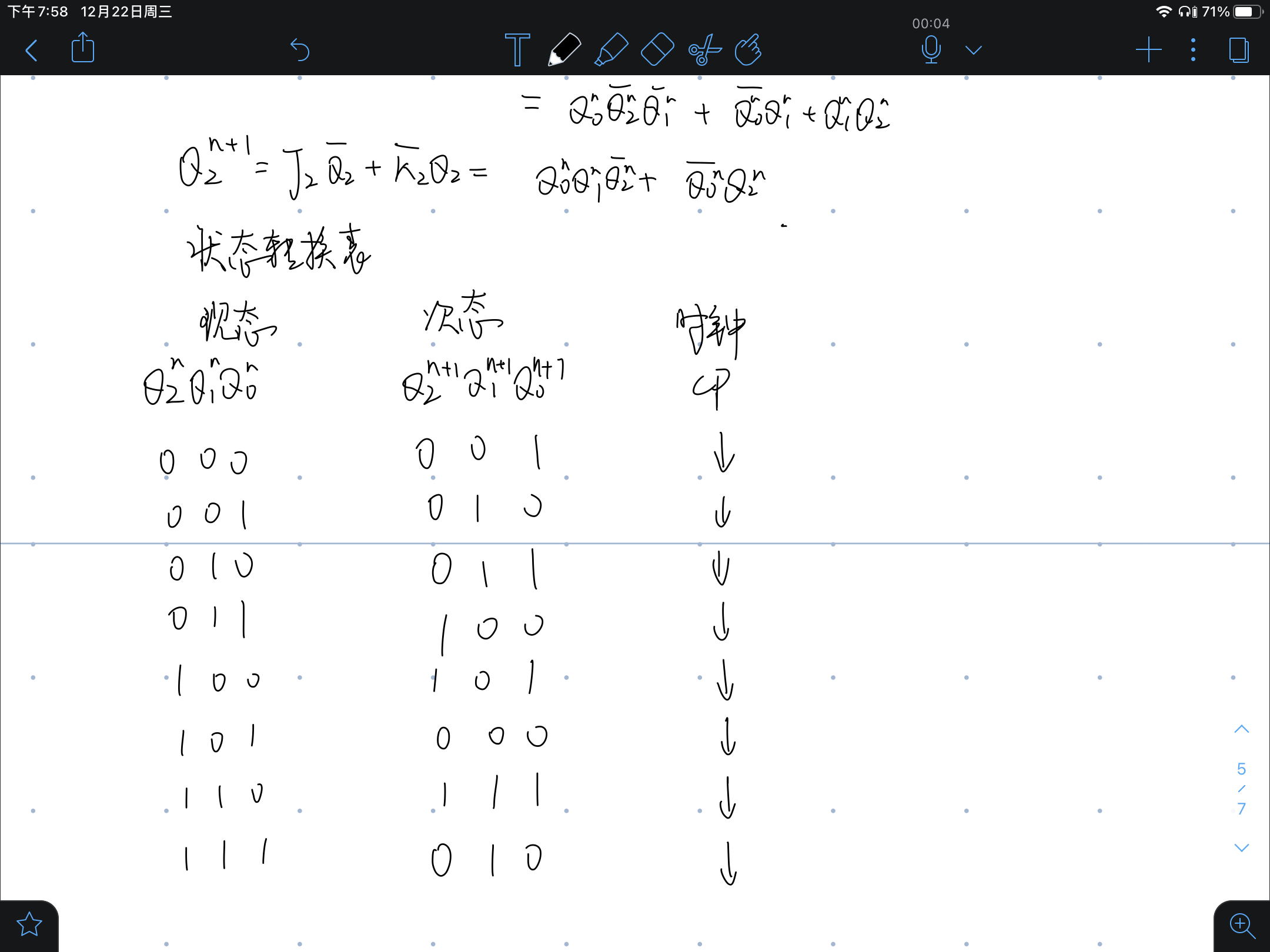
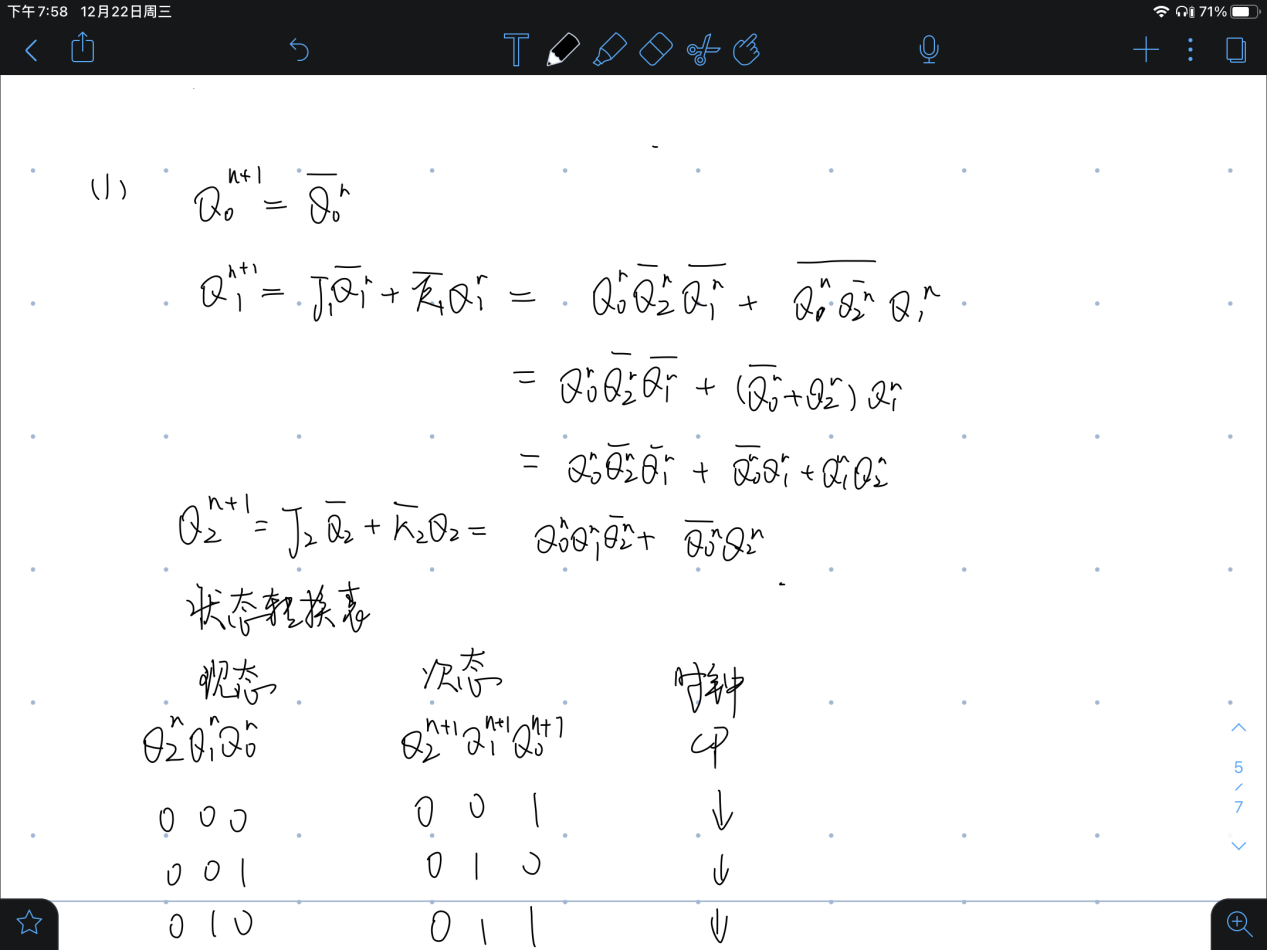
作业4

200110631 张景昊

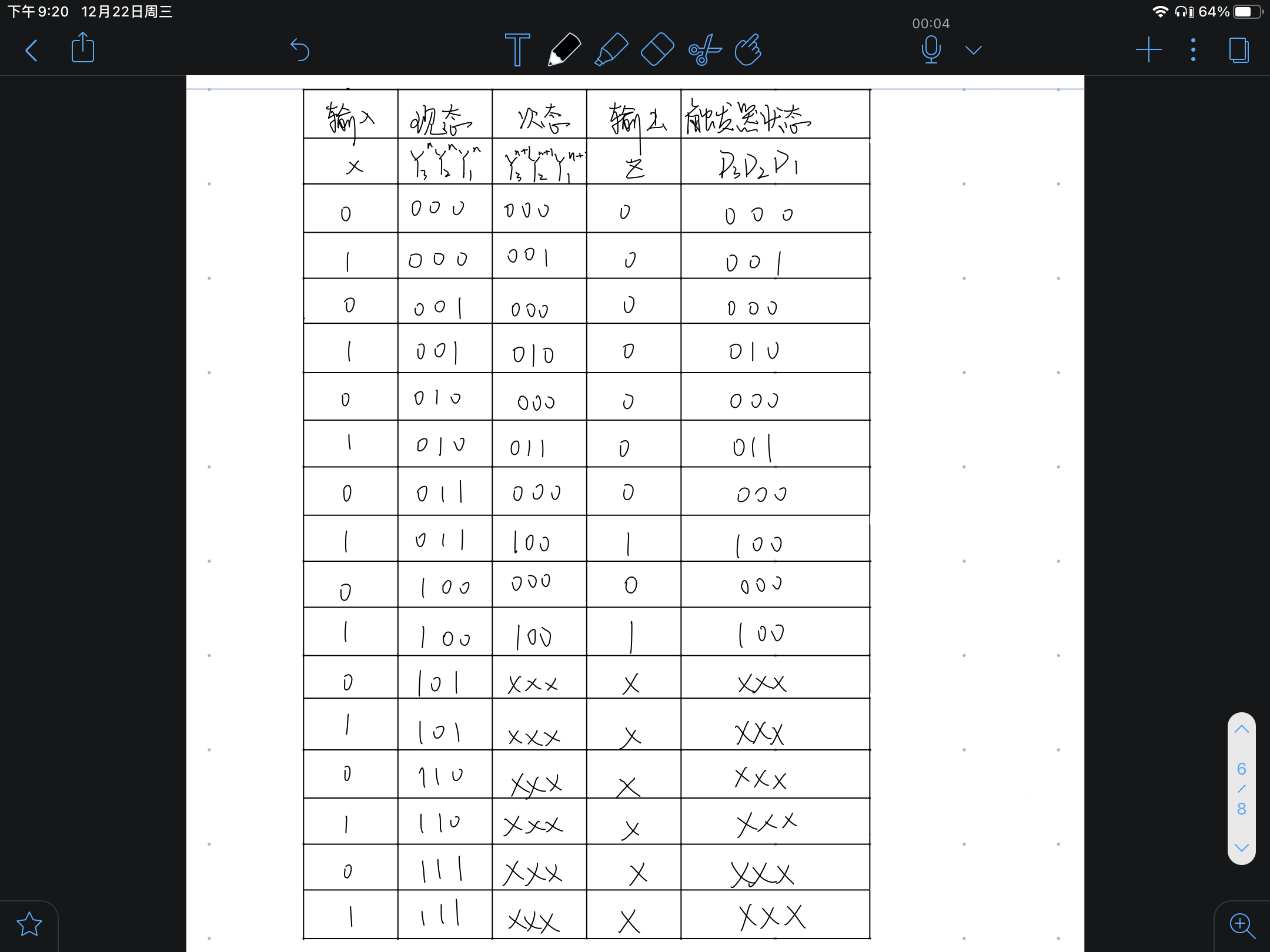
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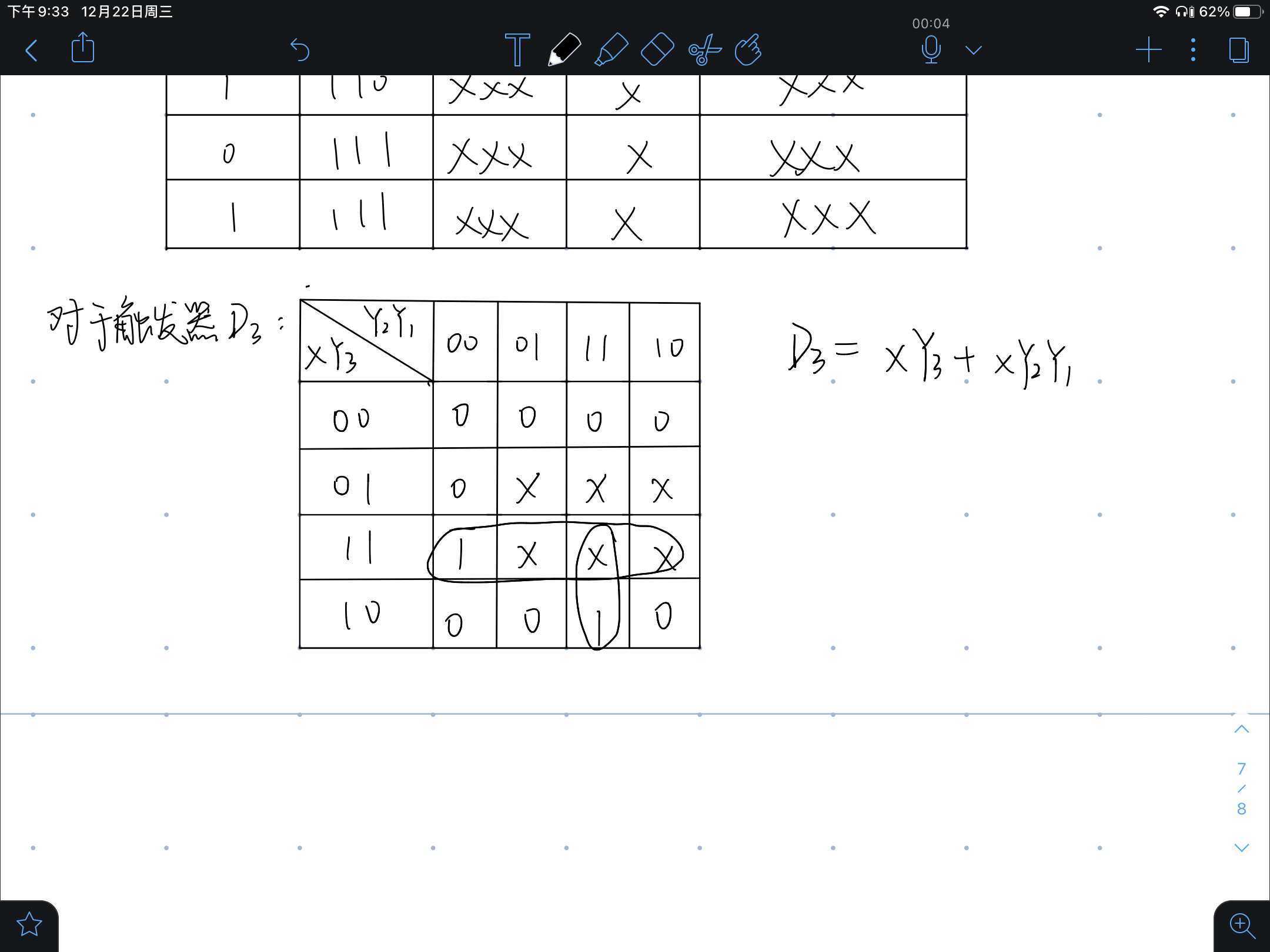


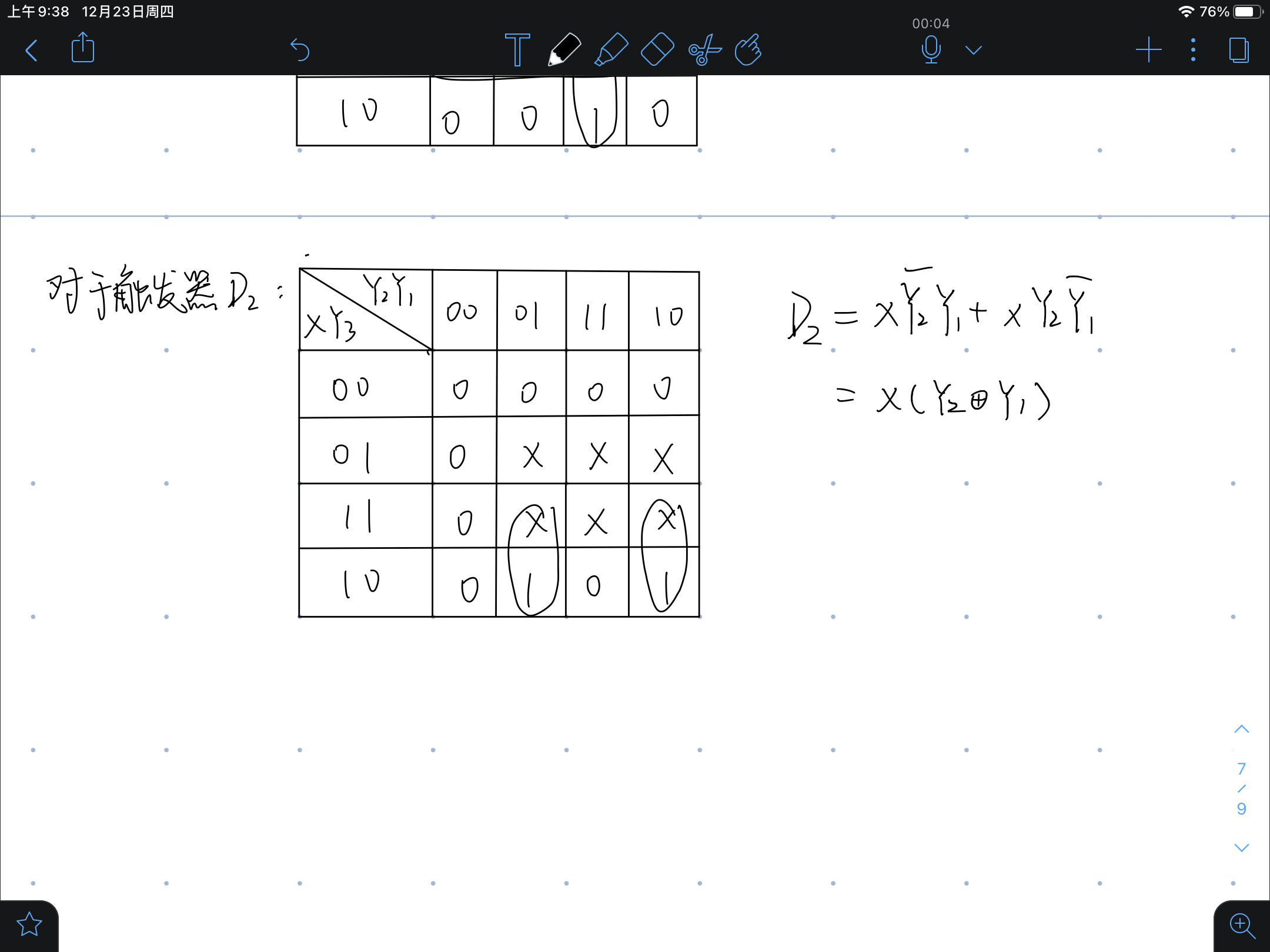
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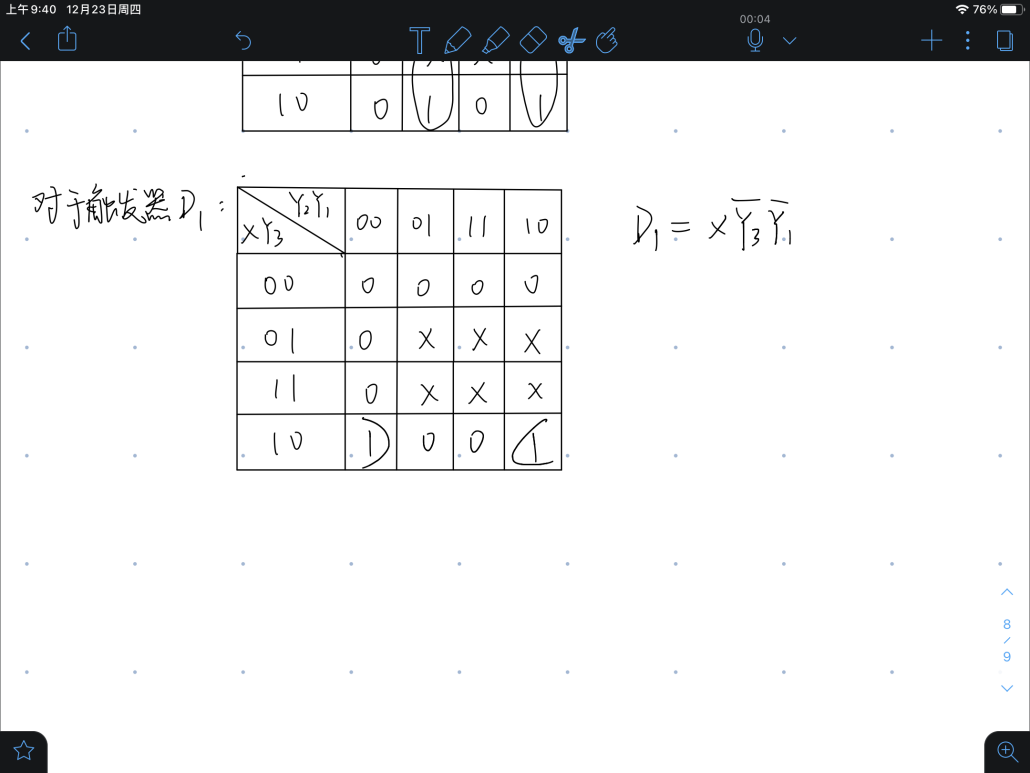


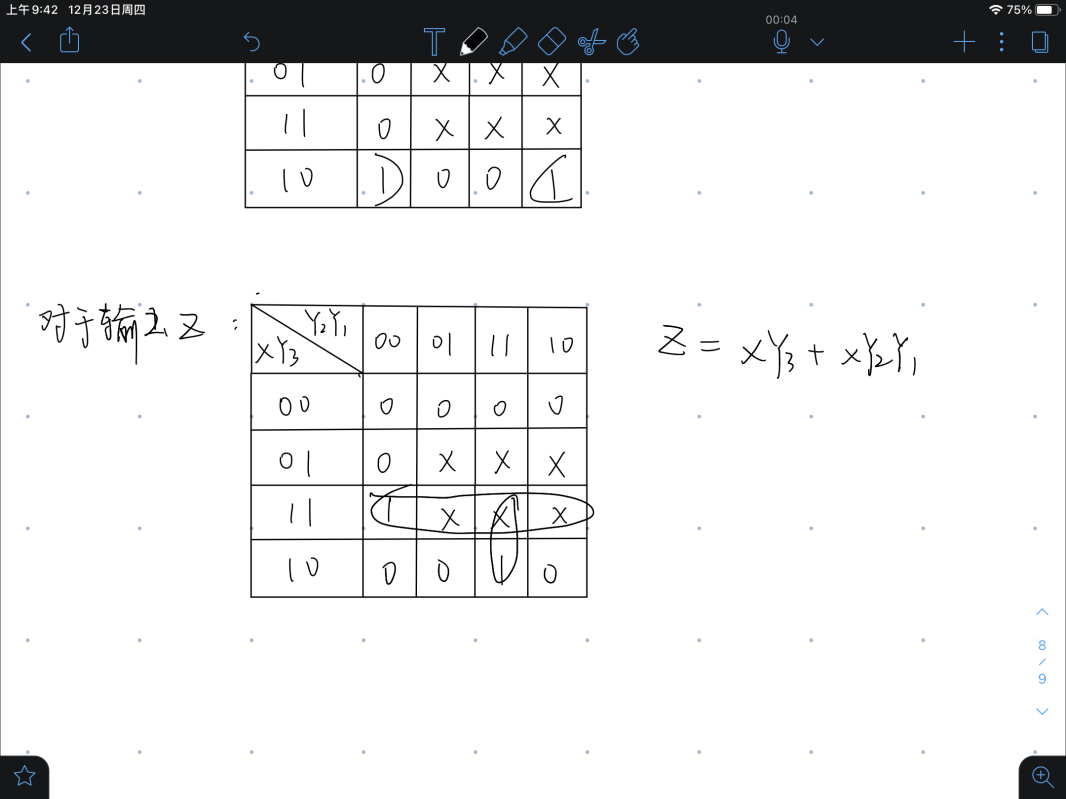
3.











电路设计如下：

