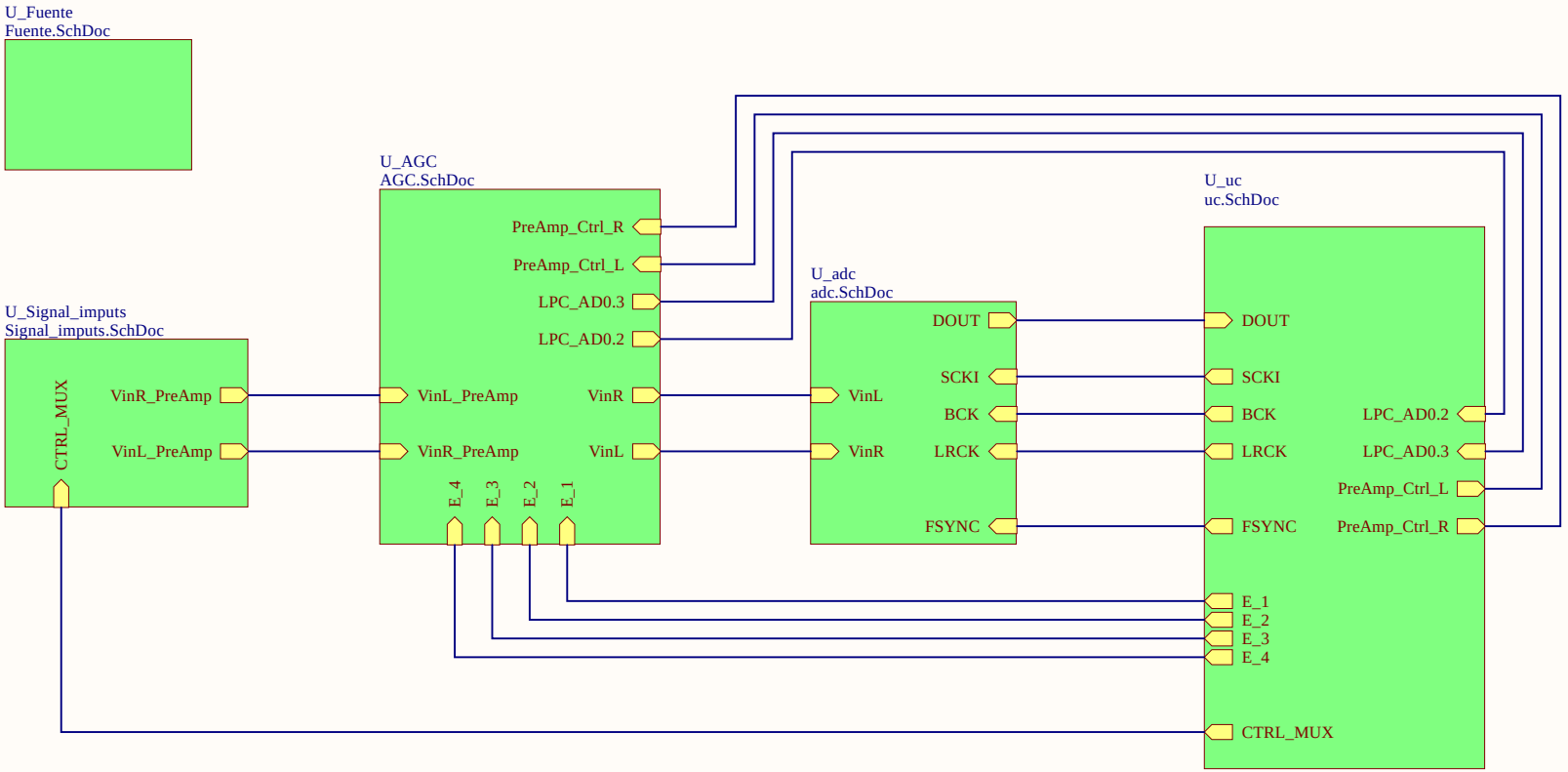
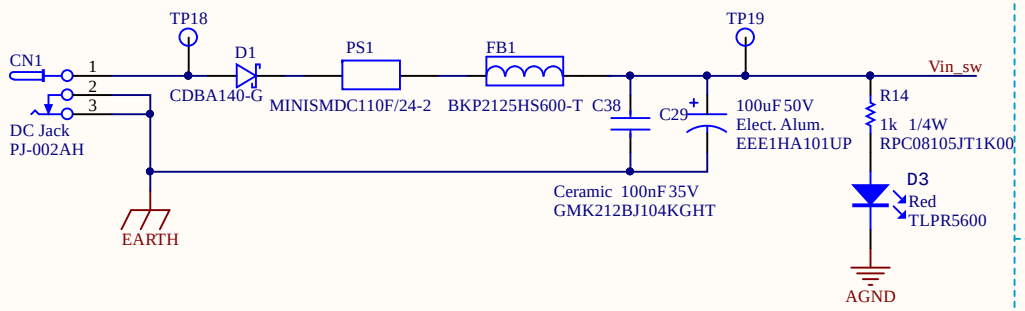
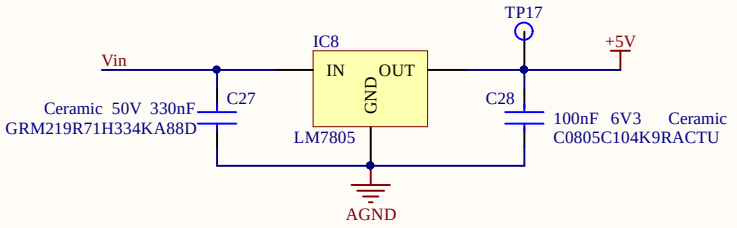


Title	Analizador_de_Espectro_SCH	
Size: A4	Number:	Revision: Rev1.0
Date: 28/09/2017	Sheet 1of 6	
File: E:\Nueva carpeta\..\Analizador_de_espectro.SchDoc	Drawn By: Hojnadel - Salazar Ato	

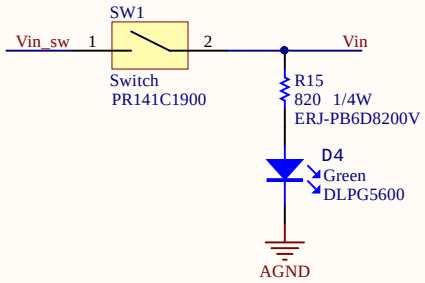
Input Voltage



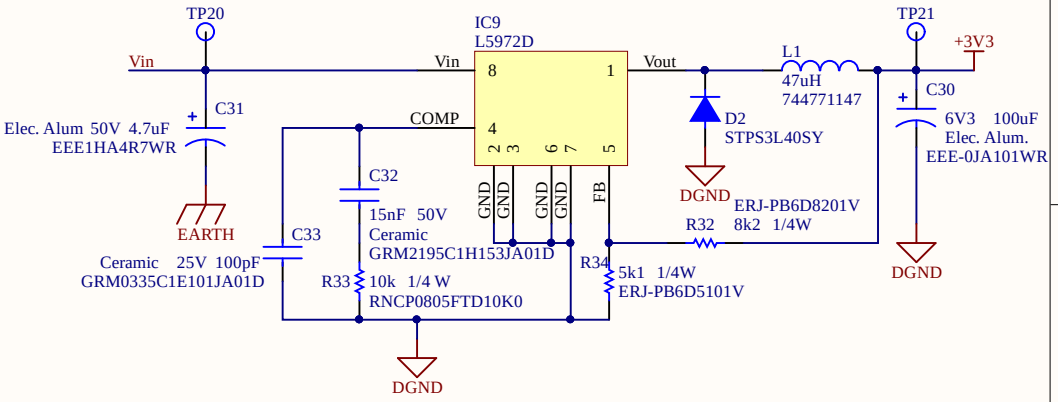
+5V Source



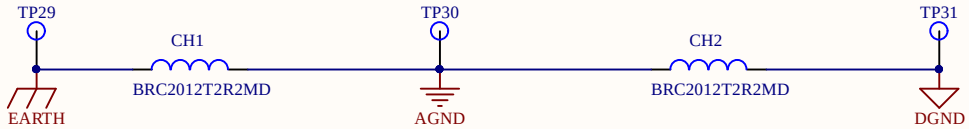
Push Button Switch Connector



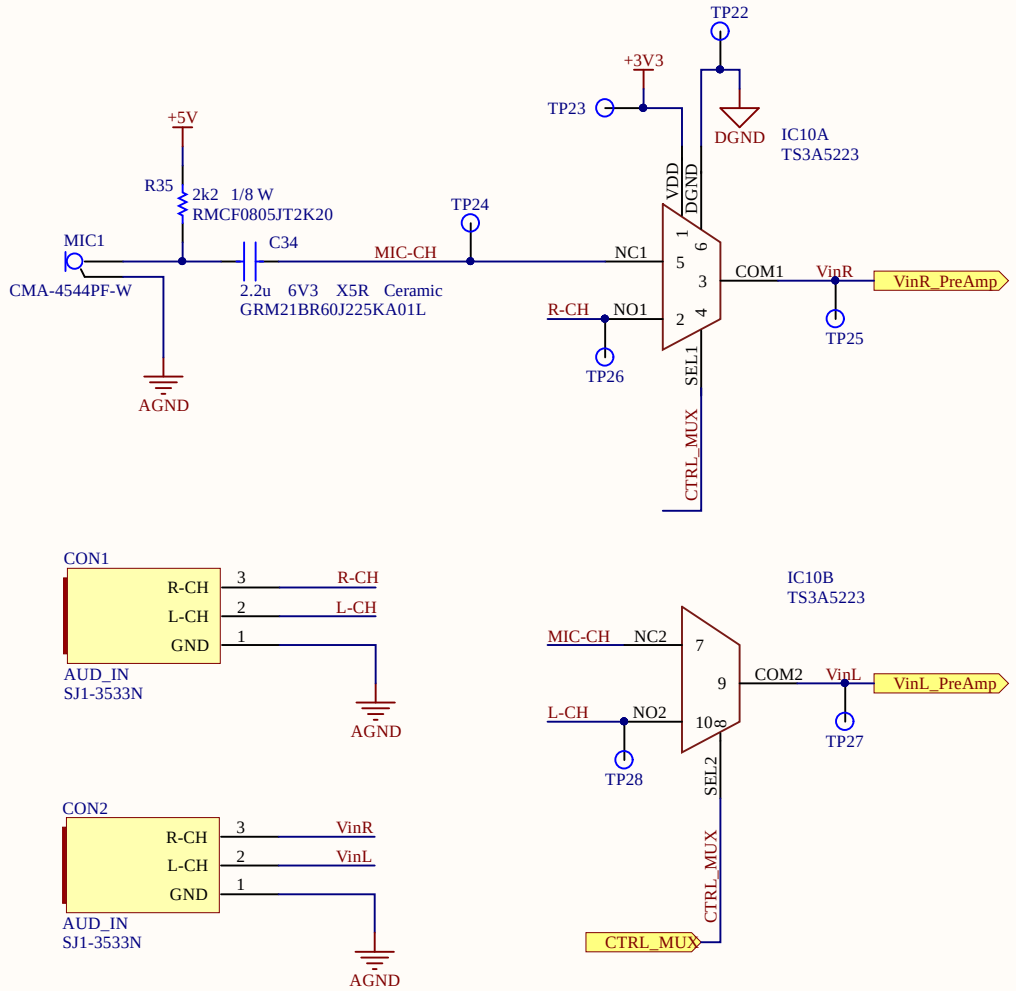
+3V3 Source



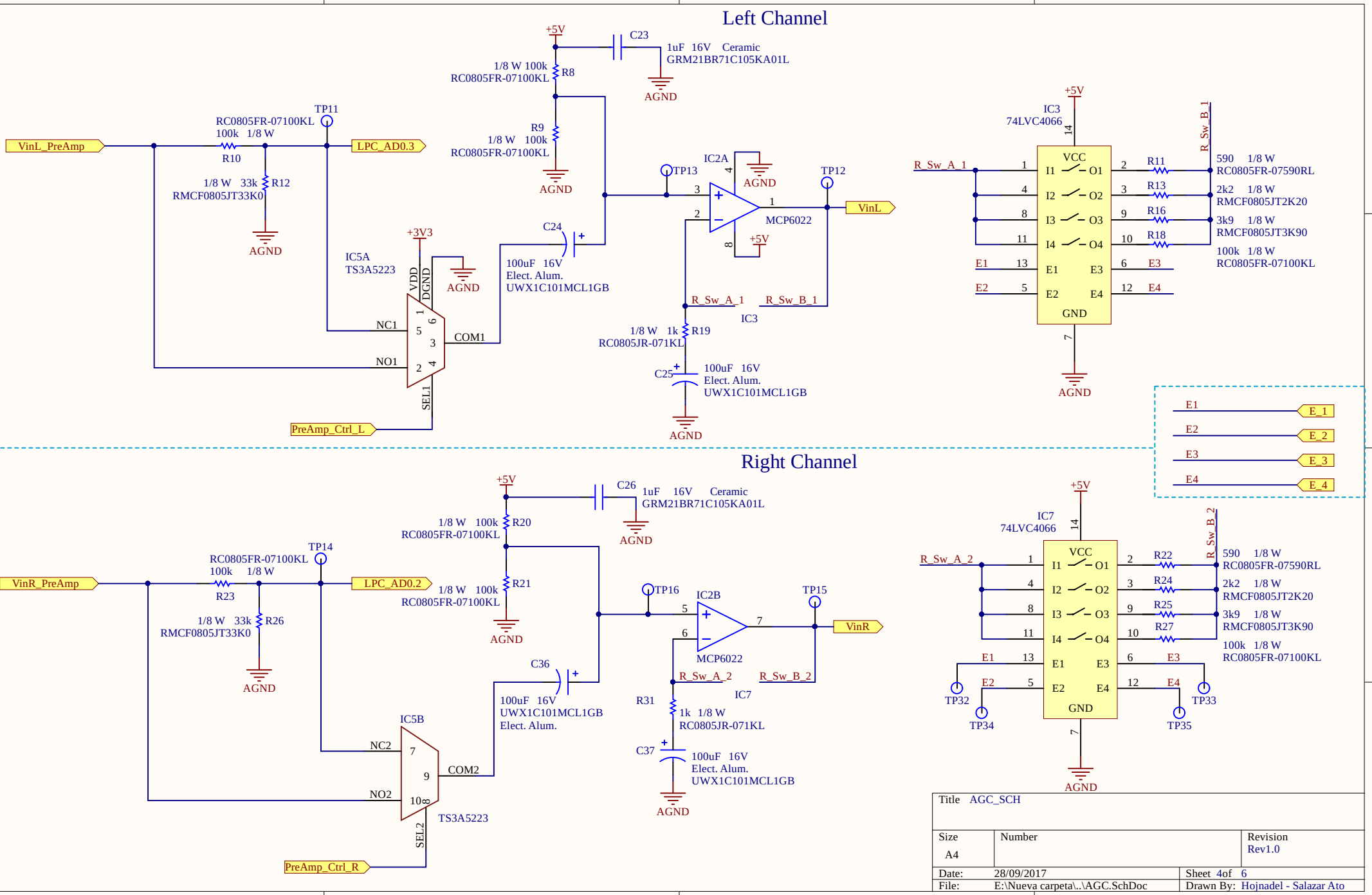
Ground Planes

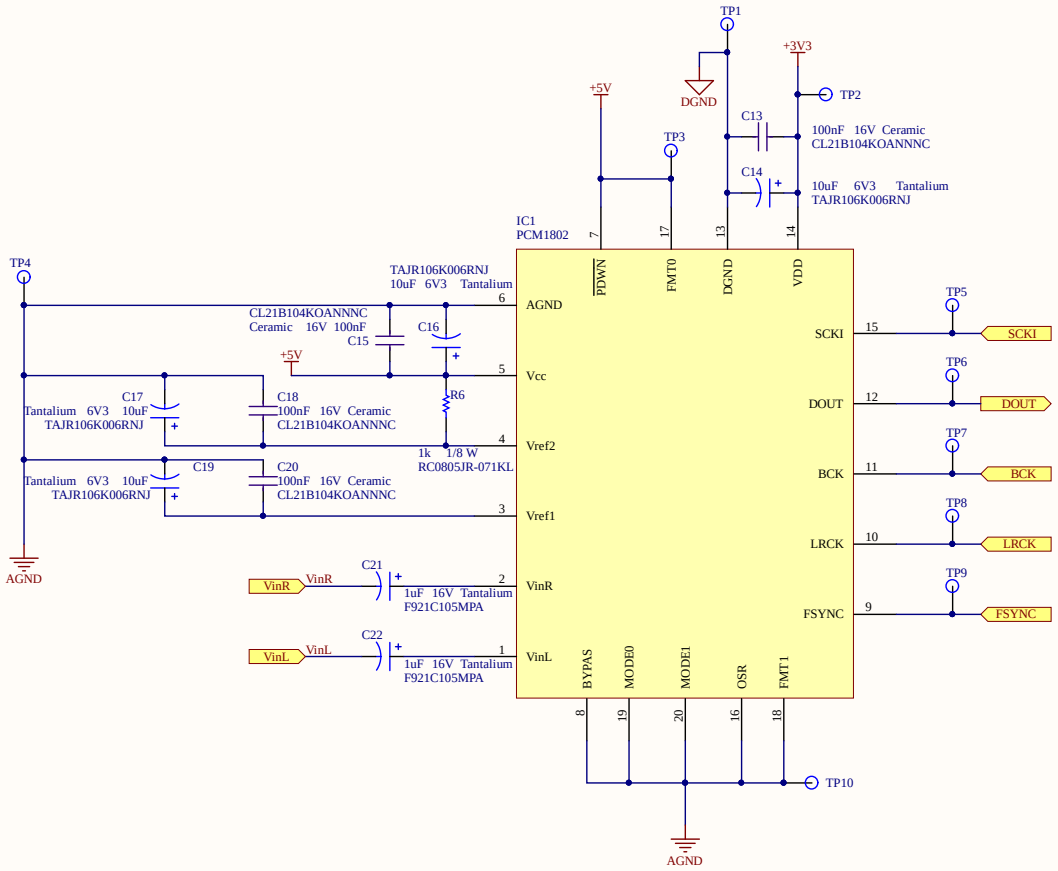


Title Fuente_SCH		
Size A4	Number	Revision Rev1.0
Date: 28/09/2017		Sheet 2of 6
File: E:\Nueva carpeta\..\Fuente.SchDoc		Drawn By: Hojnadel - Salazar Ato

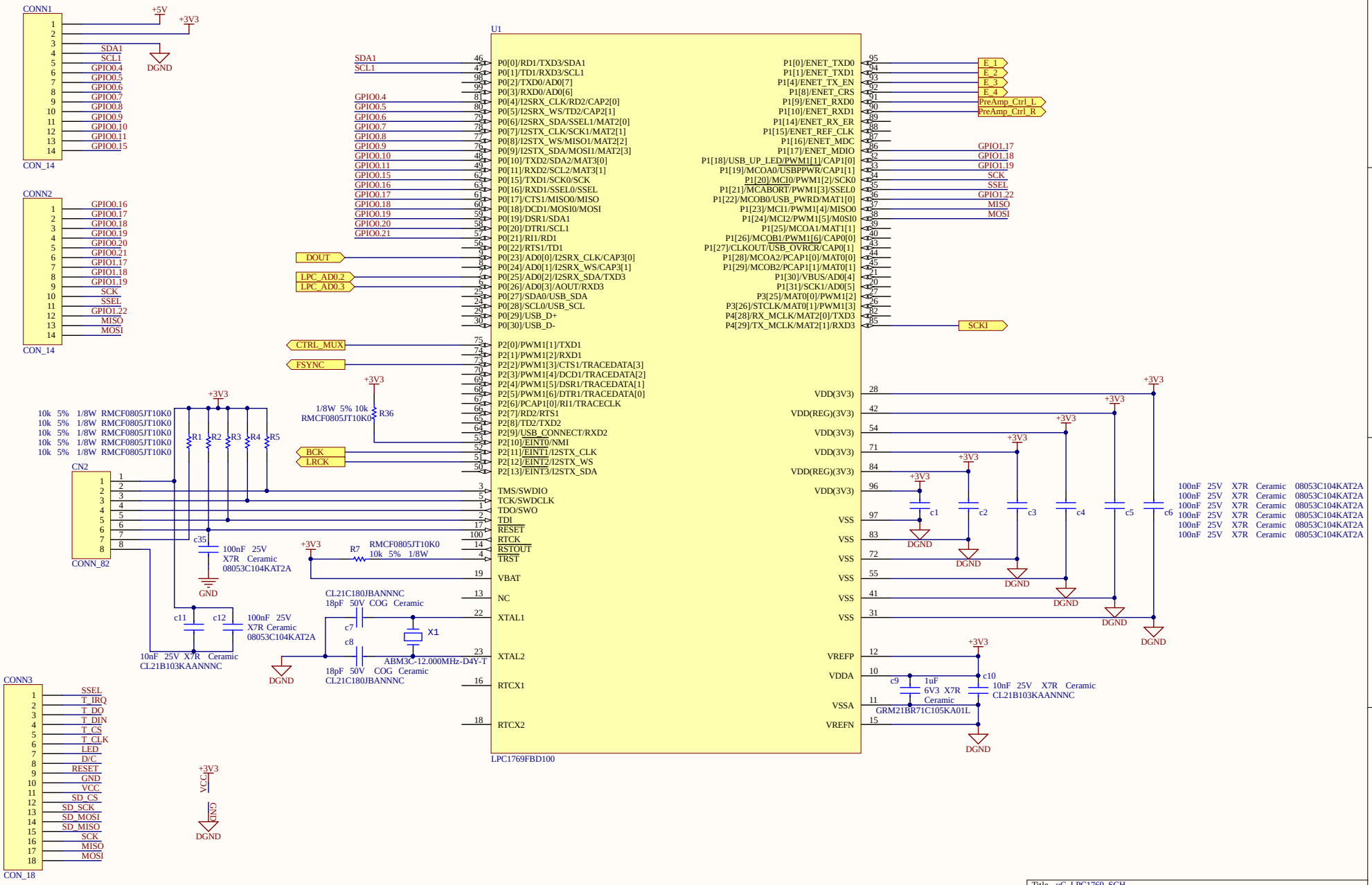


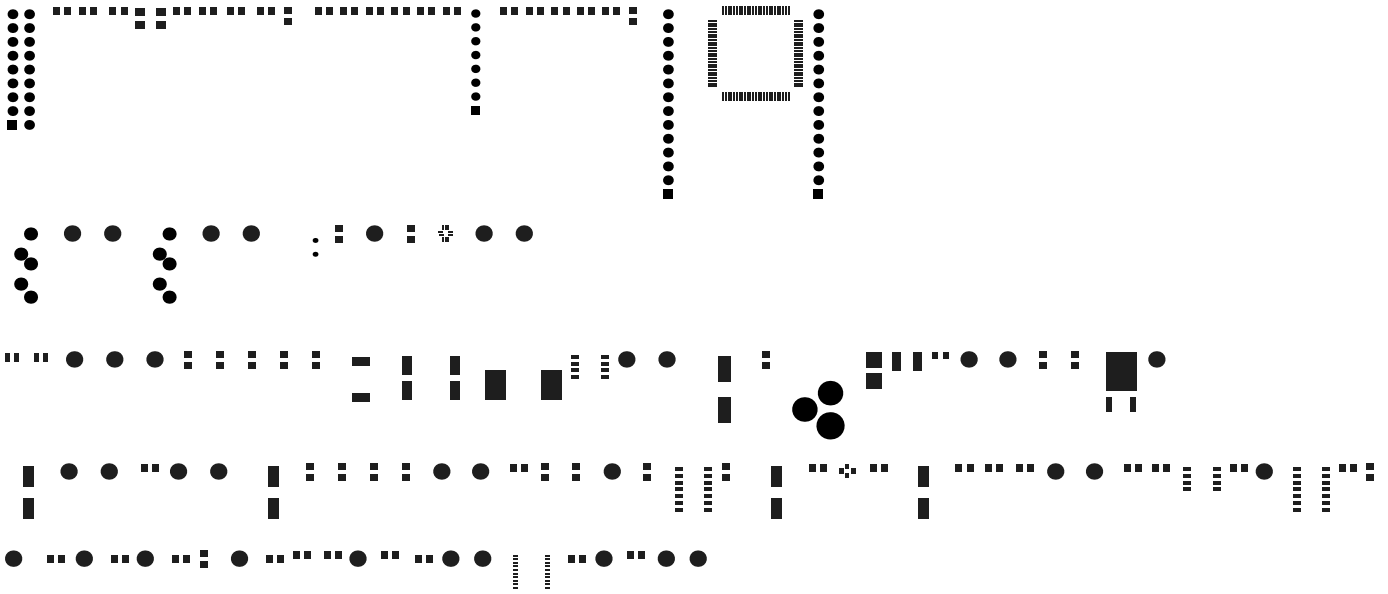
Title Signal_Imputs_SCH		
Size A4	Number	Revision Rev1.0
Date: 28/09/2017		Sheet 3of 6
File: E:\Nueva carpeta\..\Signal_imputs.SchDoc		Drawn By: Hojnadel - Salazar, Ato


Left Channel
VinL_PreAmp
LPC_AD0.3
TP11
RC0805FR-07100KL
100k 1/8 W
R10
1/8 W 33k R12
RMCF0805JT33K0
AGND
+5V
C23
1uF 16V Ceramic
GRM21BR71C105KA01L
1/8 W 100k
RC0805FR-07100KL
R8
R9
1/8 W 100k
RC0805FR-07100KL
IC2A
TP13
TP12
VinL
MCP6022
C24
100uF 16V
Elect. Alum.
UWX1C101MCL1GB
+3V3
IC5A
TS3A5223
VDD
DGND
NC1
NO1
COM1
SEL1
PreAmp_Ctrl_L
R_Sw_A_1
R_Sw_B_1
IC3
1/8 W 1k R19
RC0805JR-071KL
C25
100uF 16V
Elect. Alum.
UWX1C101MCL1GB
IC3
74LVC4066
VCC
GND
E1
E2
E3
E4
590 1/8 W
RC0805FR-07590RL
2k2 1/8 W
RMCF0805JT2K20
3k9 1/8 W
RMCF0805JT3K90
100k 1/8 W
RC0805FR-07100KL
E1
E2
E3
E4
E_1
E_2
E_3
E_4
Right Channel
VinR_PreAmp
LPC_AD0.2
TP14
RC0805FR-07100KL
100k 1/8 W
R23
1/8 W 33k R26
RMCF0805JT33K0
C26
1uF 16V Ceramic
GRM21BR71C105KA01L
1/8 W 100k
RC0805FR-07100KL
R20
1/8 W 100k
RC0805FR-07100KL
R21
TP16
IC2B
TP15
VinR
MCP6022
C36
100uF 16V
Elect. Alum.
UWX1C101MCL1GB
IC5B
TS3A5223
NC2
NO2
COM2
SEL2
PreAmp_Ctrl_R
R_Sw_A_2
R_Sw_B_2
IC7
R31
1k 1/8 W
RC0805JR-071KL
C37
100uF 16V
Elect. Alum.
UWX1C101MCL1GB
IC7
74LVC4066
TP32
TP33
TP34
TP35
Title AGC_SCH
Size A4
Number
Revision Rev1.0
Date: 28/09/2017
Sheet 4of 6
File: E:\Nueva carpeta\..\AGC.SchDoc
Drawn By: Hojnadel - Salazar Ato



Title	ADC_PCM1802_SCH		
Size	Number		Revision
A3			Rev1.0
Date:	28/09/2017	Sheet 5of 6	
File:	E:\Nueva carpeta\..\adc.SchDoc	Drawn By:	Hojnadel - Salazar Ato


CONN1
CON_14
CONN2
CON_14
U1
LPC1769FBD100
CN2
CONN_82
CONN3
CON_18
+5V
+3V3
DGND
SDA1
SCL1
GPIO0.4
GPIO0.5
GPIO0.6
GPIO0.7
GPIO0.8
GPIO0.9
GPIO0.10
GPIO0.11
GPIO0.15
GPIO0.16
GPIO0.17
GPIO0.18
GPIO0.19
GPIO0.20
GPIO0.21
GPIO1.17
GPIO1.18
GPIO1.19
GPIO1.22
SCK
SSEL
MISO
MOSI
DOUT
LPC_AD0.2
LPC_AD0.3
CTRL_MUX
FSYNC
BCK
LRCK
SCKI
E_1
E_2
E_3
E_4
PreAmp_Ctrl_L
PreAmp_Ctrl_R
10k 5% 1/8W RMCF0805JT10K0
R1
R2
R3
R4
R5
1/8W 5% 10k RMCF0805JT10K0
R36
R7
RMCF0805JT10K0 10k 5% 1/8W
c35
100nF 25V X7R Ceramic 08053C104KAT2A
c11
c12
10nF 25V X7R Ceramic CL21B103KAANNNC
CL21C180JBANNNC 18pF 50V COG Ceramic
c7
c8
X1
ABM3C-12.000MHz-D4Y-T
c1
c2
c3
c4
c5
c6
c9
1uF 6V3 X7R Ceramic GRM21BR71C105KA01L
c10
10nF 25V X7R Ceramic CL21B103KAANNNC
VCC
GND
SSEL
T_IRQ
T_DO
T_DIN
T_CS
T_CLK
LED
D/C
RESET
GND
VCC
SD_CS
SD_SCK
SD_MOSI
SD_MISO
SCK
MISO
MOSI
P0[0]/RD1/TXD3/SDA1
P0[1]/TD1/RXD3/SCL1
P0[2]/TXD0/AD0[7]
P0[3]/RXD0/AD0[6]
P0[4]/I2SRX_CLK/RD2/CAP2[0]
P0[5]/I2SRX_WS/TD2/CAP2[1]
P0[6]/I2SRX_SDA/SSEL1/MAT2[0]
P0[7]/I2STX_CLK/SCK1/MAT2[1]
P0[8]/I2STX_WS/MISO1/MAT2[2]
P0[9]/I2STX_SDA/MOSI1/MAT2[3]
P0[10]/TXD2/SDA2/MAT3[0]
P0[11]/RXD2/SCL2/MAT3[1]
P0[15]/TXD1/SCK0/SCK
P0[16]/RXD1/SSEL0/SSEL
P0[17]/CTS1/MISO0/MISO
P0[18]/DCD1/MOSI0/MOSI
P0[19]/DSR1/SDA1
P0[20]/DTR1/SCL1
P0[21]/RI1/RD1
P0[22]/RTS1/TD1
P0[23]/AD0[0]/I2SRX_CLK/CAP3[0]
P0[24]/AD0[1]/I2SRX_WS/CAP3[1]
P0[25]/AD0[2]/I2SRX_SDA/TXD3
P0[26]/AD0[3]/AOUT/RXD3
P0[27]/SDA0/USB_SDA
P0[28]/SCL0/USB_SCL
P0[29]/USB_D+
P0[30]/USB_D-
P2[0]/PWM1[1]/TXD1
P2[1]/PWM1[2]/RXD1
P2[2]/PWM1[3]/CTS1/TRACEDATA[3]
P2[3]/PWM1[4]/DCD1/TRACEDATA[2]
P2[4]/PWM1[5]/DSR1/TRACEDATA[1]
P2[5]/PWM1[6]/DTR1/TRACEDATA[0]
P2[6]/PCAP1[0]/RI1/TRACECLK
P2[7]/RD2/RTS1
P2[8]/TD2/TXD2
P2[9]/USB_CONNECT/RXD2
P2[10]/EINT0/NMI
P2[11]/EINT1/I2STX_CLK
P2[12]/EINT2/I2STX_WS
P2[13]/EINT3/I2STX_SDA
TMS/SWDIO
TCK/SWDCLK
TDO/SWO
TDI
RESET
RTCK
RSTOUT
TRST
VBAT
NC
XTAL1
XTAL2
RTCX1
RTCX2
P1[0]/ENET_TXD0
P1[1]/ENET_TXD1
P1[4]/ENET_TX_EN
P1[8]/ENET_CRS
P1[9]/ENET_RXD0
P1[10]/ENET_RXD1
P1[14]/ENET_RX_ER
P1[15]/ENET_REF_CLK
P1[16]/ENET_MDC
P1[17]/ENET_MDIO
P1[18]/USB_UP_LED/PWM1[1]/CAP1[0]
P1[19]/MCOA0/USBPPWR/CAP1[1]
P1[20]/MCI0/PWM1[2]/SCK0
P1[21]/MCABORT/PWM1[3]/SSEL0
P1[22]/MCOB0/USB_PWRD/MAT1[0]
P1[23]/MCI1/PWM1[4]/MISO0
P1[24]/MCI2/PWM1[5]/MOSI0
P1[25]/MCOA1/MAT1[1]
P1[26]/MCOB1/PWM1[6]/CAP0[0]
P1[27]/CLKOUT/USB_OVRCR/CAP0[1]
P1[28]/MCOA2/PCAP1[0]/MAT0[0]
P1[29]/MCOB2/PCAP1[1]/MAT0[1]
P1[30]/VBUS/AD0[4]
P1[31]/SCK1/AD0[5]
P3[25]/MAT0[0]/PWM1[2]
P3[26]/STCLK/MAT0[1]/PWM1[3]
P4[28]/RX_MCLK/MAT2[0]/TXD3
P4[29]/TX_MCLK/MAT2[1]/RXD3
VDD(3V3)
VDD(REG)(3V3)
VSS
VREFP
VDDA
VSSA
VREFN
Title uC_LPC1769_SCH
Size A3
Number
Revision Rev1.0
Date: 28/09/2017
Sheet 6of 6
File: E:\Nueva carpeta\..\uc.SchDoc
Drawn By: Hojnadel - Salazar Ato



Board S