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December 2013

FQU20N06L

N-Channel QFET® MOSFET

60 V, 17.2 A, 42 mΩ

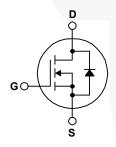
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 17.2 A, 60 V, $R_{DS(on)}$ = 42 m Ω (Max.) @ V_{GS} = 10 V, I_D = 8.6 A
- Low Gate Charge (Typ. 9.5 nC)
- · Low Crss (Typ. 35 pF)
- 100% Avalanche Tested
- Low Level Gate Drive Requirements Allowing Direct Operation Form Logic Drivers





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQU20N06LTU	Unit
V_{DSS}	Drain-Source Voltage		60	V
I _D	Drain Current - Continuous (T _C = 25°C)		17.2	Α
	- Continuous (T _C = 100°C)		10.9	Α
I _{DM}	Drain Current - Pulsed (Note 1)	68.8	Α
V _{GSS}	Gate-Source Voltage		± 20	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		170	mJ
I _{AR}	Avalanche Current (Note 1)		17.2	Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		3.8	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		7.0	V/ns
P _D	Power Dissipation (T _A = 25°C) *		2.5	W
	Power Dissipation (T _C = 25°C)		38	W
	- Derate above 25°C		0.30	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T _L	Maximum lead temperature for soldering, 1/8" from case for 5 seconds.		300	°C

Thermal Characteristics

Symbol	Parameter	FQU20N06LTU	Unit
R_{\thetaJC}	Thermal Resistance, Junction to Case, Max.	3.28	
В	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	110	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (*1 in ² Pad of 2-oz Copper), Max.	50	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQU20N06LTU	FQU20N06L	IPAK	Tube	N/A	N/A	70 units

Flectrical Characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Cha	aracteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C		0.06		V/°C
I _{DSS}	7 0 1 1/1 5 : 0 1	V _{DS} = 60 V, V _{GS} = 0 V	-		1	μΑ
	Zero Gate Voltage Drain Current	V _{DS} = 48 V, T _C = 125°C	-		10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
On Cha	aracteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	1.0		2.5	V
R _{DS(on)}	Static Drain-Source	V _{GS} = 10 V, I _D = 8.6 A		0.046	0.06	
20(0)	On-Resistance	$V_{GS} = 5 V, I_D = 8.6 A$		0.057	0.075	Ω
g _{FS}	Forward Transconductance	V _{DS} = 25 V, I _D = 8.6 A	1	11		S
Dynam	ic Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz		480	630	pF
C _{oss}	Output Capacitance			175	230	pF
C _{rss}	Reverse Transfer Capacitance			35	45	pF
Switchi	ing Characteristics					
t _{d(on)}	Turn-On Delay Time			10	30	ns
t _r	Turn-On Rise Time	$V_{DD} = 30 \text{ V, } I_{D} = 10.5 \text{ A,}$ $R_{G} = 25 \Omega$		165	340	ns
t _{d(off)}	Turn-Off Delay Time			35	80	ns
t _f	Turn-Off Fall Time	(Note 4)		70	150	ns
Qg	Total Gate Charge	V _{DS} = 48 V, I _D = 21 A,	-	9.5	13	nC
Q _{gs}	Gate-Source Charge	$V_{GS} = 40 \text{ V}, \text{ ip} = 21 \text{ A},$ $V_{GS} = 5 \text{ V}$ (Note 4)		2.5		nC
Q _{gd}	Gate-Drain Charge			5.5		nC
	Source Diede Cheresteriation of	nd Maximum Batinga				
I _S	Source Diode Characteristics and Maximum Continuous Drain-Source Dic				17.2	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				68.8	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 17.2 A			1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _F = 21 A,		54		ns
11		$dI_{\rm F}$ / dt = 100 A/ μ s				

Notes: 1. Repetitive rating : pulse-width limited by maximum junction temperature. 2. L = 670 μ H, I_{AS} = 17.2 A, V_{DD} = 25 V, R_G = 25 Ω , starting T_J = 25°C. 3. I_{SD} \leq 21 A, di/dt \leq 300 A/ μ s, V_{DD} \leq BV_{DSS}, starting T_J = 25°C. 4. Essentially independent of operating tmperature.

Typical Characteristics

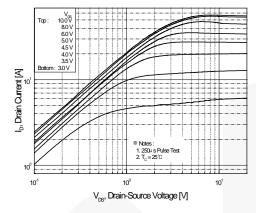


Figure 1. On-Region Characteristics

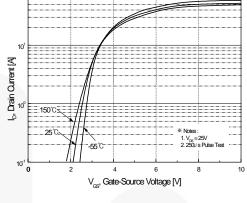


Figure 2. Transfer Characteristics

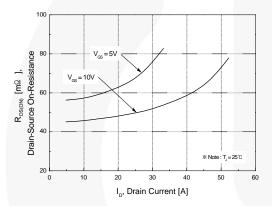


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

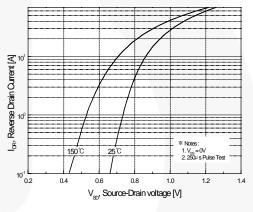


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

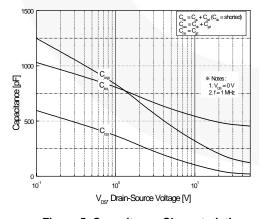


Figure 5. Capacitance Characteristics

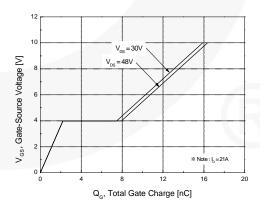


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

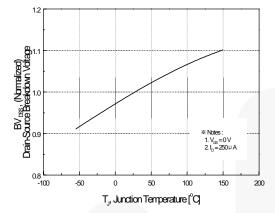


Figure 7. Breakdown Voltage Variation vs. Temperature

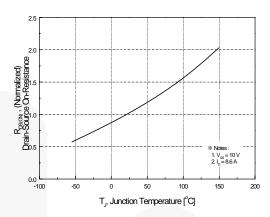


Figure 8. On-Resistance Variation vs. Temperature

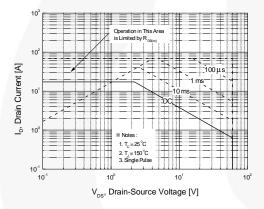


Figure 9. Maximum Safe Operating Area

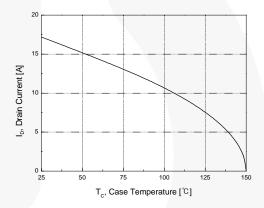


Figure 10. Maximum Drain Current vs. Case Temperature

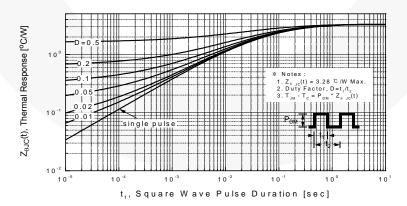


Figure 11. Transient Thermal Response Curve

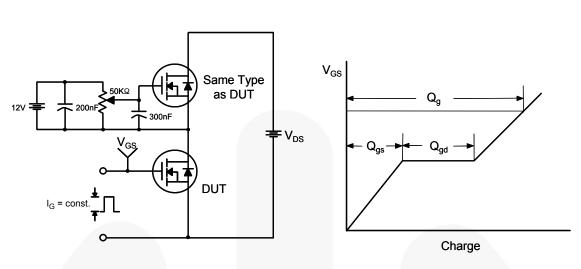


Figure 12. Gate Charge Test Circuit & Waveform

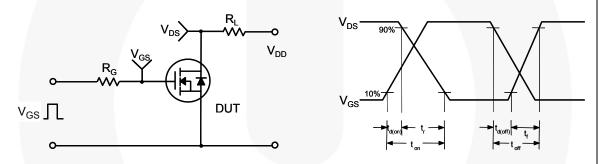


Figure 13. Resistive Switching Test Circuit & Waveforms

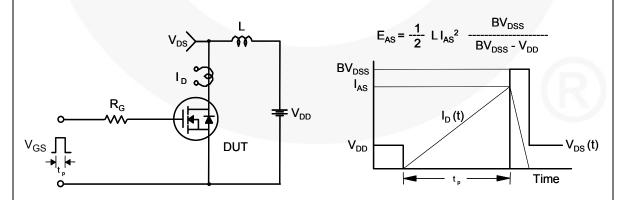
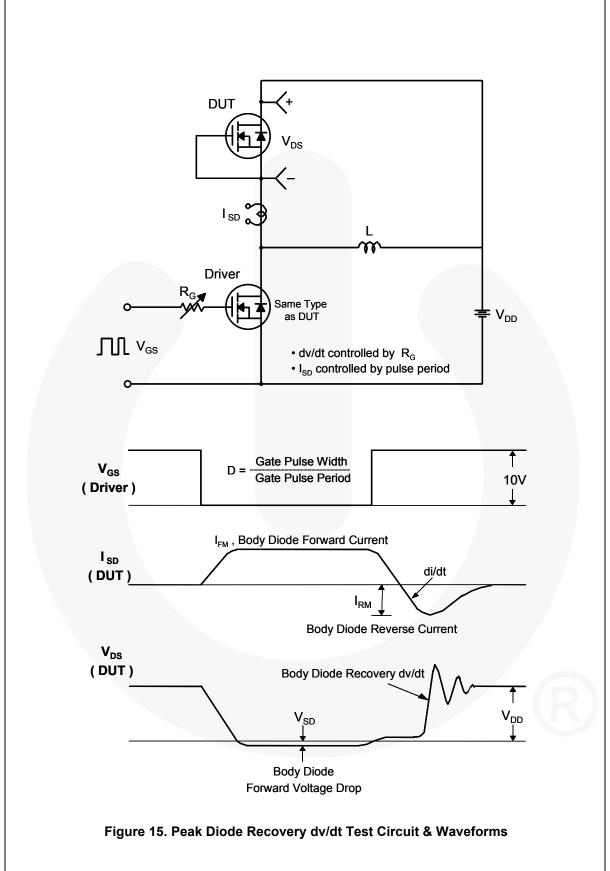


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

FQU13N06LTU

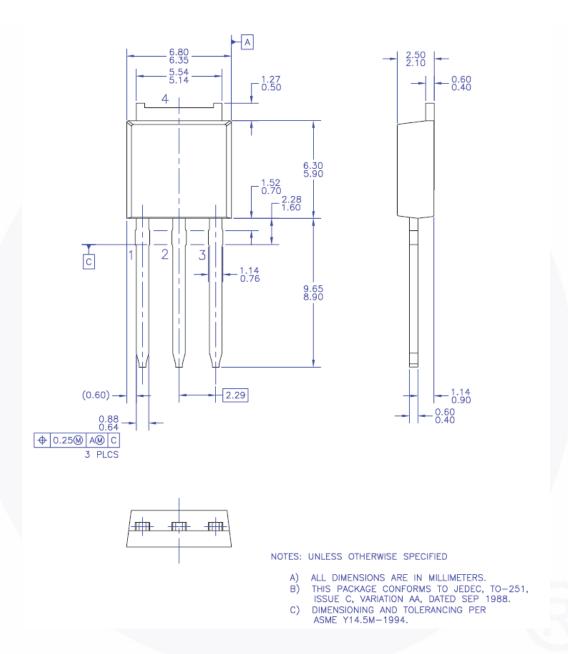


Figure 16. TO251 (I-PAK), Molded, 3-Lead

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