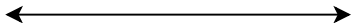
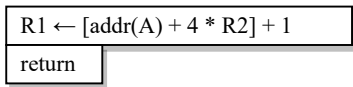
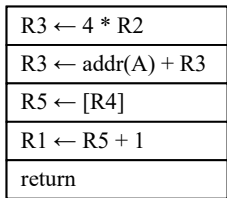


CISC



variable length

RISC



fixed length

Number of  
instructions

