



```
{
  "peripherals": [
    {
      "name" : "Logic analyzer",
      "connection" : [ 14, 15, 22, 27, 10 ],
      "lib_dir" : "peripherals",
      "lib_name" : "logic_analyzer"
    },
    {
      "name" : "Serial terminal",
      "connection" : [ 14, 15 ],
      "comment" : "pins: [TX, RX]",
      "lib_dir" : "peripherals",
      "lib_name" : "serial_terminal"
    }
  ]
}
```