ZeroMate - Rpi Zero emulator					×
▼ File Control	▼ Source Code Disassembly	Disassembly		▼ RAM GPIO IC ARM timer Monitor	ner Monitor
M Step ● Run ■ Stop	0×00000CFF	0x0000CFFC 0xEAFFFFF4	b #0xcfd4	Debug monitor: 80x25 8-bit characters	t characters
	0×00000000	0x0000D000 0xE3A02001	mov r2, #1	1: finding child = segd	
State: stopped	0000000×0	0x000000004 0xE59F104C	ldr r1, [pc, #0x4c]		
	0×0000D008	3 0xE51B0008	ldr r0, [fp, #-8]	3: creating: segd 4: Finished FS initialization	ation
▼ CPU Registers CPIS	0×00000000	0x0000D00C 0xEB000145	bl #0xd528		
CPU Mode: System	0x0000D010	0×E3A03000	mov r3, #0	6: Created process with pid 1	
USR/SYS FIQ SVC ABT IRQ UDN	0×0000D01	0x0000D014 0xE50B300C	str r3, [fp, #-0xc]	7: Created process with pid 2	pid 2 (SP = 0x28000) pid 3 (SP = 0x2C000)
HEX U32 S32	0x0000D018	3 0xE51B300C	ldr r3, [fp, #-0xc]	Created	= ds)
Register Value	0×0000D01C	0xE3530B01	cmp r3, #0x400	Created	pid 5 (SP = 0x34000)
	0×0000D020	0x0000D020 0xB3A03001	movlt r3, #1	opening	d
R2 00000001	0×0000D024	1 0xA3A03000	movge r3, #0	<pre>12: process > file descriptor = 0 13: opening file: DEV:doio/20</pre>	ptor = 0 0/20
R3 0000028F	0×0000D028	3 0xE6EF3073	uxtb r3, r3	process	ptor = 0
	0×0000D020	0x0000D02C 0xE3530000	cmp r3, #0	opening	0/19
R6 0000CE50	0×00000D03	0x000000030 0x0AFFFE1	beq #0xcfbc	<pre>10: process 3 file descriptor = 0 17: opening file: DEV: monitor/0</pre>	ptor = 0
	0×00000D03	0x0000D034 0xE51B300C	ldr r3, [fp, #-0xc]	opening	0.70
R8 0000CE24	0x0000D038	0×E2833001	add r3, r3, #1		ptor (f) = 0
0	0×00000D03(0x0000D03C 0xE50B300C	str r3, [fp, #-0xc]		ptor (rndf)= 1
R11 00004F40	0×00000D040	0x000000040 0xEAFFFF4	b #0xd018	21: 41829430990pHIRIG LITE: DEV:gpto/is 22: process 1 file descriptor = 0	e: VEV:gpio/i8 pior = 0
R12 200000DF					plocess I life descriptor = 0 2562694096194938438940474621931554527732535076234846925
R13 (LR) 000000010	0x0000D044	4 0×00000E11C	andeq Ir, r0, 1p, Isl r1		2302034030134830430340414021331334321132333010234040323. 486661328345716831868185416110666016447857208116939188
R14 (SP) 00004F34	0x0000D048	3 0x0000E128	andeq 1r, r0, r8, 1sr #2		40000132034371003100010341011390001044703722301103333100 01381446153236272884524632757032711601301463
(PC)	<u></u> Logs				Z+0.52.7.1.50.2.7.1.00.1.4.0.5
CPSR 2000005F	16.1				
Flags in CSPR	Options Clear	Copy			Filter
ZCVAI	[warning][core.cpp:367] I		tel·cpp.to+j bαsic iny mnr_limel has been signaiited RQ exception	ומט הפכוו טדאוומדדדפת	
0 0 1 0 0 0 1	[debug][interrupt_control	0]	ler.cpp:264] Basic IRQ ARM_Timer has been signalized	has been signalized	
	[warnıng][core.cpp:367] [debuq][interrupt contr	0	RQ exception ler.cpp:264l Basic IRO ARM Timer has been signalized	has been signalized	

[warning][core.cpp:367] IRQ exception [debug][interrupt_controller.cpp:264] Basic IRQ ARM_Timer has been signalized

warning][core.cpp:367] IRQ exception