

FileControl

StepRunStop

Reset

State: stopped

CPU Registers

CPU Mode: System

USR/SYSFIQ SVC

HEX U32 S32

Register	Value
R0	00000001
R1	00000000
R2	00000001
R3	0000028F
R4	00000004
R5	0000CE40
R6	0000CE50
R7	00008040
R8	0000CE24
R9	0000B854
R10	00000000
R11	00004F40
R12	200000DF
R13 (LR)	0000D010
R14 (SP)	00004F34
R15 (PC)	0000D010
CPSR	2000005F

Flags in CPSR

N	Z	C	V	A	I	F
0	0	1	0	0	0	1

Source Code Disassembly

0x0000CFFC	0xEAFFFFF4	b #0xcfd4
0x0000D000	0xF3A02001	mov r2, #1
0x0000D040		ldr r1, [pc, #0x4c]

RAMGPIOICARM timerMonitor

Debug monitor: 80x25 8-bit characters

1: finding child = segd

2: child was not found

Serial terminal

115200

8

Reset settings

270

StartStopReset

Number of collected samples: 1024

Serial terminal

1: ?elcome

2: Welcome

3: Welcome

4: Welcome

5: Welcome

6: Welcome

7: Welcome

8: Welcome

9: Welcome

10: Welcome

11: Welcome

12: Welcome

13: Welcome

14: Welcome

15:

16:

17:

18:

19:

20:

21:

22:

23:

24:

25:

Howdy

Logic analyzer

1024

Reset settings

270

StartStopReset

Number of collected samples: 1024

GPIO pins

Voltage (1 = 5V, 0 = 0V)

GPIO pin 14

GPIO pin 15

GPIO pin 27

GPIO pin 22

GPIO pin 4

GPIO pin 17

GPIO pin 16

GPIO pin 18

GPIO pin 23

Time [x * 270]

SOS btn

GPIO pin: 16

Press

(128x32 pxls)

ec)

port is

ndr)= 1

gpio/18

gment display

register: 00000000

pin: 22

in: 27

pin: 10

25:

88:

debug][interrupt_controller.cpp:264] Basic IRQ ARM_Timer has been signaled

[warning][core.cpp:367] IRQ exception

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[debug][interrupt_controller.cpp:264] Basic IRQ ARM_Timer has been signaled

[warning][core.cpp:367] IRQ exception

[info][control_window.cpp:200] CPU execution has stopped