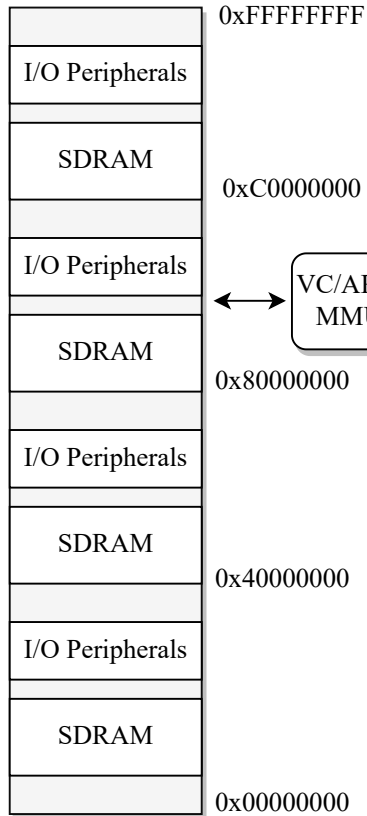
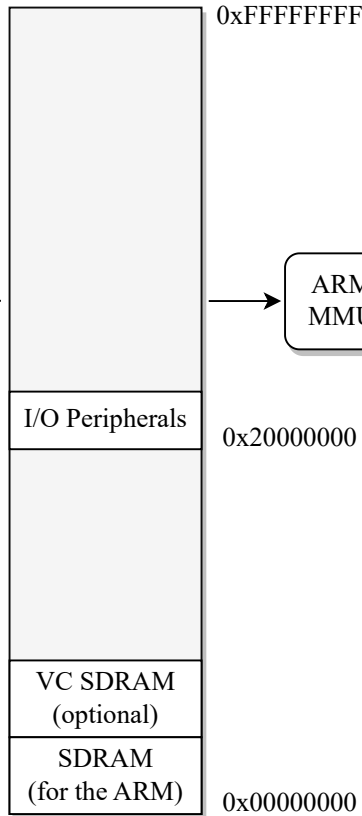


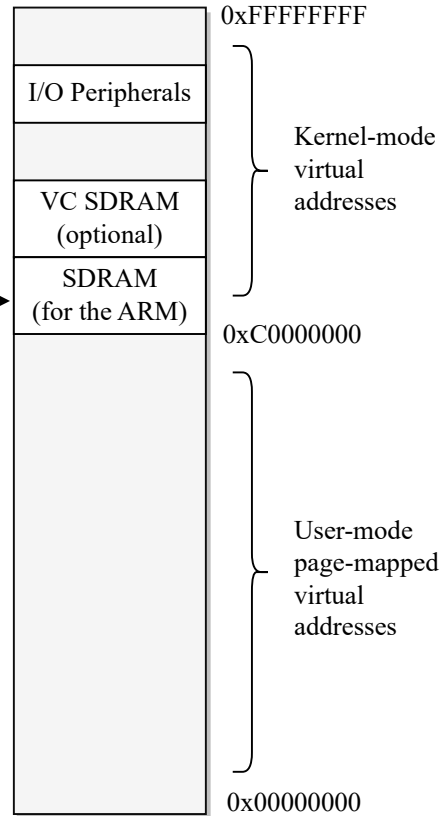
## System bus addresses



## Physical addresses



## Virtual addresses (OS-specific)



← HW implementation SW abstraction →