

6

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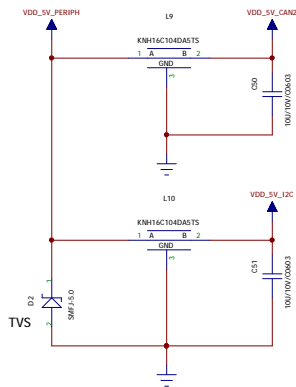
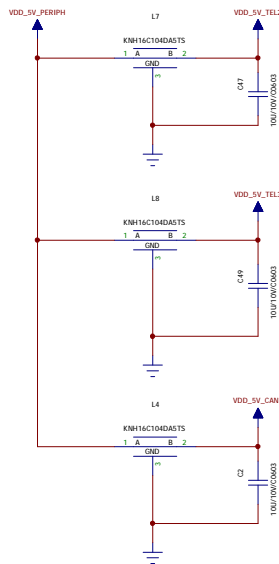
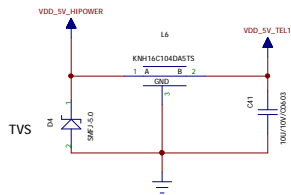
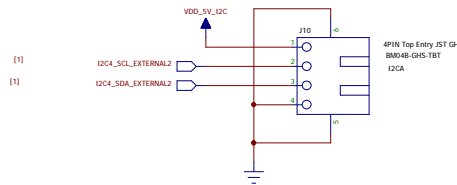
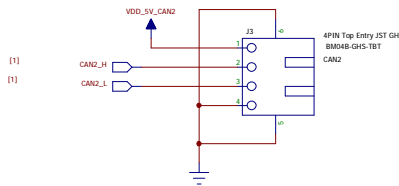
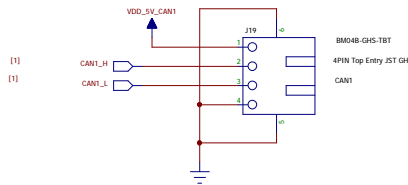
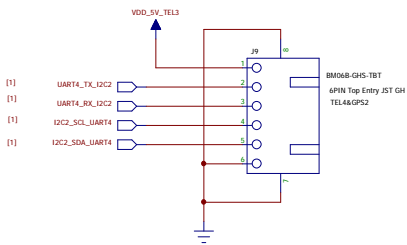
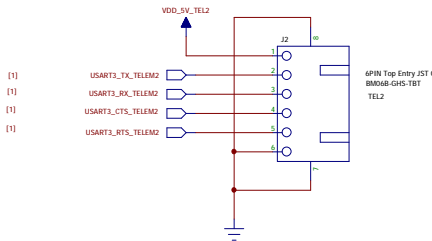
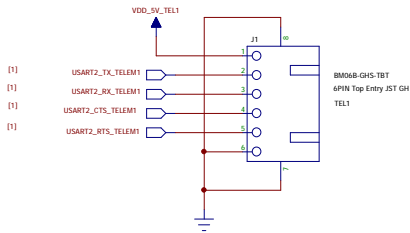
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1

REVISION RECORD			



COMPANY: Holybro			
TITLE: PIX32-V5-BASE			
CODE: 028	SIZE: A2	Sheet: UART-I2C-CAN	REV: RC02
SCALE: 1:1		SHEET: 2 of 5	

DRAWN: Jamming	DATED: 2020/02/25
CHECKED: Jamming	DATED: 2020/02/28
QUALITY CONTROL:	DATED:
RELEASED: Jamming	DATED: 2020/03/31

6

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4

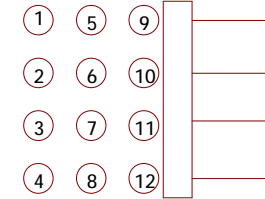
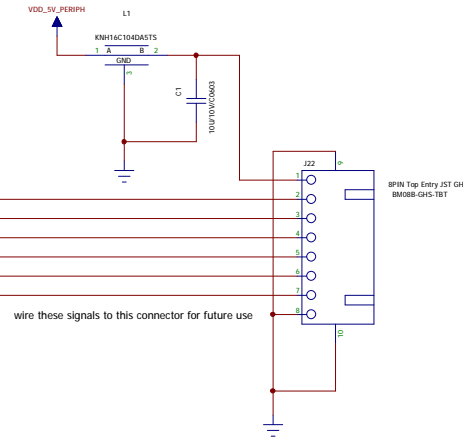
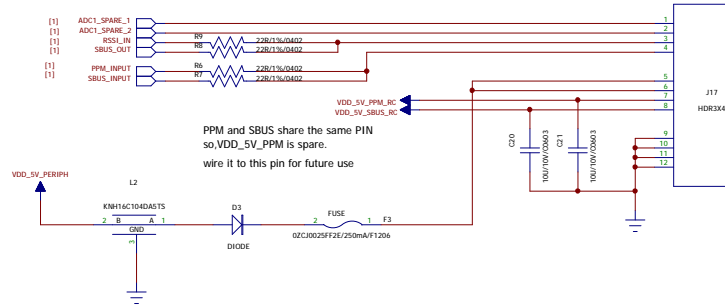
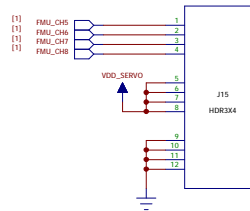
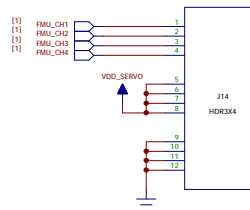
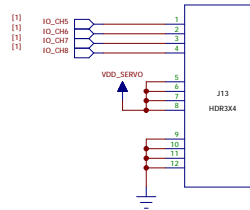
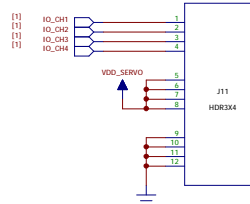
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2

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REVISION RECORD			
LTR	ECD NO.	APPROVED:	DATE:

ESD Componets are placed on FMU Board
No additional ESD protect is required here



COMPANY: Holybro			
TITLE: PIX32-V5-BASE			
DRAWN: Jamming	DATED: 2020/02/25	CODE: 028	SIZE: A2
CHECKED: Jamming	DATED: 2020/02/28	DRAWING NO: Servo Out	REV: RC02
QUALITY CONTROL:	DATED:	SCALE: 1:1	
RELEASED: Jamming	DATED: 2020/03/31	SHEET: 4 of 5	

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REVISION RECORD			

D

D

C

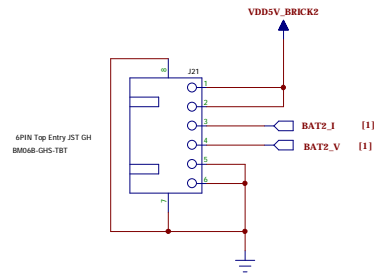
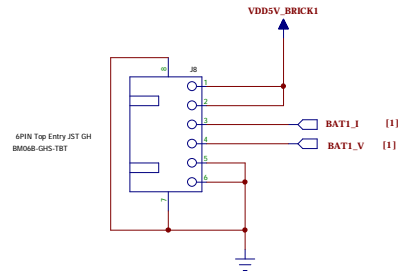
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B

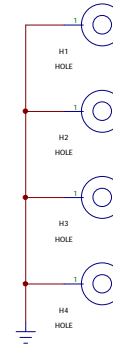
B

A

A



4 hole,M3



M1
MARK

M4
MARK

M2
MARK

M5
MARK

M3
MARK

DRAWN:	Jamming	DATED:	2020/02/25
CHECKED:	Jamming	DATED:	2020/02/28
QUALITY CONTROL:		DATED:	
RELEASED:	Jamming	DATED:	2020/03/31

COMPANY:		Holybro	
TITLE:			
PIX32-V5-BASE			
CODE:	SIZE:	SHEET:	REV:
028	A2	BAT-SENSE	RC02
SCALE: 1:1		SHEET: 5 OF 5	