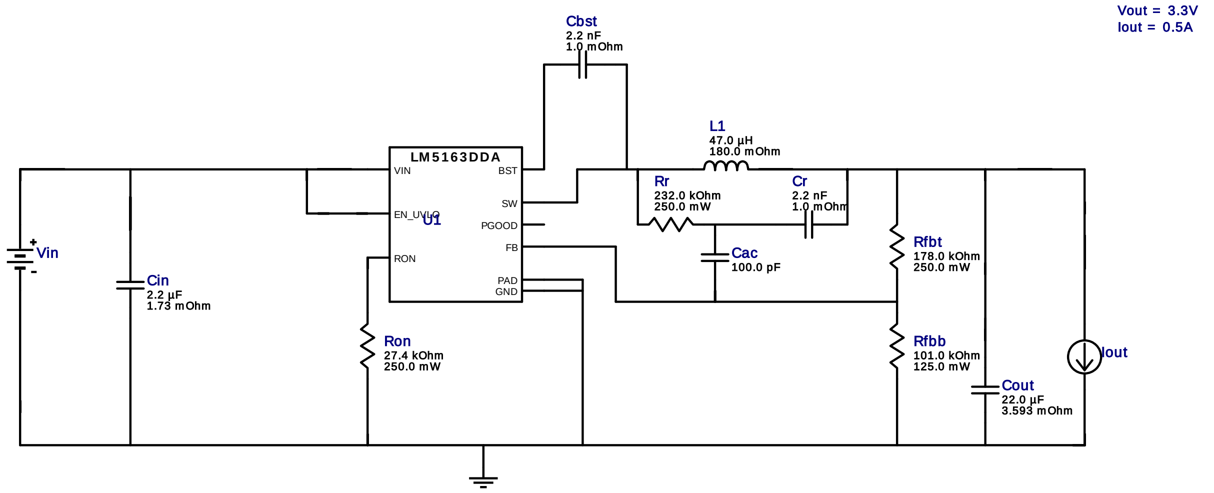
TEXAS INSTRUMENTS

WEBENCH ® Design Report

VinMin = 48.0V
VinMax = 51.6V
Vout = 3.3V
Iout = 0.5A

Device = LM5163DDAR
Topology = Buck
Created = 2024-10-16 21:56:57.561
BOM Cost = $1.58
BOM Count = 11
Total Pd = 0.54W

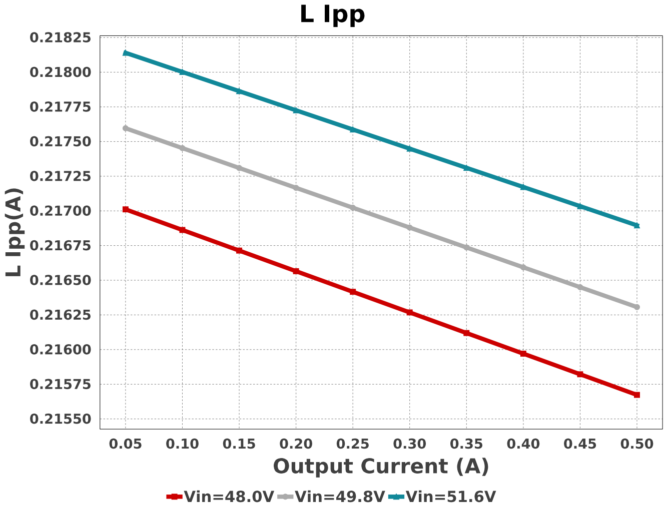
Design : 11 LM5163DDAR
LM5163DDAR **UPDATED** **FINAL**48V-51.6V to 3.30V @ 0.5A

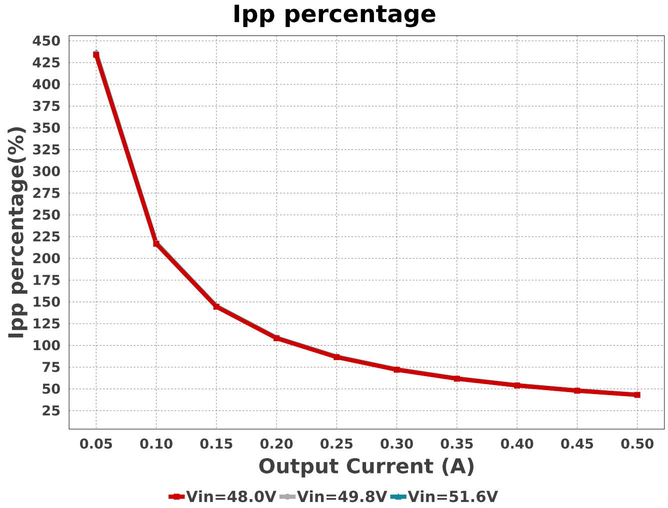


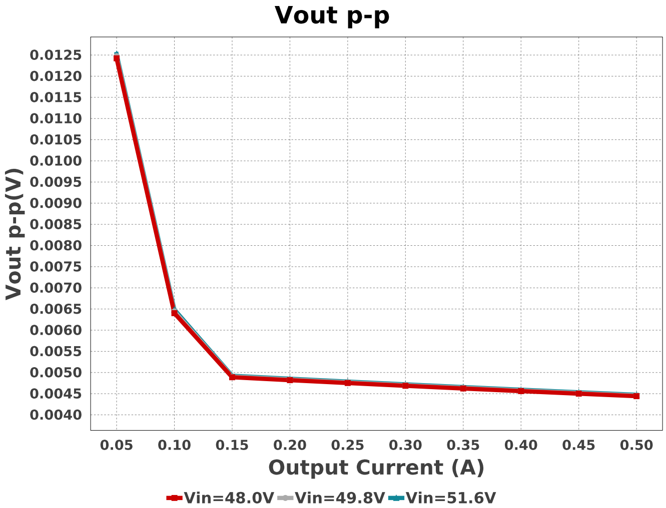
Electrical BOM

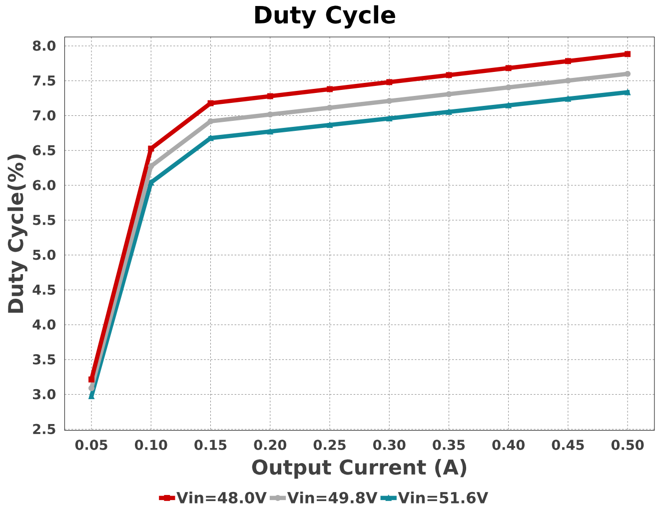
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cac	Kemet	C1206C101K5GACTU Series= C0G/NP0	Cap= 100.0 pF VDC= 50.0 V IRMS= 0.0 A	1	$0.03	1206 11 mm^2
Cbst	Yageo	CC1206KRX7R9BB222 Series= X7R	Cap= 2.2 nF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	$0.04	1206 11 mm^2
Cin	TDK	C3225X7R2A225K230AB Series= X7R	Cap= 2.2 uF ESR= 1.73 mOhm VDC= 100.0 V IRMS= 5.5932 A	1	$0.21	1210_250 15 mm^2
Cout	MuRata	GRM31CR71A226KE15L Series= X7R	Cap= 22.0 uF ESR= 3.593 mOhm VDC= 10.0 V IRMS= 3.5332 A	1	$0.12	1206_190 11 mm^2
Cr	Yageo	CC1206KRX7R9BB222 Series= X7R	Cap= 2.2 nF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	$0.04	1206 11 mm^2
L1	NIC Components	NPI75C470KTRF	L= 47.0 µH 180.0 mOhm	1	$0.11	IND_NPI75C 94 mm^2
Rfbb	Yageo	RT0805BRD07101KL Series= RT0805	Res= 101.0 kOhm Power= 125.0 mW Tolerance= 0.1%	1	$0.05	0805 7 mm^2
Rfbt	Vishay-Dale	CRCW1206178KFKEA Series= CRCW..e3	Res= 178.0 kOhm Power= 250.0 mW Tolerance= 1.0%	1	$0.01	1206 11 mm^2
Ron	Vishay-Dale	CRCW120627K4FKEA Series= CRCW..e3	Res= 27.4 kOhm Power= 250.0 mW Tolerance= 1.0%	1	$0.01	1206 11 mm^2

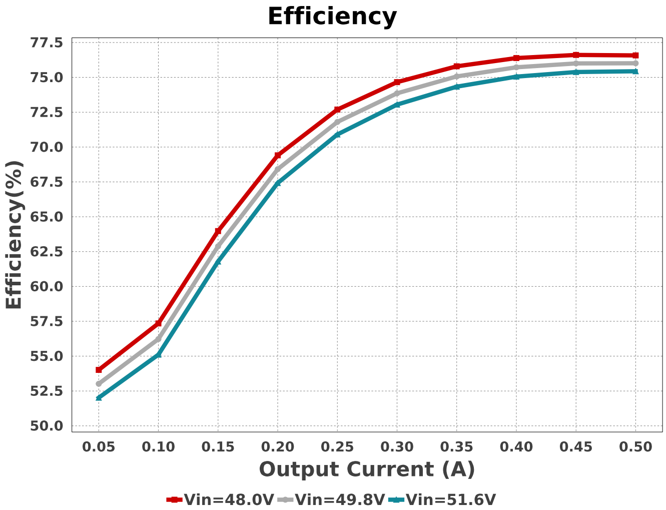
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Rr	Vishay-Dale	CRCW1206232KFKEA Series= CRCW..e3	Res= 232.0 kOhm Power= 250.0 mW Tolerance= 1.0%	1	$0.01	1206 11 mm²
U1	Texas Instruments	LM5163DDAR	Switcher	1	$0.94	DDA0008E_N 55 mm²

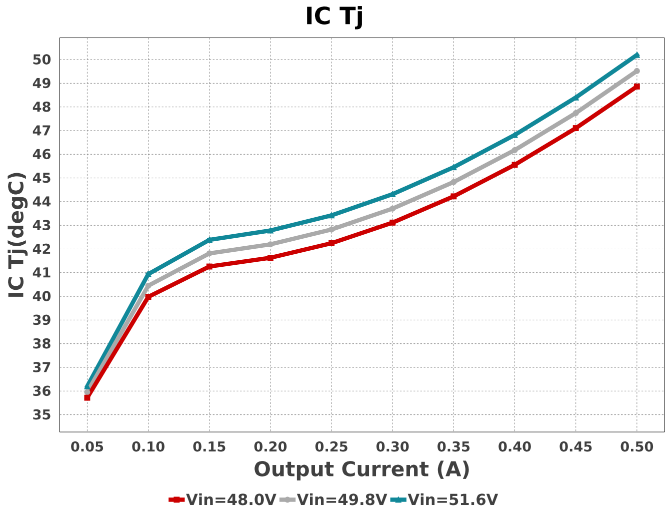


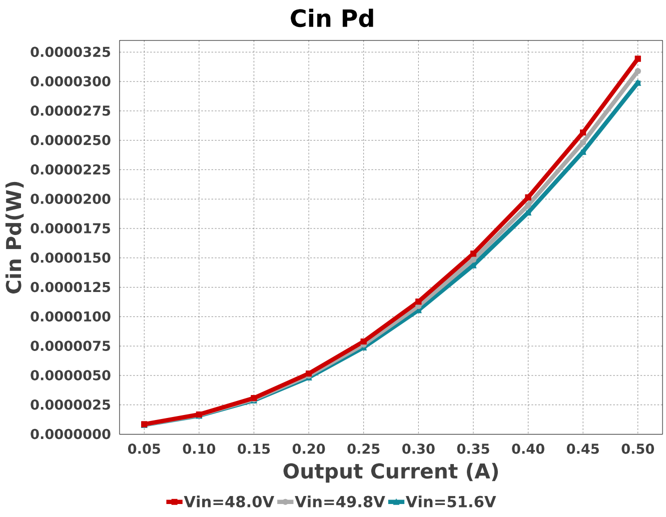


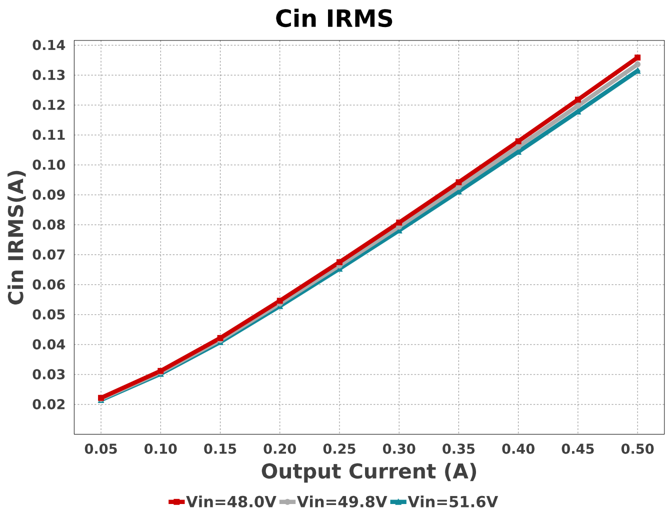


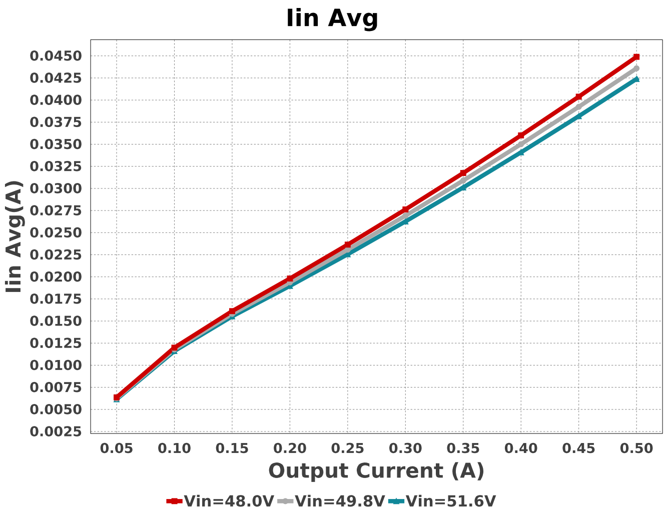


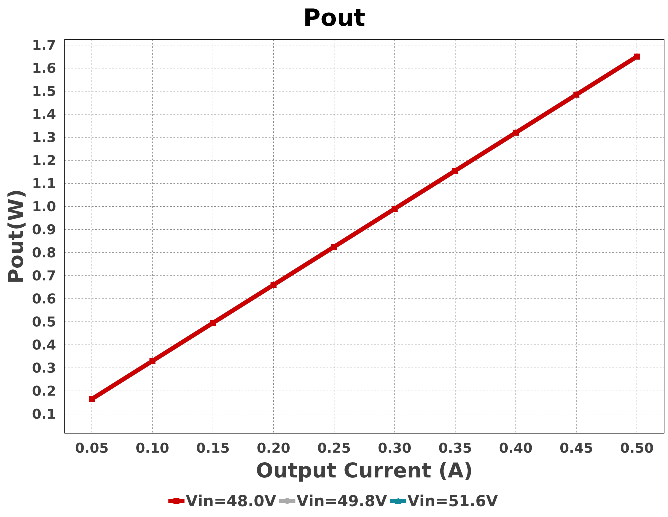


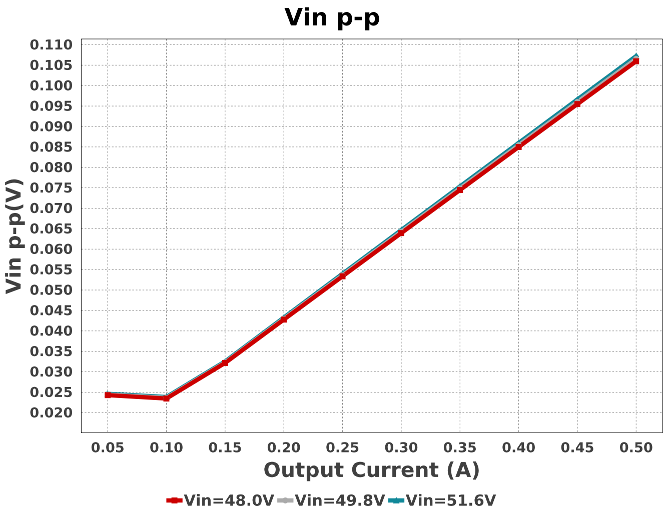


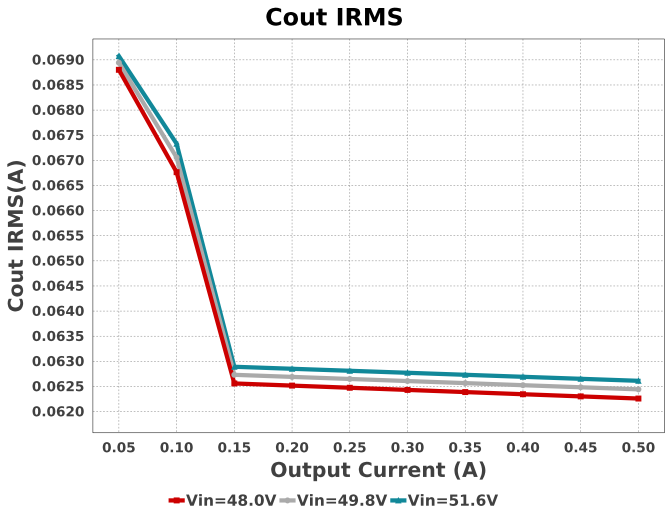

Cin Pd
Cin Pd(W)
Output Current (A)
Vin=48.0V Vin=49.8V Vin=51.6V

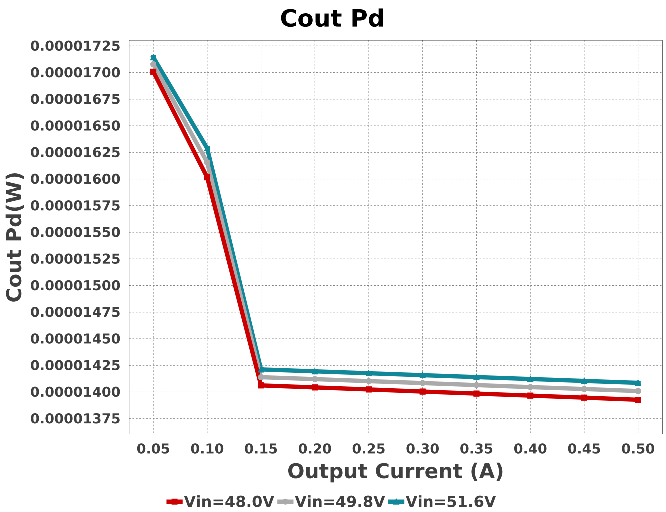

Cin IRMS
Cin IRMS(A)
Output Current (A)
Vin=48.0V Vin=49.8V Vin=51.6V

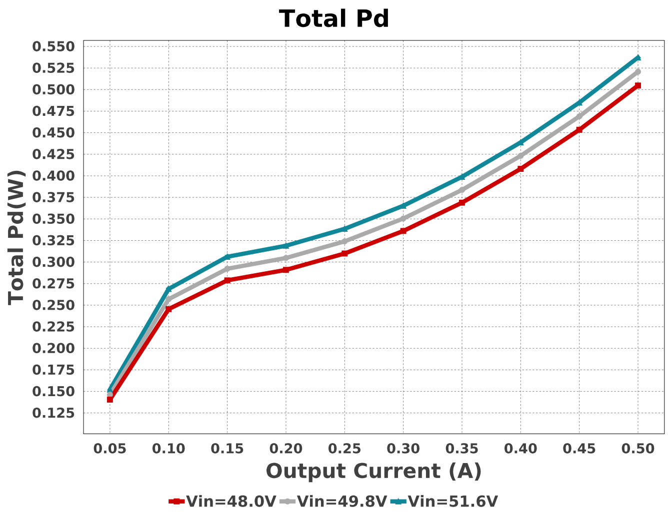

Iin Avg
Iin Avg(A)
Output Current (A)
Vin=48.0V Vin=49.8V Vin=51.6V

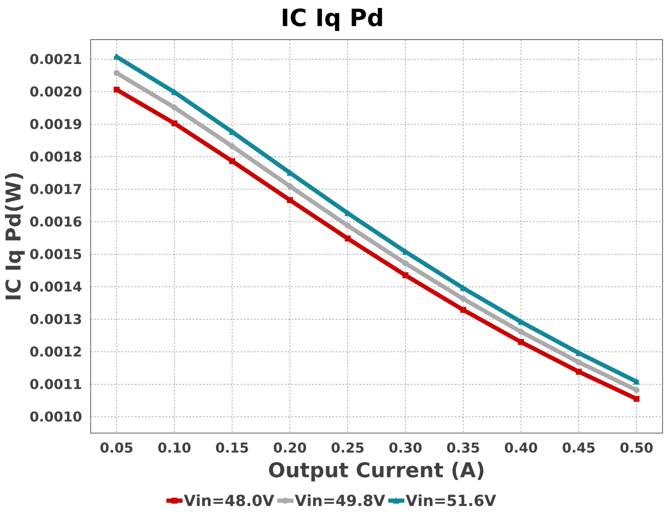

Pout
Pout(W)
Output Current (A)
Vin=48.0V Vin=49.8V Vin=51.6V

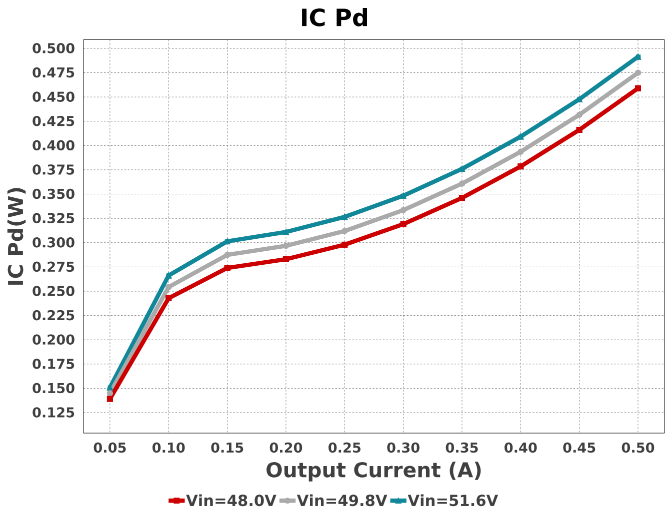

Vin p-p
Vin p-p(V)
Output Current (A)
Vin=48.0V Vin=49.8V Vin=51.6V

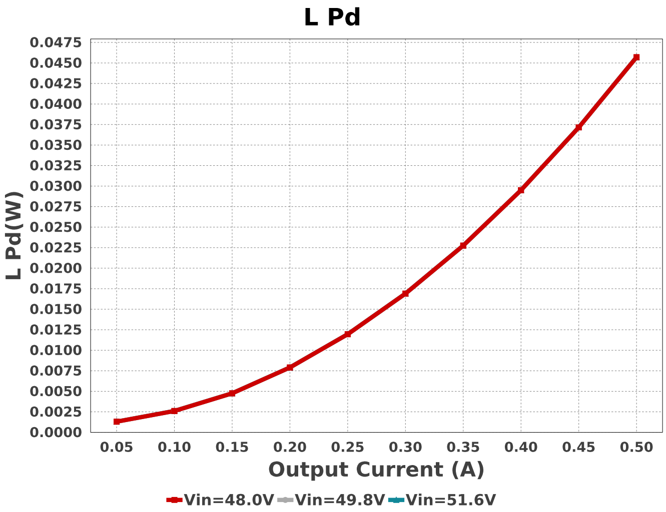

Cout IRMS
Cout IRMS(A)
Output Current (A)
Vin=48.0V Vin=49.8V Vin=51.6V

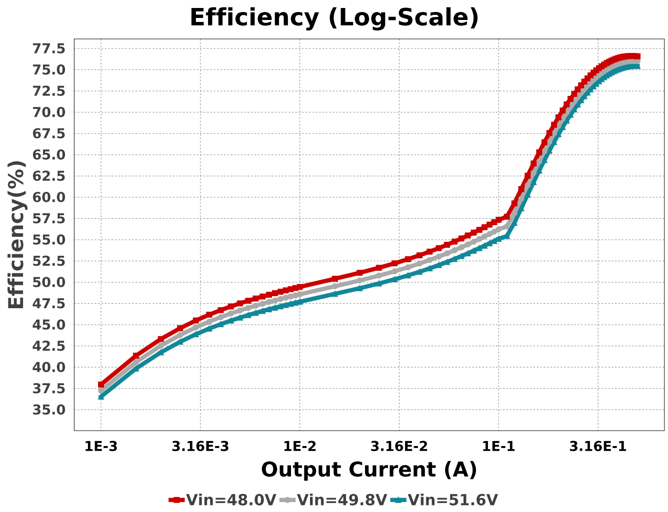












Operating Values

#	Name	Value	Category	Description
1.	BOM Count	11		Total Design BOM count
2.	Total BOM	$1.58		Total BOM Cost
3.	Cin IRMS	131.457 mA	Capacitor	Input capacitor RMS ripple current
4.	Cin Pd	29.896 µW	Capacitor	Input capacitor power dissipation
5.	Cout IRMS	62.612 mA	Capacitor	Output capacitor RMS ripple current
6.	Cout Pd	14.086 µW	Capacitor	Output capacitor power dissipation
7.	IC Iq Pd	1.108 mW	IC	IC Iq Pd
8.	IC Pd	491.4 mW	IC	IC power dissipation
9.	IC Tj	50.196 degC	IC	IC junction temperature
10.	ICThetaJA	41.1 degC/W	IC	IC junction-to-ambient thermal resistance
11.	Iin Avg	42.387 mA	IC	Average input current

#	Name	Value	Category	Description
12.	Ipp percentage	43.379 %	Inductor	Inductor ripple current percentage (with respect to average inductor current)
13.	L Ipp	216.895 mA	Inductor	Peak-to-peak inductor ripple current
14.	L Pd	45.706 mW	Inductor	Inductor power dissipation
15.	Cin Pd	29.896 µW	Power	Input capacitor power dissipation
16.	Cout Pd	14.086 µW	Power	Output capacitor power dissipation
17.	IC Pd	491.4 mW	Power	IC power dissipation
18.	L Pd	45.706 mW	Power	Inductor power dissipation
19.	Total Pd	537.191 mW	Power	Total Power Dissipation
20.	Duty Cycle	7.335 %	System Information	Duty cycle
21.	Efficiency	75.439 %	System Information	Steady state efficiency
22.	FootPrint	247.0 mm^2	System Information	Total Foot Print Area of BOM components
23.	Frequency	345.355 kHz	System Information	Switching frequency
24.	Iout	500.0 mA	System Information	Iout operating point
25.	Mode	CCM	System Information	Conduction Mode
26.	Pout	1.65 W	System Information	Total output power
27.	Vin	51.6 V	System Information	Vin operating point
28.	Vin p-p	107.215 mV	System Information	Peak-to-peak input voltage
29.	Vout	3.3 V	System Information	Operational Output Voltage
30.	Vout Actual	3.315 V	System Information	Vout Actual calculated based on selected voltage divider resistors
31.	Vout Tolerance	2.297 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
32.	Vout p-p	4.474 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	500.0 m	Maximum Output Current
VinMax	51.6	Maximum input voltage
VinMin	48.0	Minimum input voltage
Vout	3.3	Output Voltage
base_pn	LM5163	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

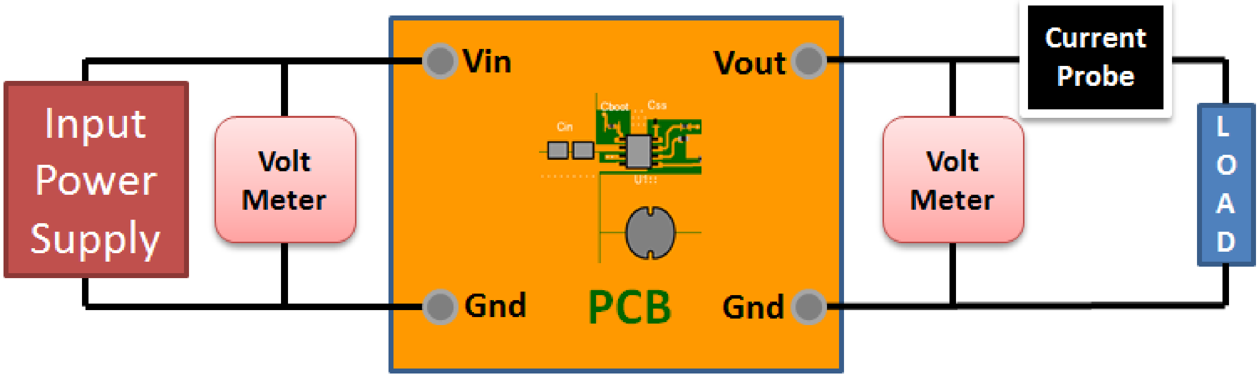
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 48.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : 8D46410E296FCE97B33F99C6313555A5[v1]

2. **LM5163** Product Folder : http://www.ti.com/product/lm5163 : contains the data sheet and other resources.