Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	Ground	17	D14	33	REQ2*	49	A11
2	Ground	18	D15	34	DONE*	50	A10
3	D0	19	UDS*	35	IRQ2*	51	A9
4	D1	20	LDS*	36	DRAM*	52	A8
5	D2	21	R/W*	37	A23	53	A7
6	D3	22	DTACK*	38	A22	54	A6
7	D4	23	CPUCLK	39	A21	55	A5
8	D5	24	RESET*	40	A20	56	A4
9	D6	25	IRQ4*	41	A19	57	A3
10	D7	26	NMI*	42	A18	58	A2
11	D8	27	AS*	43	A17	59	A1
12	D9	28	BG*	44	A16	60	OFFCARD*
13	D10	29	BR*	45	A15	61	+5V
14	D11	30	BGACK*	46	A14	62	+5V
15	D12	31	REQ1*	47	A13	63	+12V
16	D13	32	ACK1*	48	A12	64	-12V

Table 8 MM/1 Expansion Bus

NOTE - Pin 60 was NMI* in the Original Tech Manual, This is an ERROR

NOTE - Pin 64 was +12V in the Original Tech Manual, This is an ERROR

P1 & P2 are Buss Connectors on the Backplane "MiniBus" Board(s)

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^{*} Indicates an Active Low Condition