LM2940/LM2940C 1A Low Dropout Regulator

General Description

The LM2940/LM2940C positive voltage regulator features the ability to source 1A of output current with a dropout voltage of typically 0.5V and a maximum of 1V over the entire temperature range. Furthermore, a quiescent current reduction circuit has been included which reduces the ground current when the differential between the input voltage and the output voltage exceeds approximately 3V. The quiescent current with 1A of output current and an input-output differential of 5V is therefore only 30 mA. Higher quiescent currents only exist when the regulator is in the dropout mode ($V_{IN} - V_{OUT} \le 3V$).

Designed also for vehicular applications, the LM2940/ LM2940C and all regulated circuitry are protected from reverse battery installations or 2-battery jumps. During line transients, such as load dump when the input voltage can momentarily exceed the specified maximum operating voltage, the regulator will automatically shut down to protect both the internal circuits and the load. The LM2940/ LM2940C cannot be harmed by temporary mirror-image insertion. Familiar regulator features such as short circuit and thermal overload protection are also provided.

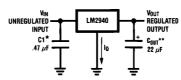
Features

- Dropout voltage typically 0.5V @I_O = 1A
- Output current in excess of 1A
- Output voltage trimmed before assembly

TL/H/8822-3

- Reverse battery protection
- Internal short circuit current limit
- Mirror image insertion protection
- P⁺ Product Enhancement tested

Typical Application



^{*}Required if regulator is located far from power supply filter

Ordering Information

Temperature	Output Voltage									
Range	5.0	8.0	9.0	10	12	15	Package			
0°C ≤ T _A ≤ 125°C	LM2940CT-5.0		LM2940CT-9.0		LM2940CT-12	LM2940CT-15	TO-220			
	LM2940CS-5.0		LM2940CS-9.0		LM2940CS-12	LM2940CS-15	TO-263			
-40°C ≤ T _A ≤ 125°C	LM2940T-5.0	LM2940T-8.0	LM2940T-9.0	LM2940T-10	LM2940T-12		TO-220			
	LM2940S-5.0	LM2940S-8.0	LM2940S-9.0	LM2940S-10	LM2940S-12		TO-263			
$-55^{\circ}\text{C} \le \text{T}_{\text{A}} \le 125^{\circ}\text{C}$	LM2940K-5.0/883	LM2940K-8.0/883			LM2940K-12/883	LM2940K-15/883	TO-3			

^{**}Cour must be at least 22 µF to maintain stability. May be increased without bound to maintain regulation during transients. Locate as close as possible to the regulator. This capacitor must be rated over the same operating temperature range as the regulator and the ESR is critical; see curve.

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. (Note 2)

 $\begin{array}{l} LM2940S,\,T\,\leq\,100\;ms\\ LM2940T,\,T\,\leq\,100\;ms\\ LM2940K/883,\,T\,\leq\,20\;ms \end{array}$ 60V 60V 40V LM2940CT, T \leq 1 ms 45V LM2940CS, T \leq 1 ms 45V Internally Limited Internal Power Dissipation (Note 3) Maximum Junction Temperature

Storage Temperature Range

150°C $-65^{\circ}C \leq T_{J} \leq \, +150^{\circ}C$

Temperature Range $\begin{array}{l} -55^{\circ}C \leq T_{A} \leq 125^{\circ}C \\ -40^{\circ}C \leq T_{A} \leq 125^{\circ}C \\ 0^{\circ}C \leq T_{A} \leq 125^{\circ}C \end{array}$ LM2940K/883 LM2940T, LM2940S LM2940CT, LM2940CS

300°C

260°C

260°C

 $2\,kV$

26V

Lead Temperature (Soldering, 10 seconds) TO-3 (K) Package

Operating Conditions (Note 1)

TO-220 (T) Package

TO-263 (S) Package

Input Voltage

ESD Susceptibility (Note 4)

Electrical Characteristics $V_{IN} = V_O + 5V$, $I_O = 1A$, $C_O = 22~\mu F$, unless otherwise specified. **Boldface** limits apply over the entire operating temperature range of the indicated device. All other specifications apply for $T_A = T_J = 25^{\circ}\text{C}$

Output Voltage (V _O)		5V							
Parameter	Conditions	Тур	LM2940 Limit (Note 5)	LM2940/883 Limit (Note 6)	Тур	LM2940 Limit (Note 5)	LM2940/883 Limit (Note 6)	Units	
			$6.25 \text{V} \leq \text{V}_{\text{IN}} \leq 26 \text{V}$			9.4V ≤ V _{IN} ≤ 26V			
Output Voltage	$5 \text{ mA} \leq I_{O} \leq 1 \text{A}$	5.00	4.85/ 4.75 5.15/ 5.25	4.85/ 4.75 5.15/ 5.25	8.00	7.76/ 7.60 8.24/ 8.40	7.76/ 7.60 8.24/ 8.40	V _{MIN} V _{MAX}	
Line Regulation	$\label{eq:VO} \begin{array}{l} V_O + 2V \leq V_{IN} \leq 26V, \\ I_O = 5 \; mA \end{array}$	20	50	40/ 50	20	80	50/ 80	mV _{MAX}	
Load Regulation		35 35	50/ 80 50	50/ 100	55 55	80/ 130 80	80/ 130	mV _{MAX}	
Output Impedance	100 mADC and 20 mArms, f _O = 120 Hz	35		1000/1000	55		1000/ 1000	mΩ	
Quiescent Current	$V_{O} + 2V \le V_{IN} \le 26V$, $I_{O} = 5 \text{ mA}$ LM2940, LM2940/883 LM2940C	10 10	15/ 20 15	15/ 20	10	15/ 20	15/ 20	mA _{MAX}	
	$V_{IN} = V_O + 5V,$ $I_O = 1A$	30	45/ 60	50/ 60	30	45/ 60	50/60	mA _{MAX}	
Output Noise Voltage	$10 \text{ Hz} - 100 \text{ kHz},$ $I_{O} = 5 \text{ mA}$	150		700/ 700	240		1000/ 1000	μV_{rms}	
Ripple Rejection	$f_{O} = 120 \text{ Hz}, 1 \text{ V}_{rms},$ $I_{O} = 100 \text{ mA}$ $LM2940$ $LM2940C$	72 72	60/ 54 60		66 66	54/ 48 54		dB _{MIN}	
	$f_O = 1 \text{ kHz}, 1 \text{ V}_{rms},$ $I_O = 5 \text{ mA}$			60/ 50			54/ 48	dB _{MIN}	
Long Term Stability		20			32			mV/ 1000 Hr	
Dropout Voltage	I _O = 1A	0.5	0.8/1.0	0.7/ 1.0	0.5	0.8/1.0	0.7/1.0	V _{MAX}	
	I _O = 100 mA	110	150/200	150/200	110	150/200	150/200	mV _{MAX}	

Electrical Characteristics $V_{IN}=V_O+5V,\ I_O=1A,\ C_O=22\ \mu\text{F},\ unless otherwise specified.$ **Boldface limits apply over the entire operating temperature range of the indicated device.** $All other specifications apply for <math>T_A=T_J=25^\circ\text{C}$ (Continued)

Output Voltage (V _O)		5 V						
Parameter	Conditions	Тур	LM2940 Limit (Note 5)	LM2940/883 Limit (Note 6)	Тур	LM2940 Limit (Note 5)	LM2940/883 Limit (Note 6)	Units
Short Circuit Current	(Note 7)	1.9	1.6	1.5/1.3	1.9	1.6	1.6/1.3	A _{MIN}
Maximum Line Transient	$\begin{aligned} R_{O} &= 100\Omega \\ LM2940, T &\leq 100 \text{ ms} \\ LM2940/883, T &\leq 20 \text{ ms} \\ LM2940C, T &\leq 1 \text{ ms} \end{aligned}$	75 55	60/ 60 45	40/ 40	75 55	60/ 60 45	40/ 40	V _{MIN}
Reverse Polarity DC Input Voltage	$R_{O} = 100\Omega$ LM2940, LM2940/883 LM2940C	-30 -30	-15/- 15 -15	-15/- 15	-30 -30	-15/- 15 -15	-15/- 15	V _{MIN}
Reverse Polarity Transient Input Voltage	$\begin{aligned} R_O &= 100\Omega \\ LM2940, T &\leq 100 \text{ ms} \\ LM2940/883, T &\leq 20 \text{ ms} \\ LM2940C, T &\leq 1 \text{ ms} \end{aligned}$	-75 -55	-50/- 50 -45/- 45	-45/- 45	-75	-50/- 50	-45/- 45	V _{MIN}

Electrical Characteristics $V_{IN}=V_O+5V,\ I_O=1A,\ C_O=22\ \mu F,$ unless otherwise specified. **Boldface limits apply over the entire operating temperature range of the indicated device.** All other specifications apply for $T_A=T_J=25^{\circ}C$ (Continued)

Output Voltage (V _O)			9V				
Parameter	Conditions	Тур	LM2940 Limit (Note 5)	Тур	LM2940 Limit (Note 5)	Units	
		10.5V ≤ V _{IN} ≤ 26V		11.5V			
Output Voltage	5 mA ≤ I _O ≤1A	9.00	8.73/ 8.55 9.27/ 9.45	10.00	9.70/ 9.50 10.30/ 10.50	V _{MIN} V _{MAX}	
Line Regulation	$\begin{array}{c} V_O + 2V \leq V_{IN} \leq 26V, \\ I_O = 5 mA \end{array} \label{eq:volume}$	20	90	20	100	mV _{MA}	
Load Regulation	$50 \text{ mA} \leq I_{O} \leq 1\text{A}$ LM2940 LM2940C	60 60	90/ 150 90	65	100/ 165	mV _{MA>}	
Output Impedance	100 mADC and 20 mArms, f _O = 120 Hz	60		65		mΩ	
Quiescent Current	$V_{O} + 2V \le V_{IN} < 26V,$ $I_{O} = 5 \text{ mA}$ $LM2940$ $LM2940C$	10 10	15/ 20 15	10	15/ 20	mA _{MA})	
	$V_{IN} = V_O + 5V, I_O = 1A$	30	45/60	30	45/ 60	mA _{MA}	
Output Noise Voltage	10 Hz — 100 kHz, I _O = 5 mA	270		300		μV _{rms}	
Ripple Rejection	$f_{O} = 120$ Hz, 1 V_{rms} , $I_{O} = 100$ mA LM2940 LM2940C	64 64	52/ 46 52	63	51/ 45	dB _{MIN}	
Long Term Stability		34		36		mV/ 1000 H	
Dropout Voltage	I _O = 1A	0.5	0.8/1.0	0.5	0.8/1.0	V _{MAX}	
	I _O = 100 mA	110	150/200	110	150/200	mV _{MA}	
Short Circuit Current	(Note 7)	1.9	1.6	1.9	1.6	A _{MIN}	
Maximum Line Transient	$R_O = 100 \Omega$ $T \le 100 \text{ ms}$ LM2940 LM2940C	75 55	60/ 60 45	75	60/ 60	V _{MIN}	
Reverse Polarity DC Input Voltage	R _O = 100Ω LM2940 LM2940C	-30 -30	-15/- 15 -15	-30	-15/- 15	V _{MIN}	
Reverse Polarity Transient Input Voltage	$\begin{array}{l} R_O = 100\Omega \\ T \leq 100 \text{ ms} \\ LM2940 \\ LM2940C \end{array}$	-75 -55	-50/- 50 -45/- 45	-75	-50/- 50	V _{MIN}	

Electrical Characteristics $V_{IN}=V_O+5V,\ I_O=1A,\ C_O=22\ \mu\text{F},\ unless otherwise specified.$ **Boldface limits apply over the entire operating temperature range of the indicated device.** $All other specifications apply for <math>T_A=T_J=25^\circ\text{C}$ (Continued)

Output Voltage (V _O)		12V						
Parameter	Conditions	Тур	LM2940 Limit (Note 5)	LM2940/833 Limit (Note 6)	Тур	LM2940 Limit (Note 5)	LM2940/833 Limit (Note 6)	Units
			13.6V ≤ V _{IN}	≤ 26V				
Output Voltage	$5 \text{ mA} \leq I_{O} \leq 1 \text{A}$	12.00	11.64/ 11.40 12.36/ 12.60	11.64/ 11.40 12.36/ 12.60	15.00		14.55/ 14.25 15.45/ 15.75	V _{MIN} V _{MAX}
Line Regulation	$\begin{aligned} &V_O+2V\leq V_{IN}\leq 26V,\\ &I_O=5\text{mA} \end{aligned}$	20	120	75/ 120	20	150	95/ 150	mV _{MAX}
Load Regulation	$\begin{array}{l} \mbox{50 mA} \leq \mbox{I}_{\mbox{O}} \leq \mbox{1A} \\ \mbox{LM2940, LM2940/883} \\ \mbox{LM2940C} \end{array}$	55 55	120/ 200 120	120/ 190	70	150	150/ 240	mV _{MAX}
Output Impedance	100 mADC and 20 mArms, f _O = 120 Hz	80		1000/1000	100		1000/1000	mΩ
Quiescent Current	$V_{O} + 2V \le V_{IN} \le 26V,$ $I_{O} = 5 \text{ mA}$ LM2940, LM2940/883 LM2940C	10 10	15/ 20 15	15/ 20	10	15	15/ 20	mA _{MAX}
	$V_{IN} = V_O + 5V, I_O = 1A$	30	45/ 60	50/60	30	45/ 60	50/ 60	mA _{MAX}
Output Noise Voltage	$10 \text{ Hz} - 100 \text{ kHz},$ $I_{O} = 5 \text{ mA}$	360		1000/1000	450		1000/1000	μV _{rms}
Ripple Rejection	$f_{O} = 120 \text{ Hz}, 1 \text{ V}_{rms},$ $I_{O} = 100 \text{ mA}$ $LM2940$ $LM2940C$	66 66	54/ 48 54		64	52		dB _{MIN}
	$f_O = 1 \text{ kHz}, 1 \text{ V}_{rms},$ $I_O = 5 \text{ mA}$			52/ 46			48/ 42	dB _{MIN}
Long Term Stability		48			60			mV/ 1000 Hr
Dropout Voltage	I _O = 1A	0.5	0.8/1.0	0.7/ 1.0	0.5	0.8/1.0	0.7/ 1.0	V _{MAX}
	I _O = 100 mA	110	150/200	150/200	110	150/200	150/200	mV_{MAX}
Short Circuit Current	(Note 7)	1.9	1.6	1.6/ 1.3	1.9	1.6	1.6/ 1.3	A _{MIN}
Maximum Line Transient	$R_{O} = 100\Omega$ LM2940, T \leq 100 ms LM2940/883, T \leq 20 ms LM2940C, T \leq 1 ms	75 55	60/ 60	40/ 40	55	45	40/ 40	V _{MIN}
Reverse Polarity DC Input Voltage	R _O = 100Ω LM2940, LM2940/883 LM2940C	-30 -30	-15/- 15 -15	-15/- 15	-30	-15	-15/- 15	V _{MIN}
Reverse Polarity Transient Input Voltage	$R_{O} = 100\Omega$ LM2940, T \leq 100 ms LM2940/883, T \leq 20 ms LM2940C, T \leq 1 ms	-75 -55	-50/- 50 -45/- 45	-45/- 45	-55	-45/- 45	-45/- 45	V _{MIN}

Note 1: Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Conditions are conditions under which the device functions but the specifications might not be guaranteed. For guaranteed specifications and test conditions see the Electrical Characteristics.

Note 2: Military specifications complied with RETS/SMD at the time of printing. For current specifications refer to RETS LM2940K-5.0, LM2940K-8.0, LM2940K-12, and LM2940K-15. SMD numbers are 5962-8958701YA(5V), 5962-9083301YA(8V), 5962-9088401YA(12V), and 5962-9088501YA(15V).

Note 3: The maximum power dissipation is a function of the maximum junction temperature, $T_J = 150^{\circ}\text{C}$, the junction-to-ambient thermal resistance, θ_{JA} , and the ambient temperature, T_A . The maximum allowable power dissipation at any ambient temperature is $P_{DMX} = (150 - T_A)/\theta_{JA}$. If this dissipation is exceeded, the die temperature will rise above 150°C and the LM2940 will go into thermal shutdown. For the LM2940T and LM2940T, the junction-to-ambient thermal resistance (θ_{JA}) is 53°C/W . When using a heatsink, θ_{JA} is the sum of the 3°C/W junction-to-case thermal resistance (θ_{JC}) of the LM2940T or LM2940T and the case-to-ambient thermal resistance of the heatsink. If the TO-263 package is used, the thermal resistance can be used by increasing the P.C. board copper area thermally connected to the package. Using 0.5 square inches of copper area, θ_{JA} is 30°C/W . For the LM2940K, θ_{JA} is 30°C/W , with 1 square inches of copper area, θ_{JA} is 30°C/W . For the LM2940K, θ_{JA} is 30°C/W .

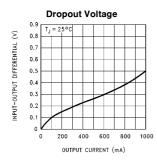
Note 4: ESD rating is based on the human body model, 100 pF discharged through 1.5 k Ω .

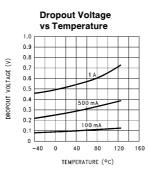
Note 5: All limits are guaranteed at $T_A = T_J = 25^{\circ}$ C only (standard typeface) or over the entire operating temperature range of the indicated device (**boldface type**). All limits at $T_A = T_J = 25^{\circ}$ C are 100% production tested. All limits at temperature extremes are guaranteed via correlation using standard Statistical Quality Control methods

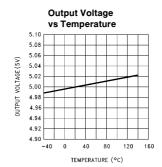
Note 6: All limits are guaranteed at $T_A = T_J = 25^{\circ}C$ only (standard typeface) or over the entire operating temperature range of the indicated device (**boldface type**). All limits are 100% production tested and are used to calculate Outgoing Quality Levels.

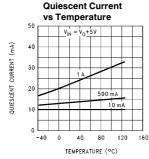
Note 7: Output current will decrease with increasing temperature but will not drop below 1A at the maximum specified temperature.

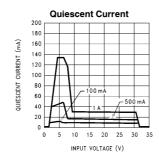
Typical Performance Characteristics

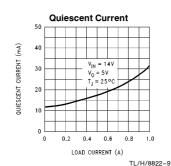


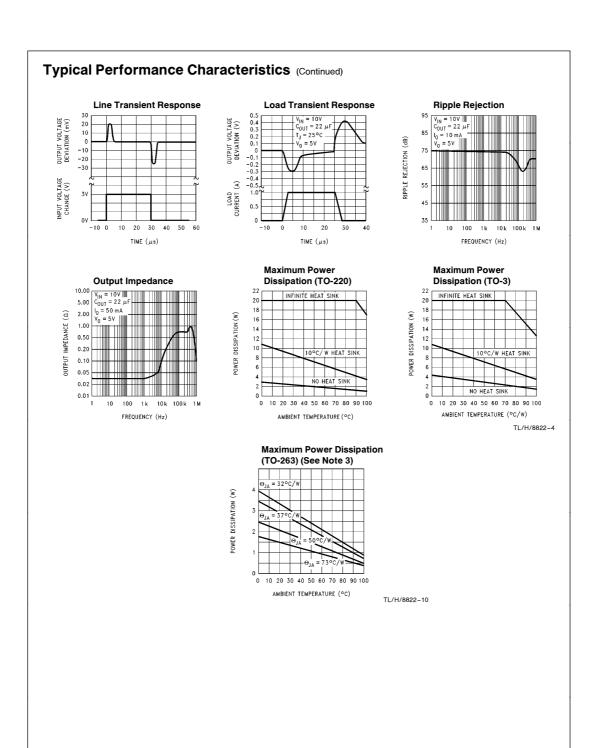




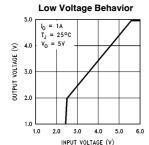


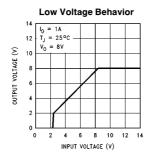


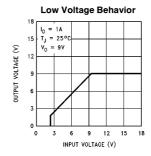


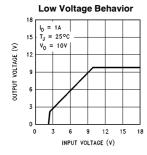


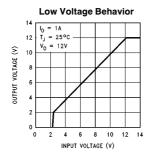
Typical Performance Characteristics (Continued)

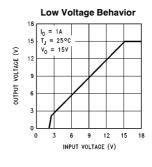


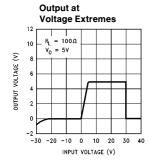


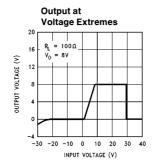


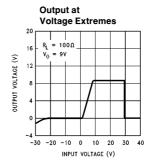


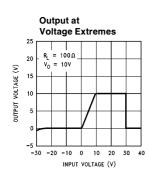


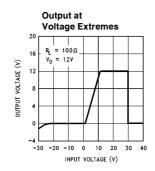


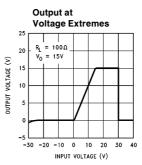




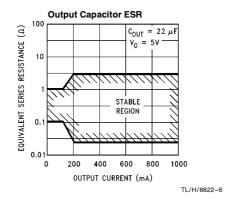


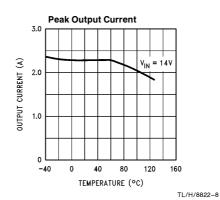




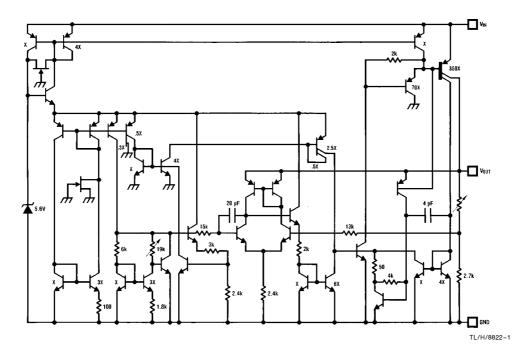


Typical Performance Characteristics (Continued)



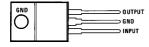


Equivalent Schematic Diagram



Connection Diagrams

(TO-220) Plastic Package

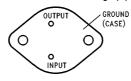


TL/H/8822-2

Front View

Order Number LM2940CT-5.0, LM2940CT-9.0, LM2940CT-12, LM2940CT-15, LM2940T-5.0, LM2940T-8.0, LM2940T-9.0, LM2940T-10 or LM2940T-12 See NS Package Number TO3B

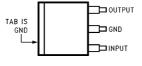
TO-3 Metal Can Package (K)



TL/H/8822-7

Bottom View Order Number LM2940K-5.0/883, LM2940K-8.0/883, LM2940K-12/883, LM2940K-15/883 See NS Package Number K02A

(TO-263) Surface-Mount Package



Top View

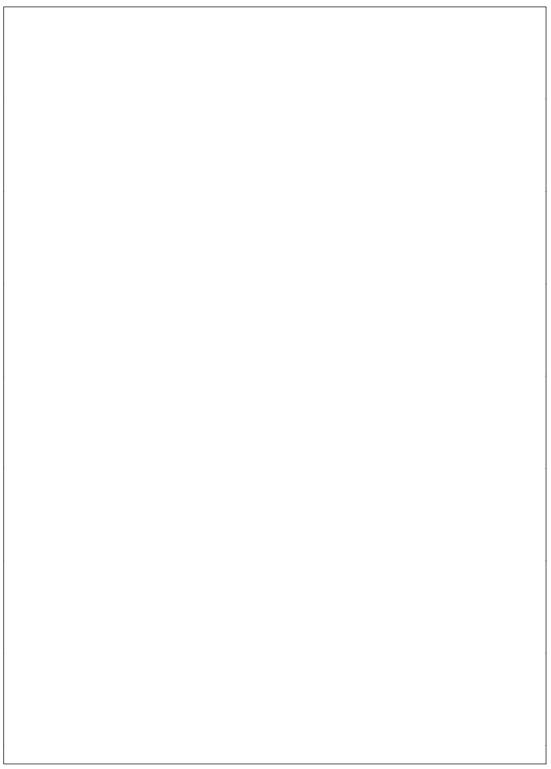
TL/H/8822-11

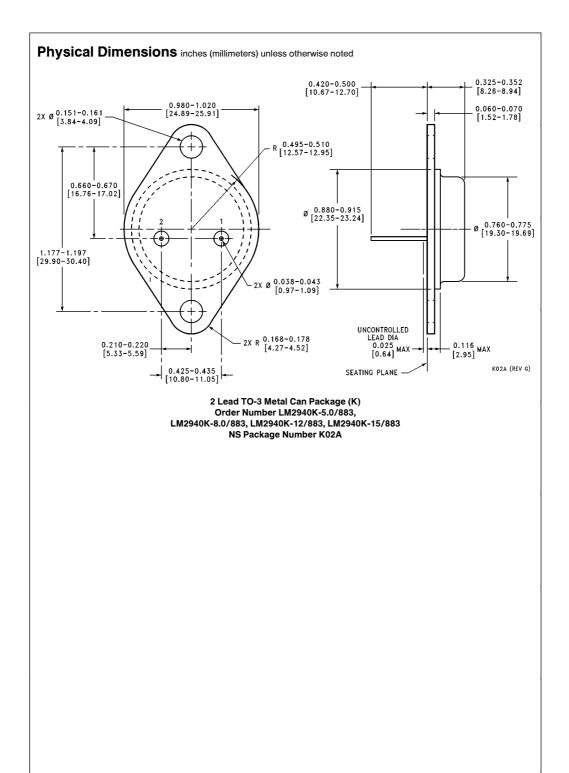


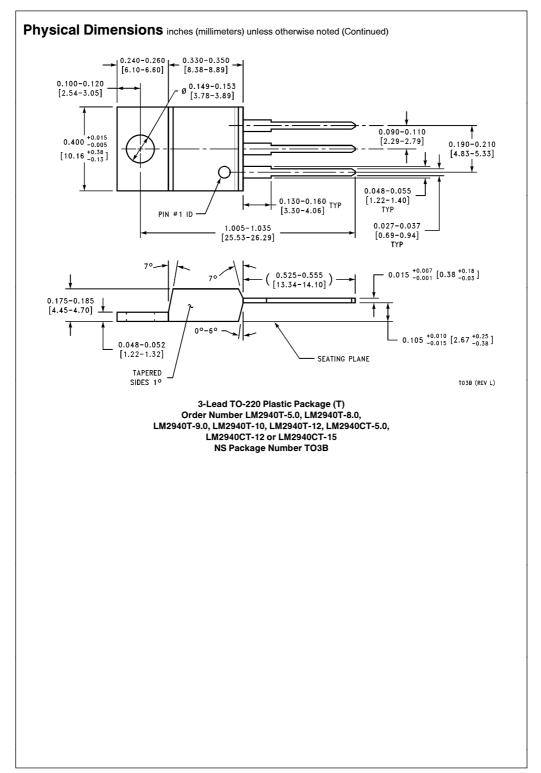
TL/H/8822-12

Order Number LM2940CS-5.0, LM2940CS-9.0, LM2940CS-12, LM2940CS-15, LM2940S-5.0, LM2940S-8.0, LM2940S-9.0, LM2940S-10 or LM2940S-12 See NS Package Number TS3B

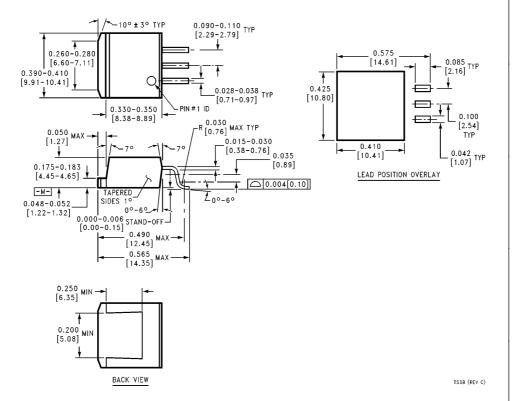
Side View







Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



3-Lead TO-263 Surface Mount Package Order Number LM2940S-5.0, LM2940S-8.0, LM2940S-9.0, LM2940S-10, LM2940S-12, LM2940CS-5.0, LM2940CS-12 or LM2940CS-15 NS Package Number TS3B

LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



National Semiconductor Corporation 1111 West Bardin Road Arlington, TX 76017 Tel: 1(800) 272-9959 Fax: 1(800) 737-7018

http://www.national.com

National Semiconductor Europe

Europe Fax: +49 (0) 180-530 85 86 Email: europe.support@nsc.cor Deutsch Tel: +49 (0) 180-530 85 85 English Tel: +49 (0) 180-532 78 32 Français Tel: +49 (0) 180-532 32 58 Italiano Tel: +49 (0) 180-534 16 80

National Semiconductor Hong Kong Ltd. 13th Floor, Straight Block, Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon Hong Kong Tel: (852) 2737-1600 Fax: (852) 2736-9960

National Semiconductor Japan Ltd. Tel: 81-043-299-2308 Fax: 81-043-299-2408